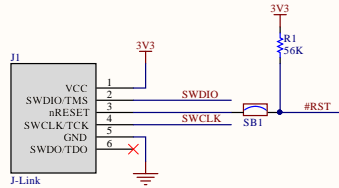
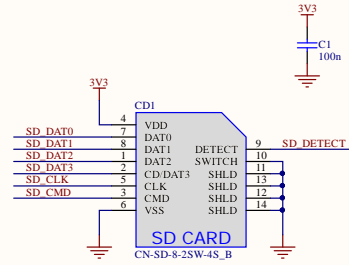


Top	
Variant name [No Variations]	
Number: Strain Logger	Revision: 0.11
Date: 7/19/2018	Sheet 1 of 5
Drawn by Jamie Stepanian	
Verified by:	

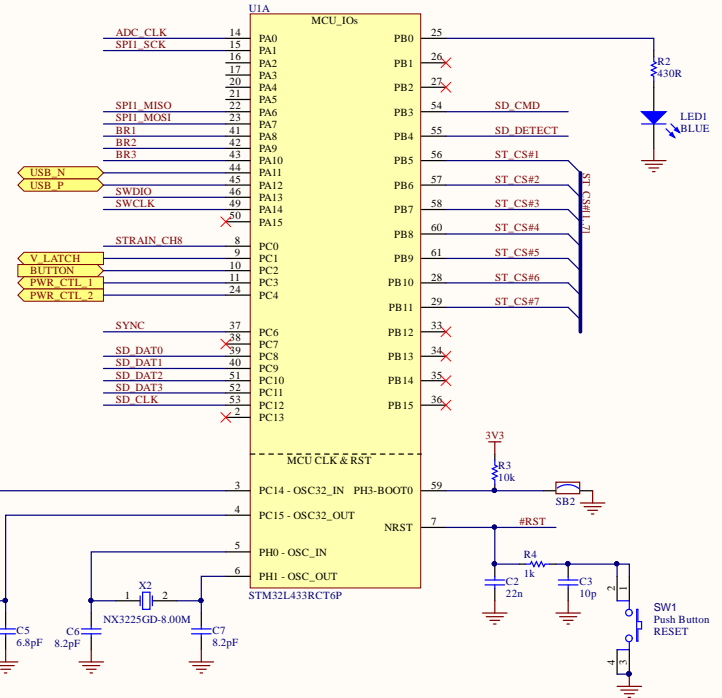
J-LINK TAG-CONNECT DEBUG



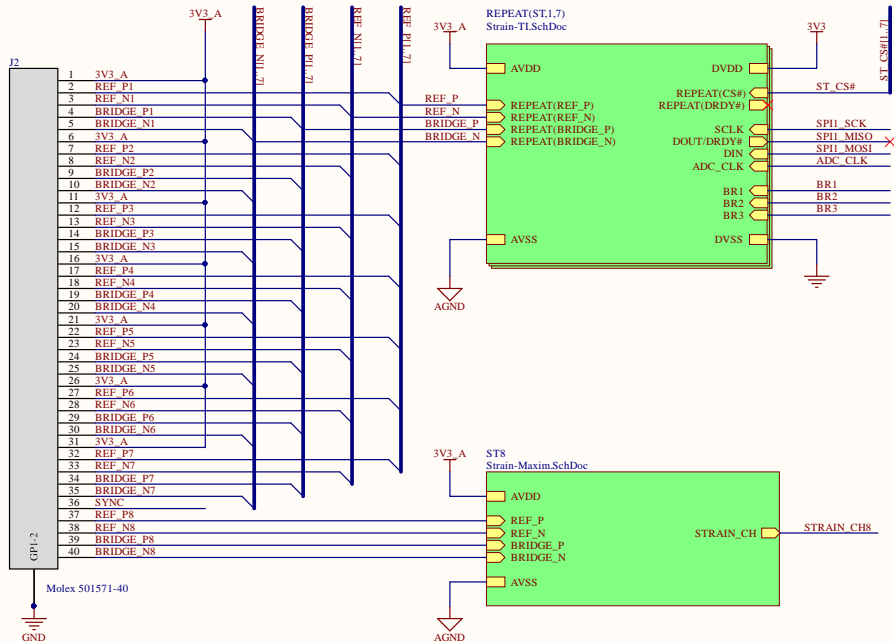
SD CARD



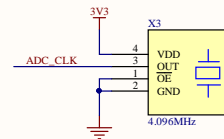
MCU



STRAIN GAUGES AND SYNC LINE



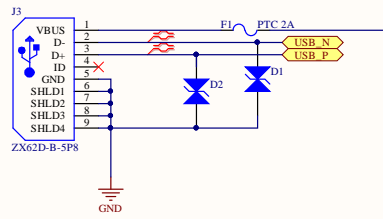
ADC SAMPLE CLOCK



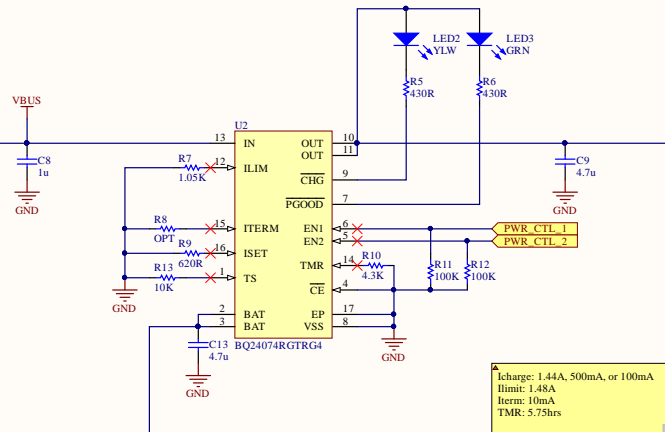
Controller

Variant name			[No Variations]	
Number:	Strain Logger		Revision:	0.11
Date:	7/19/2018		Sheet	2 of 5

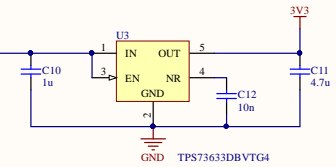
MICRO USB



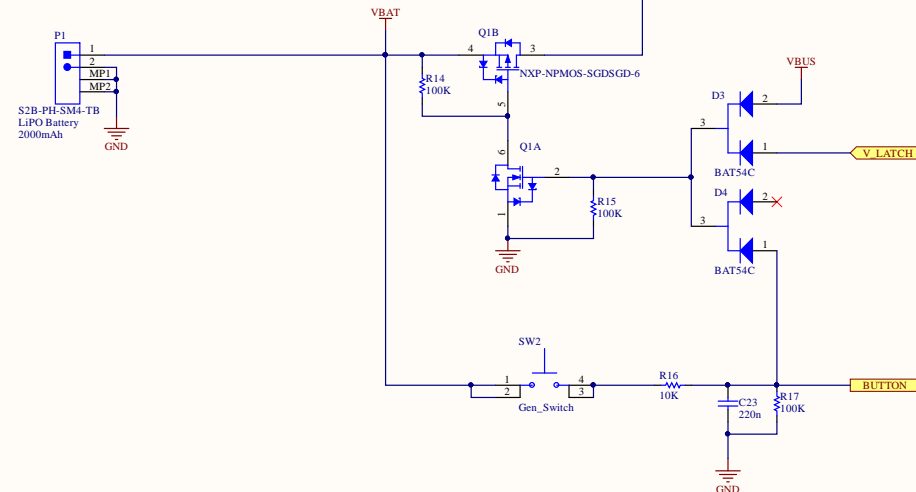
POWER MANAGER IC



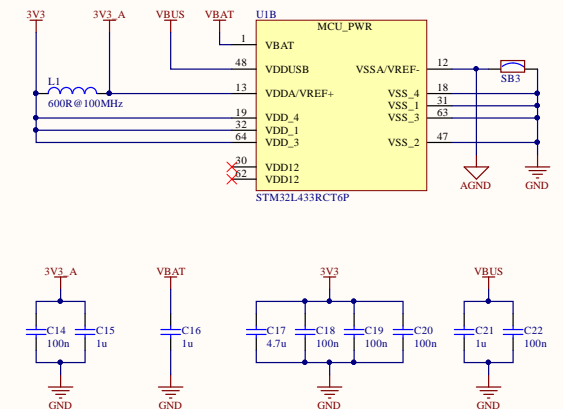
3V3 LINEAR REGULATOR



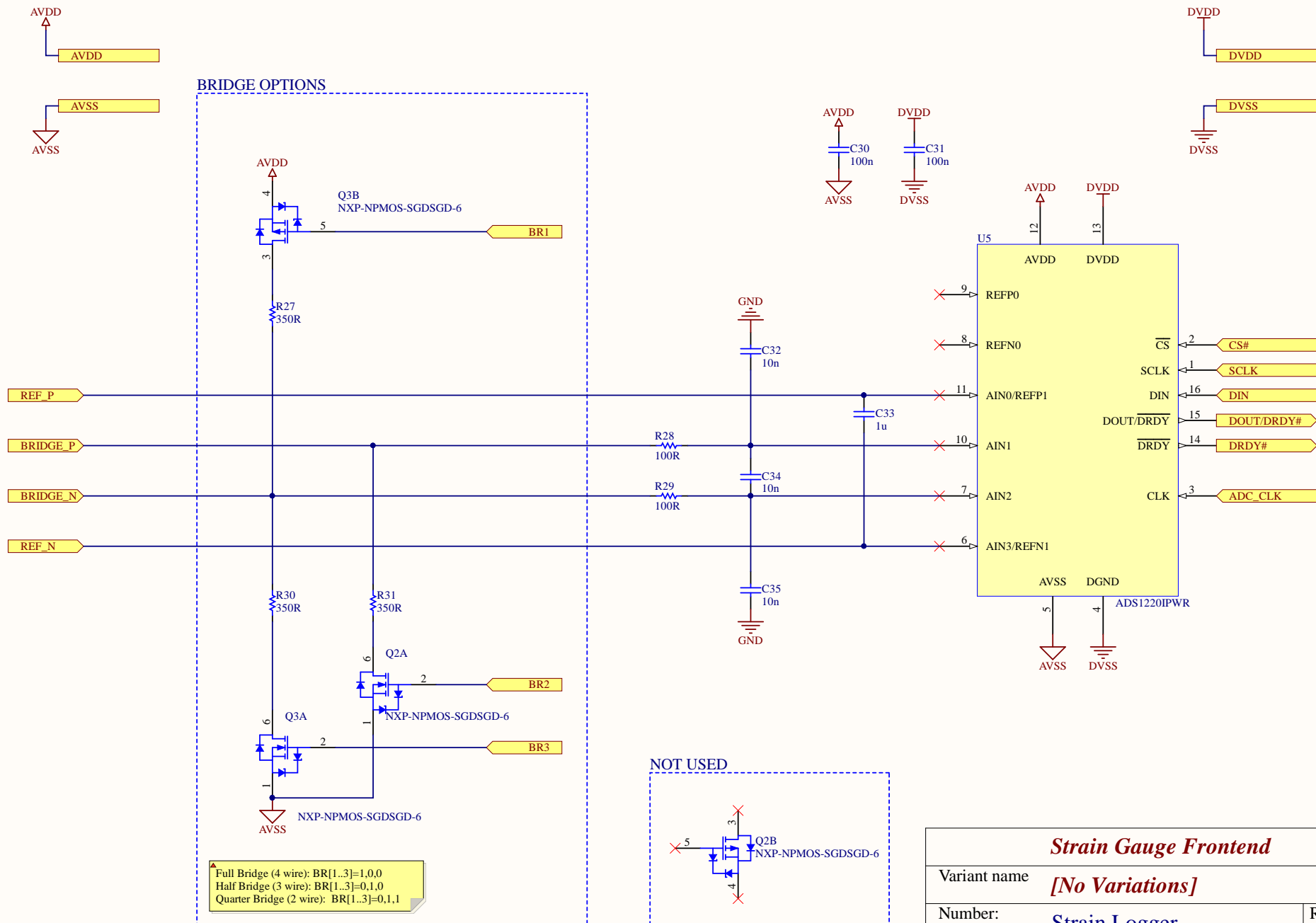
BATTERY INPUT AND PUSH BUTTON LATCH



MCU POWER

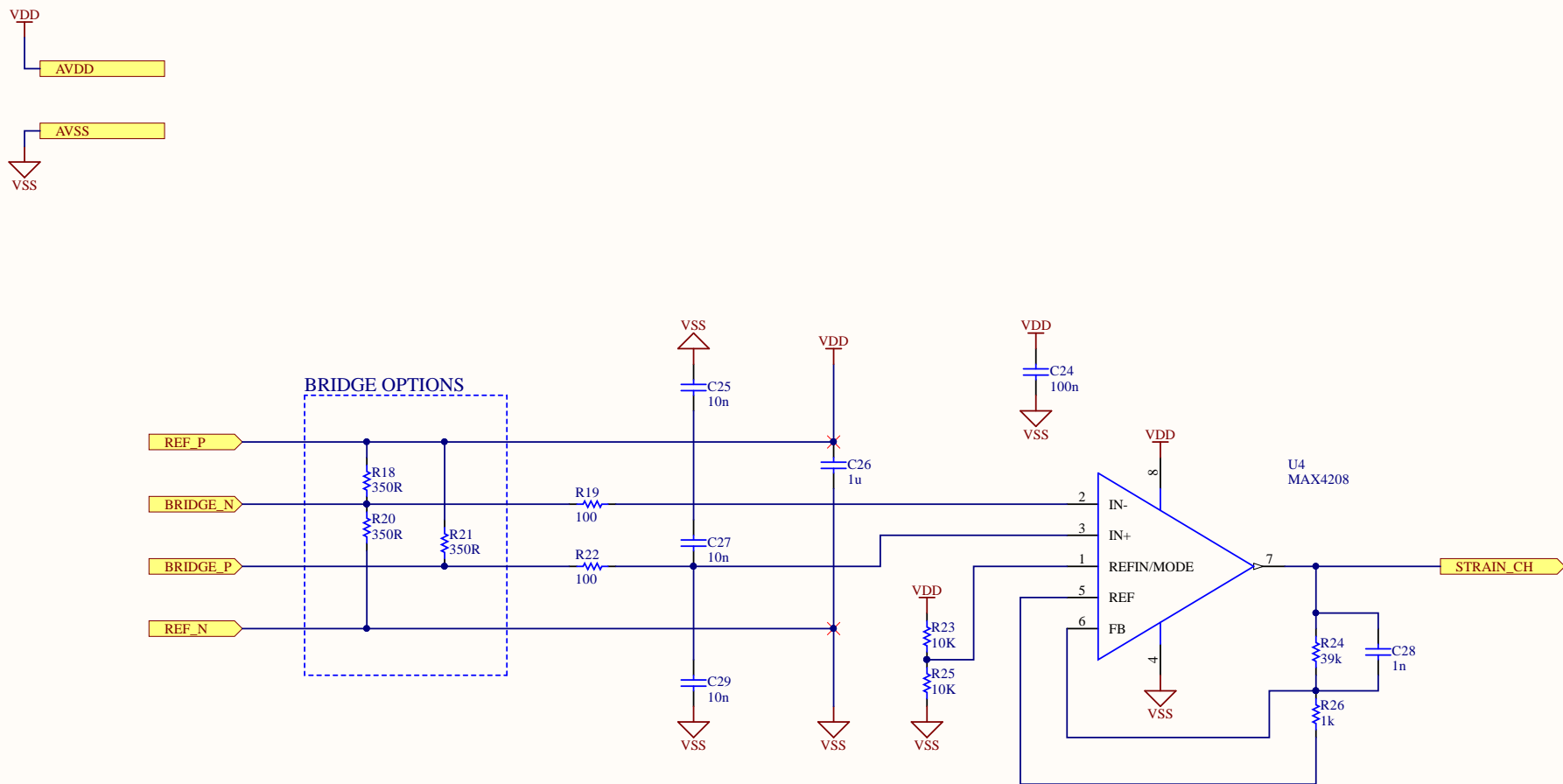
**Power**

Variant name [No Variations]		
Number:	Strain Logger	Revision: 0.11
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Strain Gauge Frontend

Variant name	[No Variations]	
Number:	Strain Logger	Revision: 0.11
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▲ Full Bridge (4 wire): Do not populate R15, R17, and R18.
 Half Bridge (3 wire): Populate R15 and R17 with 350R 0.01%.
 Quarter Bridge (2 wire): Populate R15, R17, and R18 with 350R 0.01%.

▲ AMP SETTINGS
 Vref: 1.65V
 Gain: 40

Strain Gauge Frontend		
Variant name	[No Variations]	
Number:	Strain Logger	Revision: 0.11
Date:	7/19/2018	Sheet 5 of 5