

Model Checking

An automated technique used for verifying finite-state systems against specified properties. It systematically explores all possible states of a system to verify if certain properties hold true.

State Space:

The set of all possible states that a system can be in. In a program, the state can be described by the set of all variable addresses and values.

Modifying a variable's value results in a new state. Model checking over a program may require an additional layer of abstraction (e.g., predicate abstraction) to reduce the number of states to a manageable size.

Transitions:

These are rules that describe how the system moves between states.

Each state can transition to another based on specific conditions or actions.

Properties:

These are specifications that define the desired behavior of the system.

Model checking verifies whether the system satisfies these properties.

Temporal Logic:

Temporal logic is the language used to express the properties to be verified.

Examples include Linear Temporal Logic (LTL) and Computation Tree Logic (CTL), which help express the system's behavior over time.

Simple Example: Traffic Light Controller

Imagine a traffic light controller with three states:

States: {RED, YELLOW, GREEN}

Initial State: RED

Transitions: RED → GREEN, GREEN → YELLOW, YELLOW → RED

In this system, the traffic light switches from red to green, from green to yellow, and from yellow back to red, repeatedly.

Model checking would explore all possible states of this traffic light controller to verify whether it behaves according to its specifications (e.g., ensuring that the light always transitions in this exact sequence, without errors or violations of the system's rules).

Model checking is a powerful technique for verifying system properties by exhaustively exploring all states and transitions within the system. It uses temporal logic to express and check properties, ensuring that the system behaves as expected under all possible conditions.

Example Program:

```
precondition: numTickets > 0
reserved = false;
while (true) {
  getQuery();
  if (numTickets > 0 && !reserved)
    reserved = true;
  if (numTickets > 0 && reserved) {
    reserved = false;
    numTickets--;
  }
}
```

What is interesting about this?

Are tickets available? **a**

Is a ticket reserved? **r**

```
graph TD
    S((nT=2, r=false)) -- a --> T1((nT=1, r=false))
    S -- r --> T2((nT=2, r=true))
    T1 -- a --> T3((nT=0, r=false))
    T1 -- r --> T2
    T2 -- a --> T3
    T2 -- r --> T2
    T3 -- a --> S
    T3 -- r --> T3
```

Abstracted Program: fewer states

```
precondition: available == true
reserved = false;
while (true) {
  getQuery();
  if (available && !reserved)
    reserved = true;
  if (available && reserved) {
    reserved = false;
    available = ?;
  }
}
```

```
graph TD
    Start(( )) --> a_lr((a lr))
    a_lr -- a --> a_r((a r))
    a_lr -- r --> a_lr
    a_r -- a --> a_lr
    a_r -- r --> not_a_lr((!a lr))
    not_a_lr -- a --> a_r
    not_a_lr -- r --> not_a_lr
```

State Transition Graph or Kripke Model

State: valuations to all variables
concrete state: (numTickets=5, reserved=false)
abstract state: (a=true, r=false)

Initial states: subset of states

Arcs: transitions between states

Atomic Propositions:
a: numTickets > 0
r: reserved = true

```
graph TD
    Start(( )) --> a_lr((a lr))
    a_lr -- a --> a_r((a r))
    a_lr -- r --> a_lr
    a_r -- a --> a_lr
    a_r -- r --> not_a_lr((!a lr))
    not_a_lr -- a --> a_r
    not_a_lr -- r --> not_a_lr
```

State Transition Graph or Kripke Model

Model of Computation

```
graph TD
    a((a)) --> b((b))
    b --> c((c))
    c --> a
    c --> c
```

State Transition Graph

Computation Traces

Unwind State Graph to obtain traces. A *trace* is an infinite sequence of states. The *semantics* of a FSM is a set of traces.

```
graph TD
    a((a)) --> b((b))
    b --> c((c))
    c --> a
    c --> c
```

State Transition Graph

Infinite Computation Tree

Represent all traces with an infinite computation tree

The Logic LTL

Linear Time Logic (LTL) [Pnueli 77]: logic of temporal sequences.

- α : α holds in the current state
- $X\alpha$: α holds in the next state
- $F\gamma$: γ holds eventually
- $G\lambda$: λ holds from now on
- $(\alpha \cup \beta)$: α holds until β holds

```
graph LR
    subgraph Alpha
        direction LR
        A1(( )) --> A2(( ))
        A1 -- alpha --> A1
    end
    subgraph XAlpha
        direction LR
        XA1(( )) --> XA2(( ))
        XA2 -- alpha --> XA2
    end
    subgraph FGamma
        direction LR
        FG1(( )) --> FG2(( ))
        FG2 -- gamma --> FG2
    end
    subgraph GLambda
        direction LR
        GL1(( )) --> GL2(( ))
        GL2 -- lambda --> GL2
    end
    subgraph AlphaUnionBeta
        direction LR
        AU1(( )) --> AU2(( ))
        AU1 -- alpha --> AU1
        AU2 -- beta --> AU2
    end
```

Typical LTL Formulas

- $G (Req \Rightarrow F Ack)$: whenever *Request* occurs, it will be eventually *Acknowledged*.
- $G (DeviceEnabled)$: *DeviceEnabled* always holds on every computation path.
- $G (F Restart)$: Fairness: from any state one will eventually get to a *Restart* state. I.e. *Restart* states occur infinitely often.
- $G (Reset \Rightarrow F Restart)$: whenever the reset button is pressed one will eventually get to the *Restart* state.

LTL Conventions

- G is sometimes written \square
- F is sometimes written \diamond

Notation

- A path π in M is an infinite sequence of states s_0, s_1, \dots such that for every $i \geq 0, (s_i, s_{i+1}) \in R$
- π^i denotes the suffix of π starting at s_i
- $M, \pi \models f$ means that f holds along path π in the Kripke structure M

Semantics of LTL Formulas

	$M, \pi \models p$	\Leftrightarrow	$\pi = s \dots \wedge p \in L(s)$
	$M, \pi \models \neg g$	\Leftrightarrow	$M, \pi \not\models g$
	$M, \pi \models g_1 \wedge g_2$	\Leftrightarrow	$M, \pi \models g_1 \wedge M, \pi \models g_2$
	$M, \pi \models g_1 \vee g_2$	\Leftrightarrow	$M, \pi \models g_1 \vee M, \pi \models g_2$
	$M, \pi \models X g$	\Leftrightarrow	$M, \pi^1 \models g$
	$M, \pi \models F g$	\Leftrightarrow	$\exists k \geq 0 \mid M, \pi^k \models g$
	$M, \pi \models G g$	\Leftrightarrow	$\forall k \geq 0 \mid M, \pi^k \models g$
	$M, \pi \models g_1 U g_2$	\Leftrightarrow	$\exists k \geq 0 \mid M, \pi^k \models g_2$ $\wedge \forall 0 \leq j < k \ M, \pi^j \models g_1$

g2 must eventually hold
semantics of "until" in English are potentially unclear—that's why we have a formal definition

Practice Writing Properties

- If the door is locked, it will not open until someone unlocks it
 - assume atomic predicates locked, unlocked, open
 - $G (\text{locked} \Rightarrow (\neg \text{open} U \text{unlocked}))$
- If you press ctrl-C, you will get a command line prompt
 - $G (\text{ctrlC} \Rightarrow F \text{prompt})$
- The saw will not run unless the safety guard is engaged
 - $G (\neg \text{safety} \Rightarrow \neg \text{running})$

LTL Model Checking

- f (primitive formula)
 - Just check the properties of the current state
- $X f$
 - Verify f holds in all successors of the current state
- $G f$
 - Find all reachable states from the current state, and ensure f holds in all of them
 - use depth-first or breadth-first search
- $f U g$
 - Do a depth-first search from the current state. Stop when you get to a g or you loop back on an already visited state. Signal an error if you hit a state where f is false before you stop.
- $F f$
 - Harder. Intuition: look for a path from the current state that loops back on itself, such that f is false on every state in the path. If no such path is found, the formula is true.
 - Reality: use Büchi automata

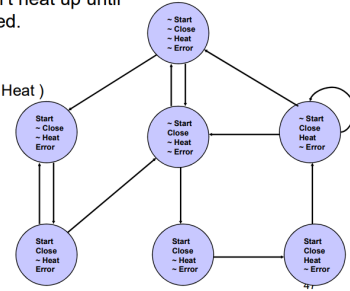
LTL Model Checking Example

- The oven doesn't heat up until the door is closed.

$(\neg \text{Heat}) U \text{Close}$

$(\neg \text{Heat}) W \text{Close}$

$G (\text{not Closed} \Rightarrow \text{not Heat})$



Efficient Algorithms for LTL Model Checking

- Use Büchi automata
 - Beyond the scope of this course
- Canonical reference on Model Checking:
 - Edmund Clarke, Orna Grumberg, and Doron A. Peled. *Model Checking*. MIT Press, 1999.

SPIN: The Promela Language

- PROcess MEta Language
- Asynchronous composition of independent processes
- Communication using channels and global variables
- Non-deterministic choices and interleavings

Mutual Exclusion in SPIN

