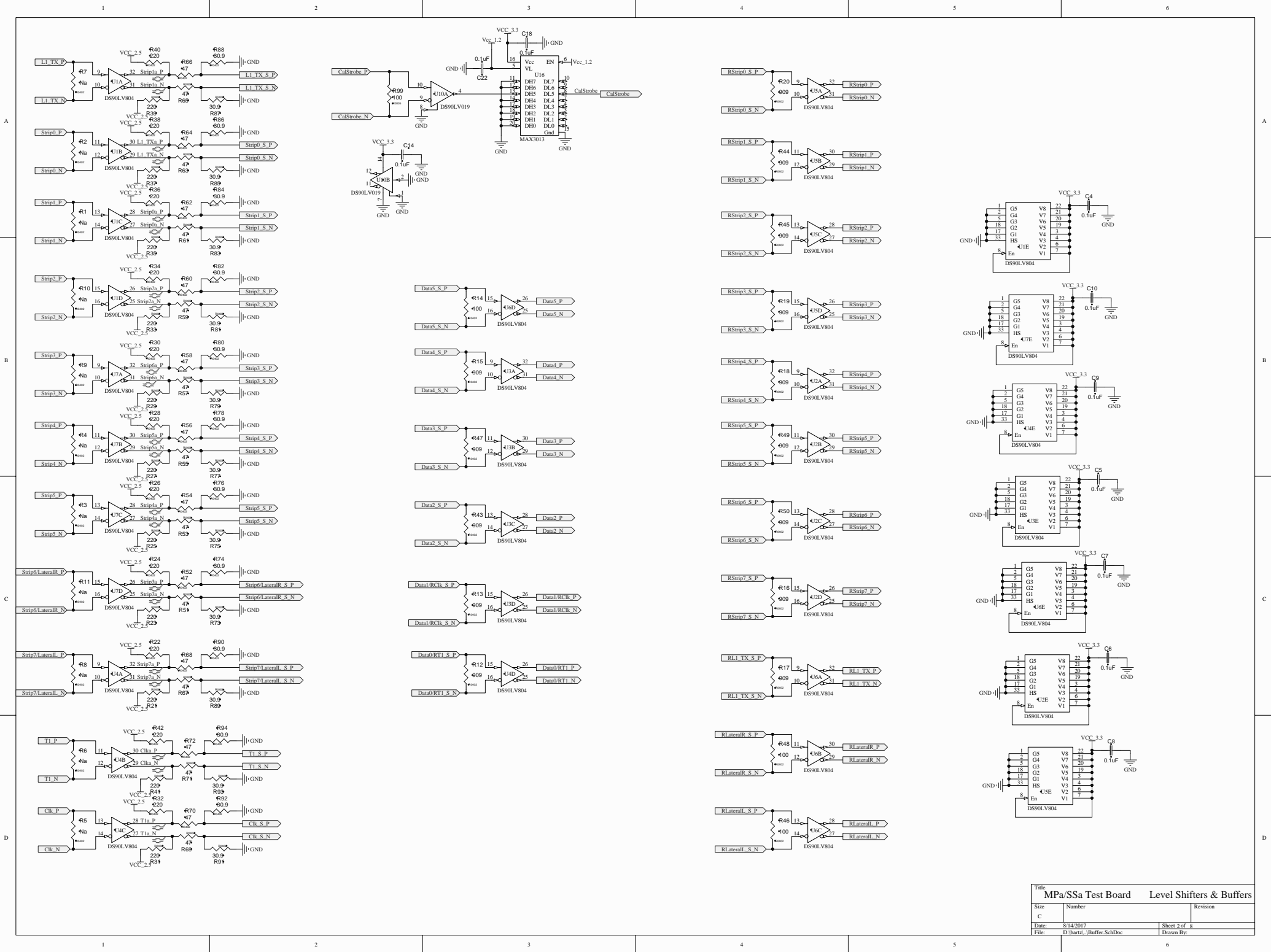
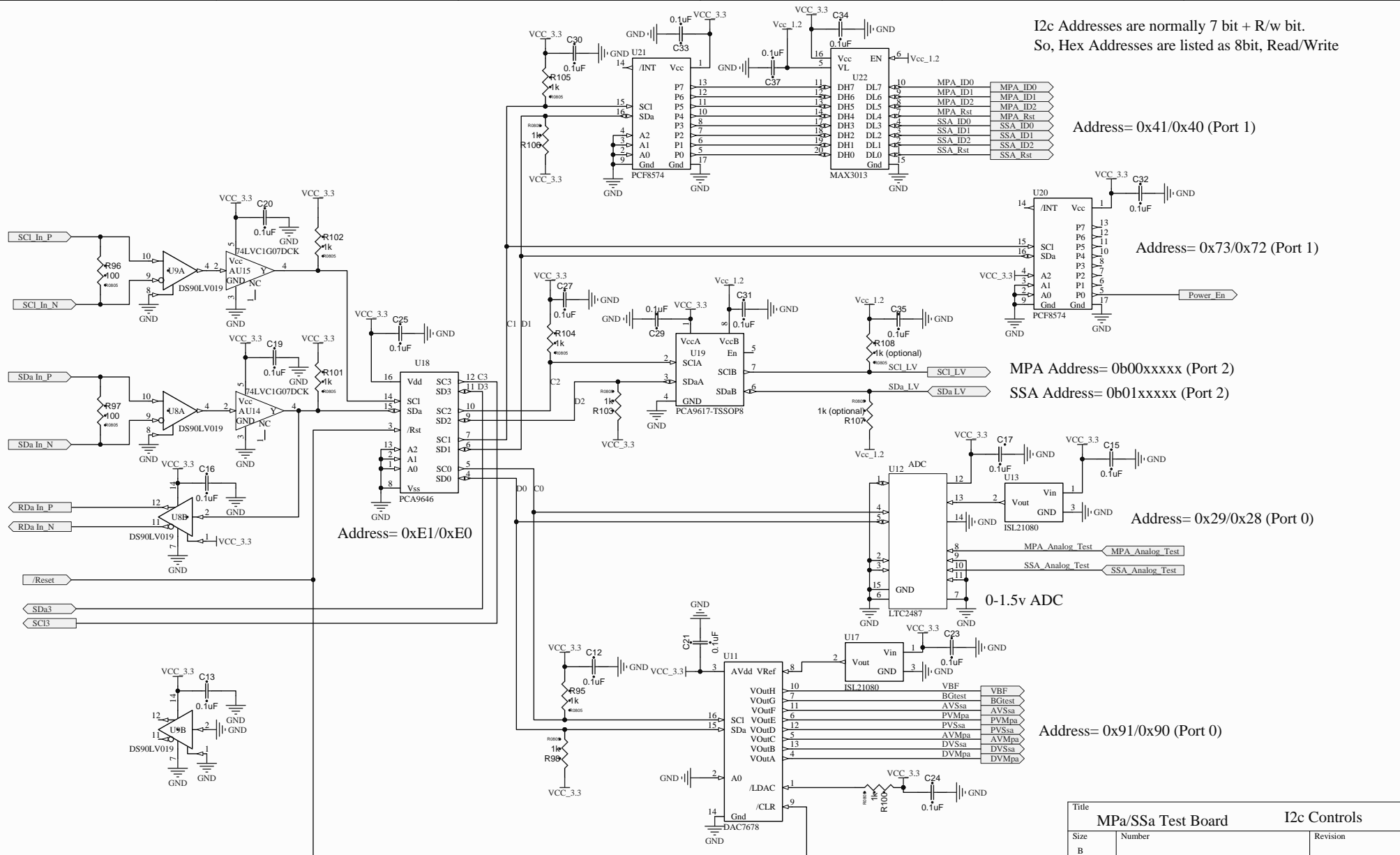


Title			
MPa/SSa Test Board		Top Level	
Size	Number	Revision	
C			
Date:	8/14/2017	Sheet 1 of 8	
File:	D:\bartz\MPA_Testboard_Top.SchDoc		Drawn By:





I2c Addresses are normally 7 bit + R/w bit.  
So, Hex Addresses are listed as 8bit, Read/Write

Address= 0x41/0x40 (Port 1)

Address= 0x73/0x72 (Port 1)

MPA Address= 0b00xxxxx (Port 2)

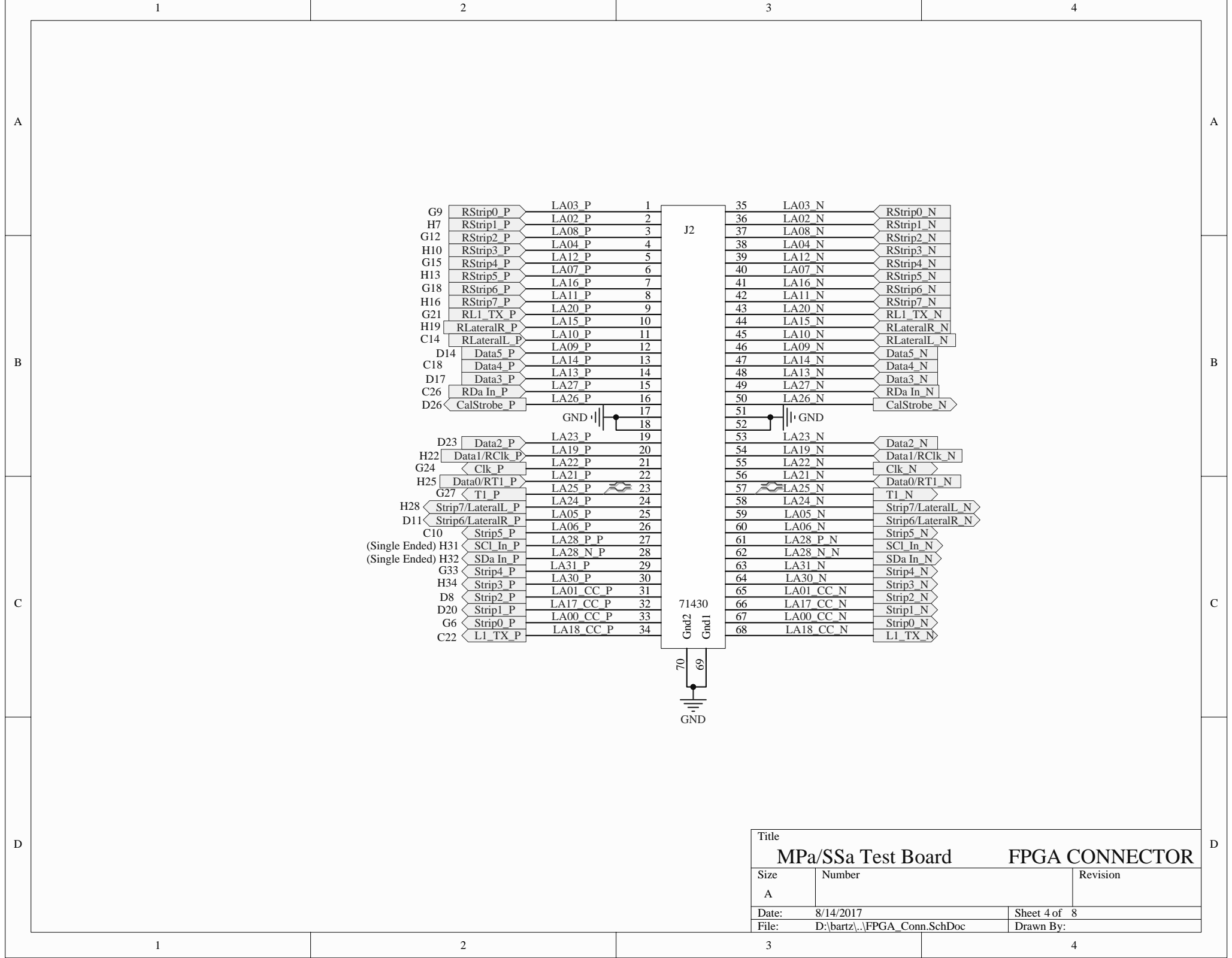
SSA Address= 0b01xxxxx (Port 2)

Address= 0x29/0x28 (Port 0)

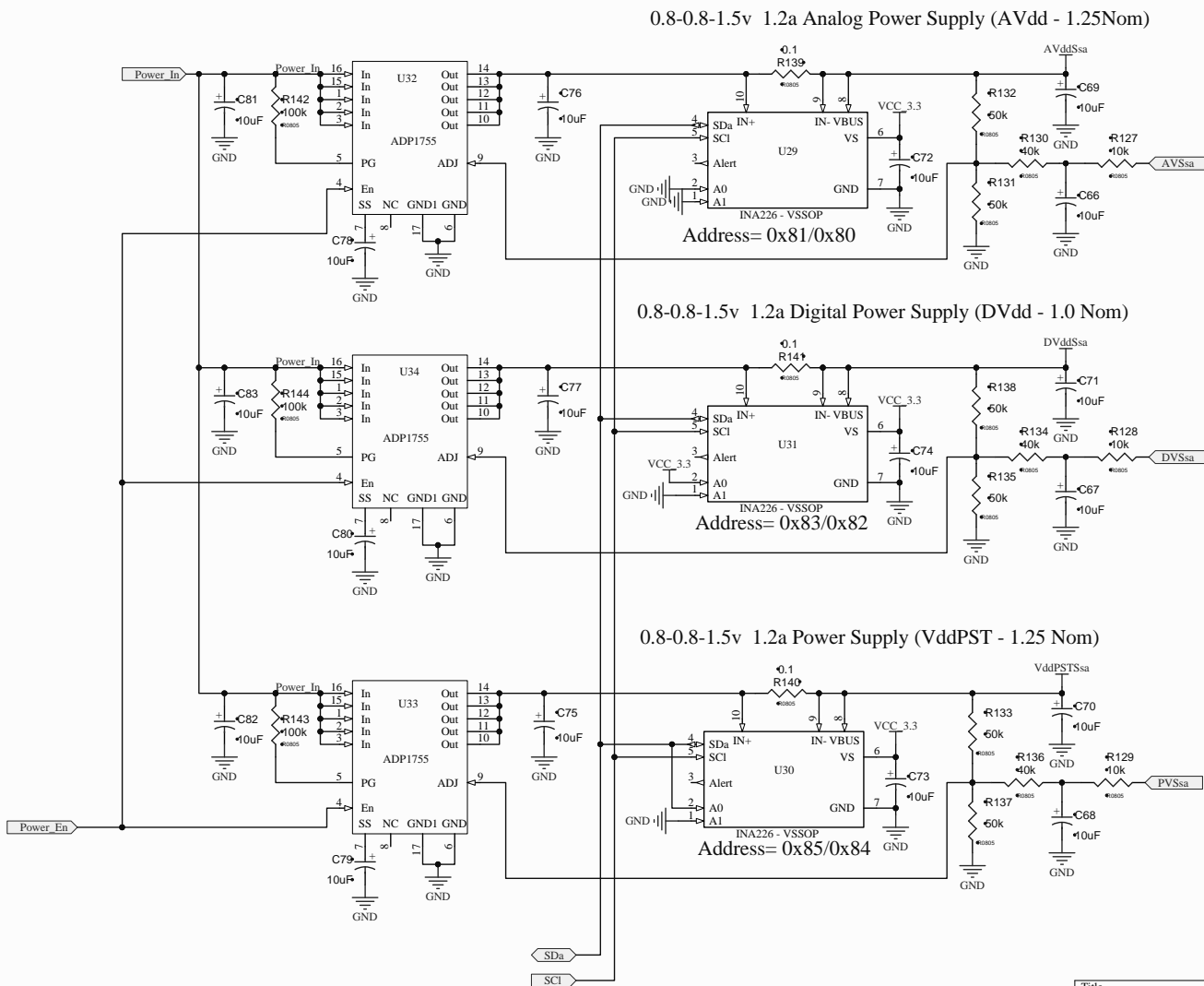
0-1.5v ADC

Address= 0x91/0x90 (Port 0)

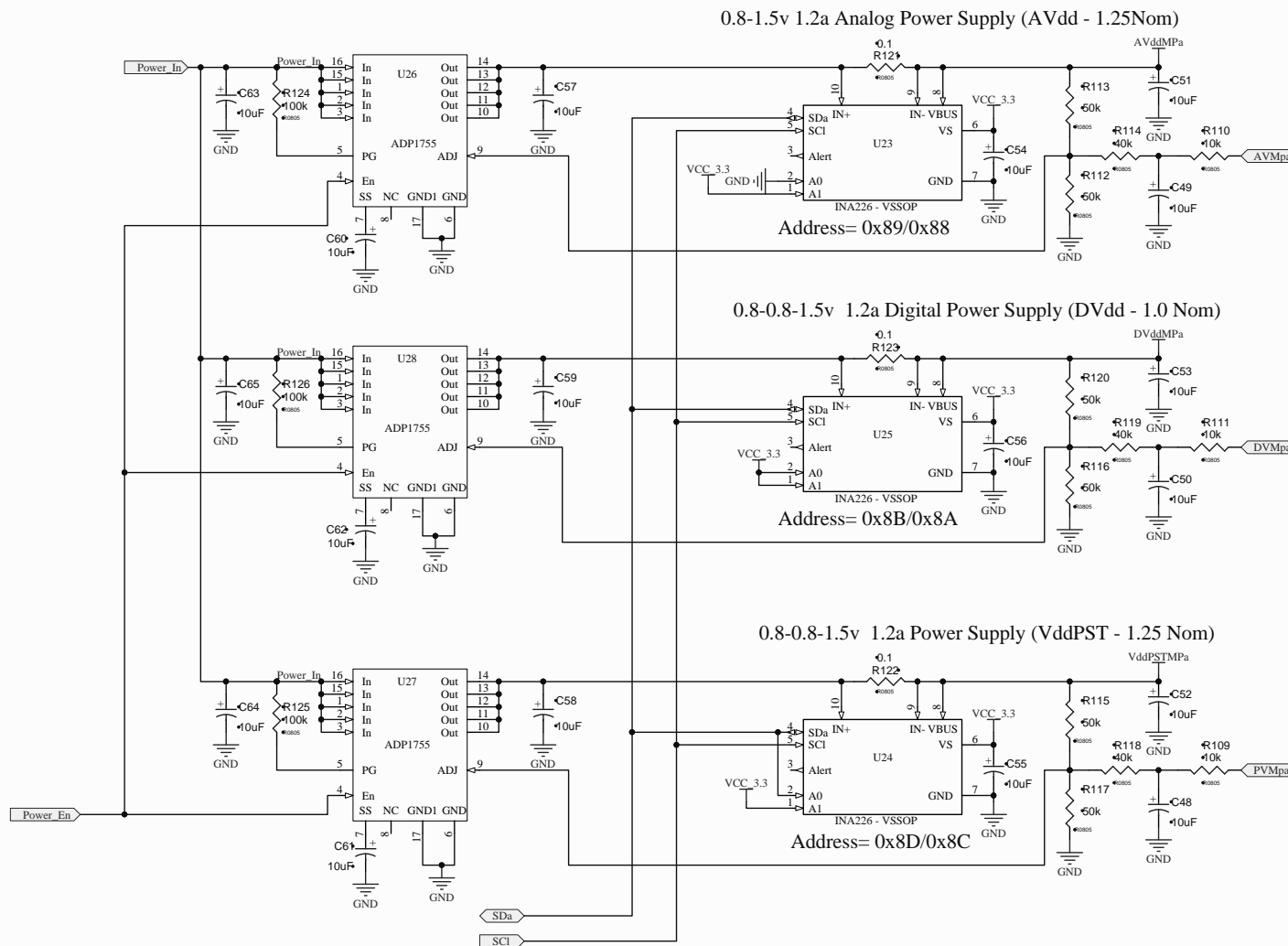
Title		I2c Controls	
Size	Number	Revision	
B			
Date:	8/14/2017	Sheet 3 of 8	
File:	D:\bartz\I2c.SchDoc	Drawn By:	



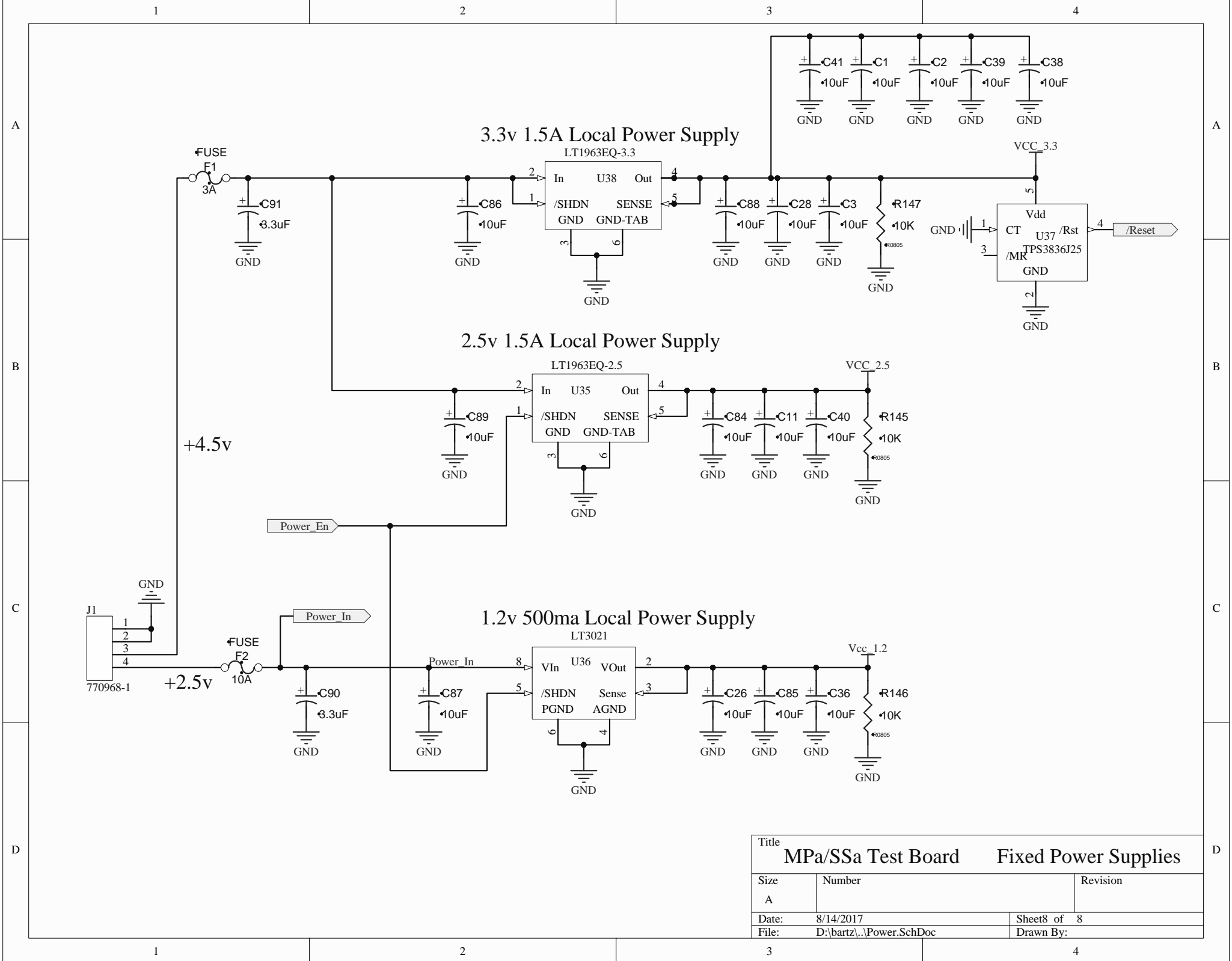
Title		
MPa/SSa Test Board		FPGA CONNECTOR
Size	Number	Revision
A		
Date:	8/14/2017	Sheet 4 of 8
File:	D:\bartz\...\FPGA_Conn.SchDoc	Drawn By:



Title		MPa/SSa Test Board	SSa Power Supplies
Size	Number	Revision	
B			
Date:	8/14/2017	Sheet 6 of 8	
File:	D:\bartz\SSaPower.SchDoc	Drawn By:	



Title		MPa/SSa Test Board	MPa Power Supplies
Size	Number	Revision	
B			
Date:	8/14/2017	Sheet 7 of	8
File:	D:\bartz\...\MPaPower.SchDoc	Drawn By:	



Title		
MPa/SSa Test Board Fixed Power Supplies		
Size	Number	Revision
A		
Date:	8/14/2017	Sheet8 of 8
File:	D:\bartz\...\Power.SchDoc	Drawn By:

