



RISC-V based MYTH

MYTH- Microprocessor for You in Thirty Hours

Workshop Certificate

{26th-30th August 2020}

Janani R

*has successfully completed
an online cloud based workshop conducted on **VSD-IAT***

Skills learned <ul style="list-style-type: none">• RISC-V ISA Overview• RISC-V GNU Tool-chain• Spike debugging• Digital Logic• Pipelined CPU Design• TL-Verilog• Markerchip IDE	17/17
Lab Skills Activities	82/82
% of RISC-V Core Designed	100%

Kunal Ghosh
(Design & Sign-off Expertise, Founder VLSI System Design)