We acknowledge and pay our respects to the Kaurna people, the traditional custodians whose ancestral lands we gather on.

We acknowledge the deep feelings of attachment and relationship of the Kaurna people to country and we respect and value their past, present and ongoing connection to the land and cultural beliefs.



Computer Systems

Lecture 02: Logic Gates



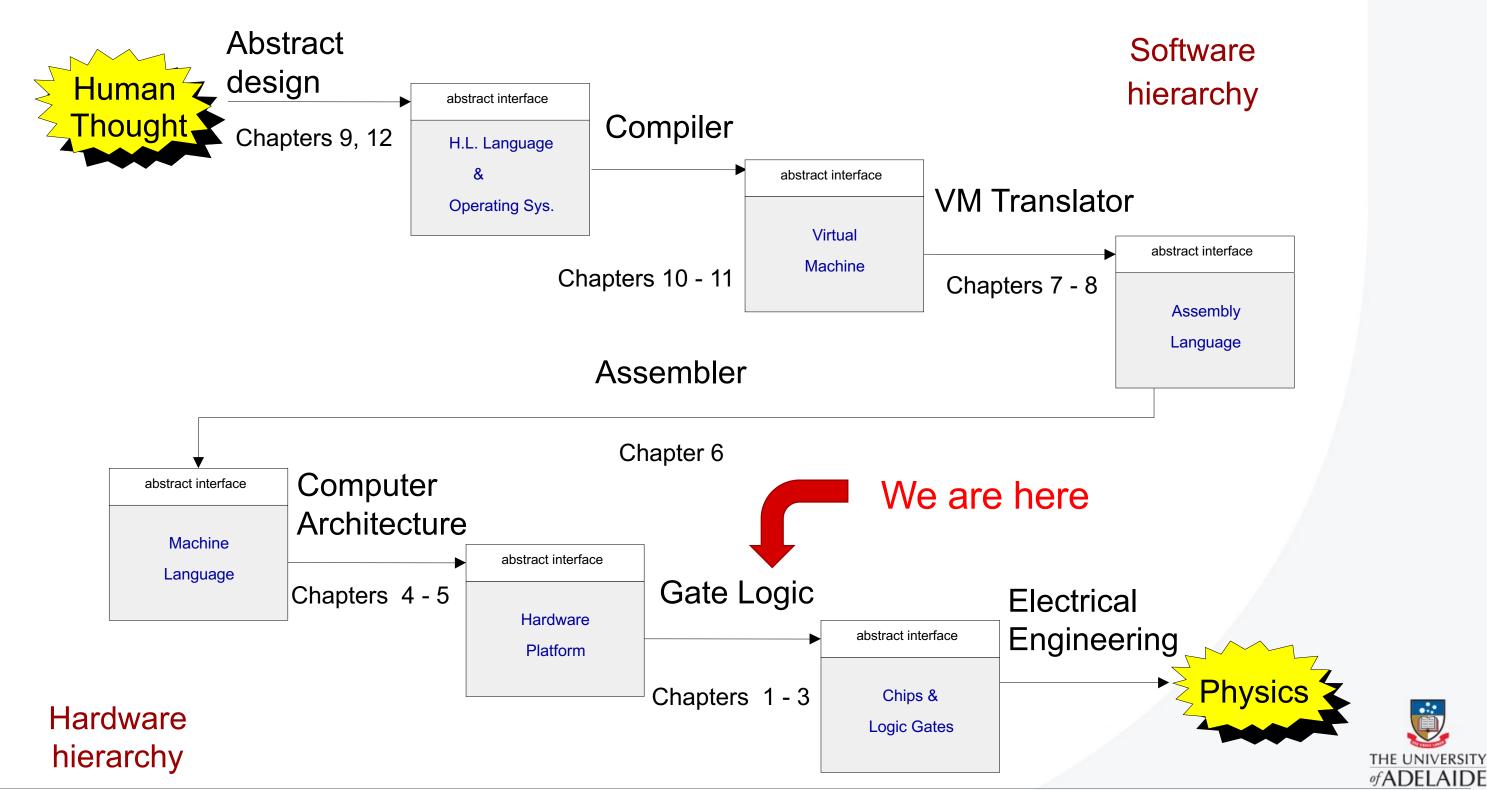
Course Summary

We're going to show you how software and hardware work together to build a computer system.

Over the course, you will build parts of that system and get practice in combinational, sequential and gate logic, as well as learning how high-level languages make things happen in real systems.



Our Journey



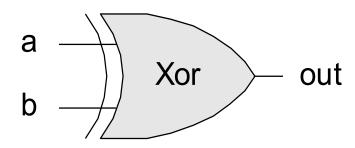
Poll

[open poll in browser]



Review: Gate Logic

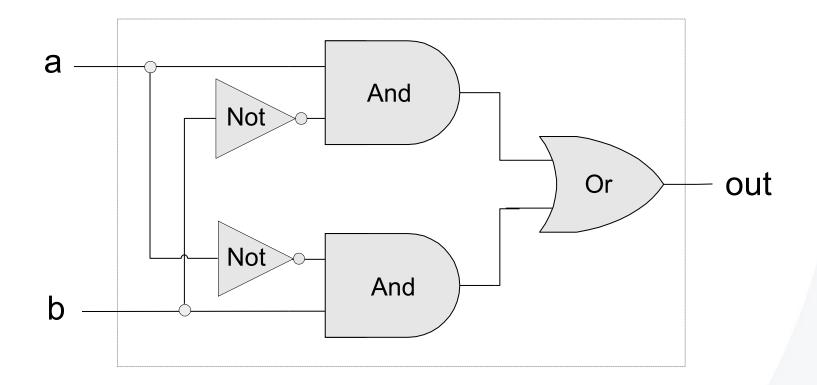
Interface



a	b	out		
0	0	0		
0	1	1		
1	0	1		
1	1	0		

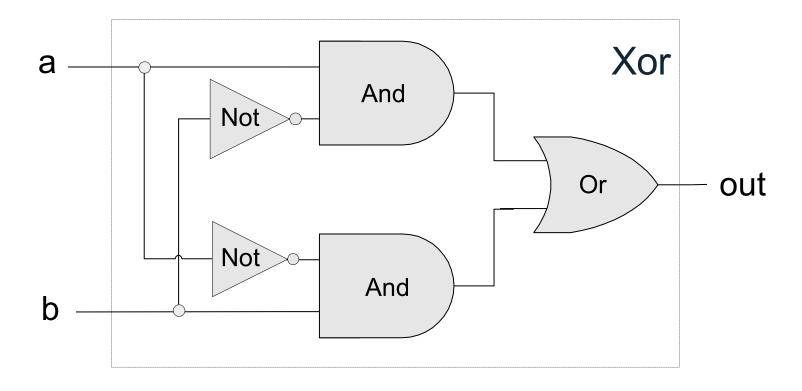
(Truth table)

Implementation





In Workshops You Will Do:



```
CHIP Xor {
    IN a, b;
    OUT out;
    PARTS:
    Not(in=a,out=na);
    Not(in=b,out=nb);
    And(a=na,b=b,out=c);
    And(a=a,b=nb,out=d);
    Or(a=c,b=d,out=out);
```



All Boolean functions of 2 variables

Eupotion	x	0	0	1	1
Function	у	0	1	0	1
Constant $0 \leftrightarrow x \cdot \bar{x}$	0	0	0	0	0
x And y	$x \cdot y$	0	0	0	1
x And Not y	$x \cdot \overline{y}$	0	0	1	0
x	χ	0	0	1	1
Not x And y	$\bar{x} \cdot y$	0	1	0	0
у	у	0	1	0	1
x Xor y (Add / Difference)	$x \cdot \bar{y} + \bar{x} \cdot y$	0	1	1	0
x Or y	x + y	0	1	1	1
x Nor y	$\overline{x+y}$	1	0	0	0
x Xnor y (Equivalence)	$x \cdot y + \bar{x} \cdot \bar{y}$	1	0	0	1
Not y	\bar{y}	1	0	1	0
$x ext{ Or Not } y ext{ (If y then x)}$	$x + \bar{y}$	1	0	1	1
Not x	\bar{x}	1	1	0	0
Not x Or y (If x then y)	$\bar{x} + y$	1	1	0	1
x Nand y	$\overline{x \cdot y}$	1	1	1	0
Constant $1 \leftrightarrow x + \bar{x}$	1	1	1	1	1



Canonical Form: <u>representation of a Boolean</u> <u>function in terms of And, Or, and Not</u>

We can construct a canonical representation of any Boolean function with:

- And (symbol "dot product" Example: $x \cdot y$)
- Or (symbol "plus sign" Example: x + y)
- Not (symbol "bar on top" Example: \overline{x})

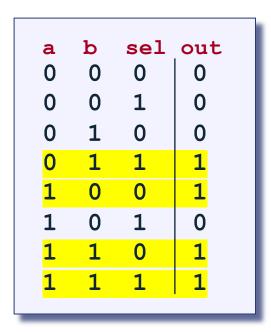
How to write a canonical form?

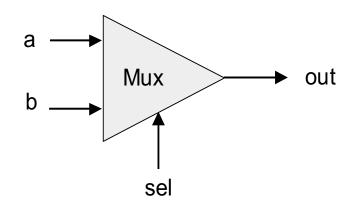
- Sum of Product (SoP) ← focus in this course
- Product of Sum (PoS)
- Karnaugh Map (K-map)

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Canonical Form – Sum of Product (SoP)





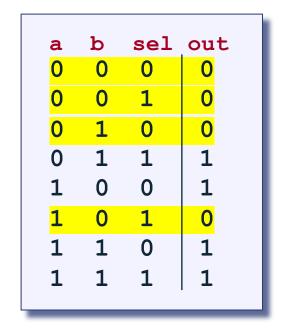
Exercise at the end!

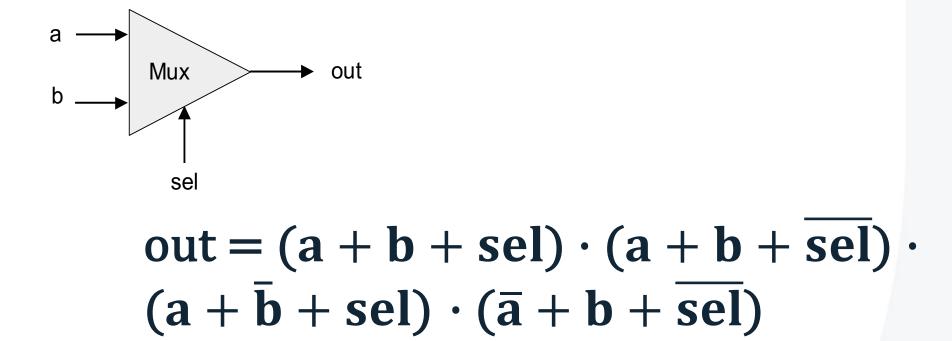
out =
$$(\bar{a} \cdot b \cdot sel) + (a \cdot \bar{b} \cdot \overline{sel}) + (a \cdot b \cdot \overline{sel}) + (a \cdot b \cdot sel)$$

- 1. Find all lines with out = 1, ignore all lines with out = 0.
- 2. Write inputs in product (And), if the input is 1 write as it is, otherwise, write in bar (Not) form.
- 3. Repeat 2. for each line with out = 1.
- 4. Use sum (Or) to connect each term.



Canonical Form – Product of Sum (PoS)

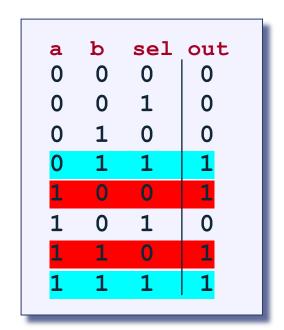


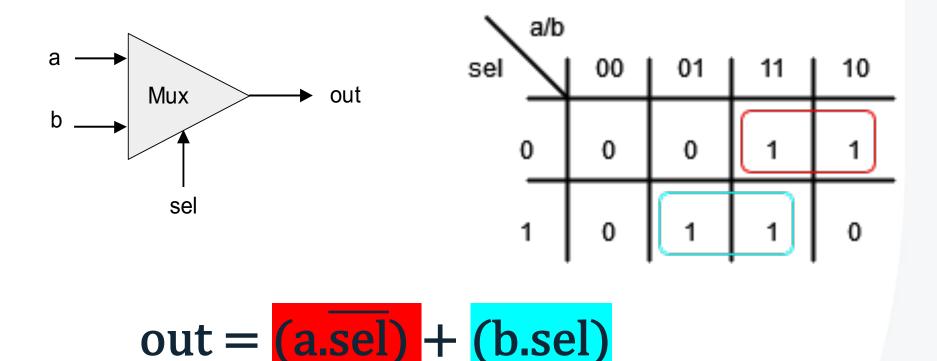


- 1. Find all lines with out = 0, ignore all lines with out = 1.
- 2. Write inputs in sum (Or), if the input is 0 write as it is, otherwise, write in bar (Not).
- 3. Repeat 2. for each line with out = 0.
- 4. Use product (And) to connect each term.



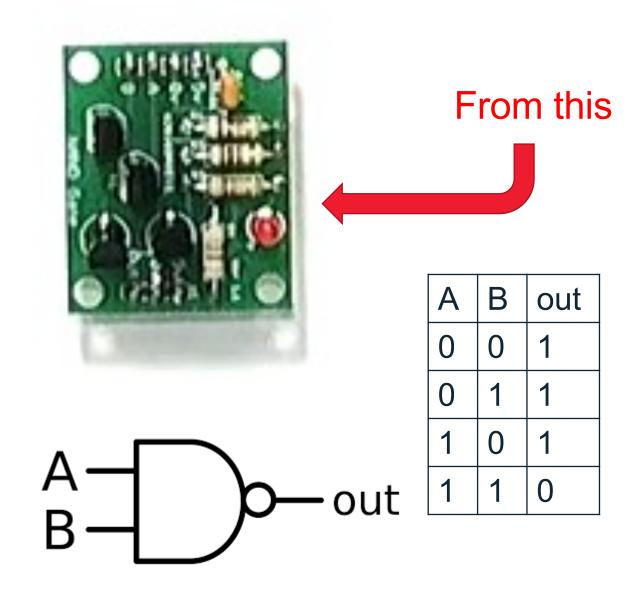
Canonical Form – Karnaugh Map (K-map)





K-map is one of the systematic ways of simplifying Boolean expressions. In this course, we will not ask for efficient/simplified Boolean expressions. Extra reading: https://www.allaboutcircuits.com/textbook/digital/chpt-8/logic-simplification-karnaugh-maps/

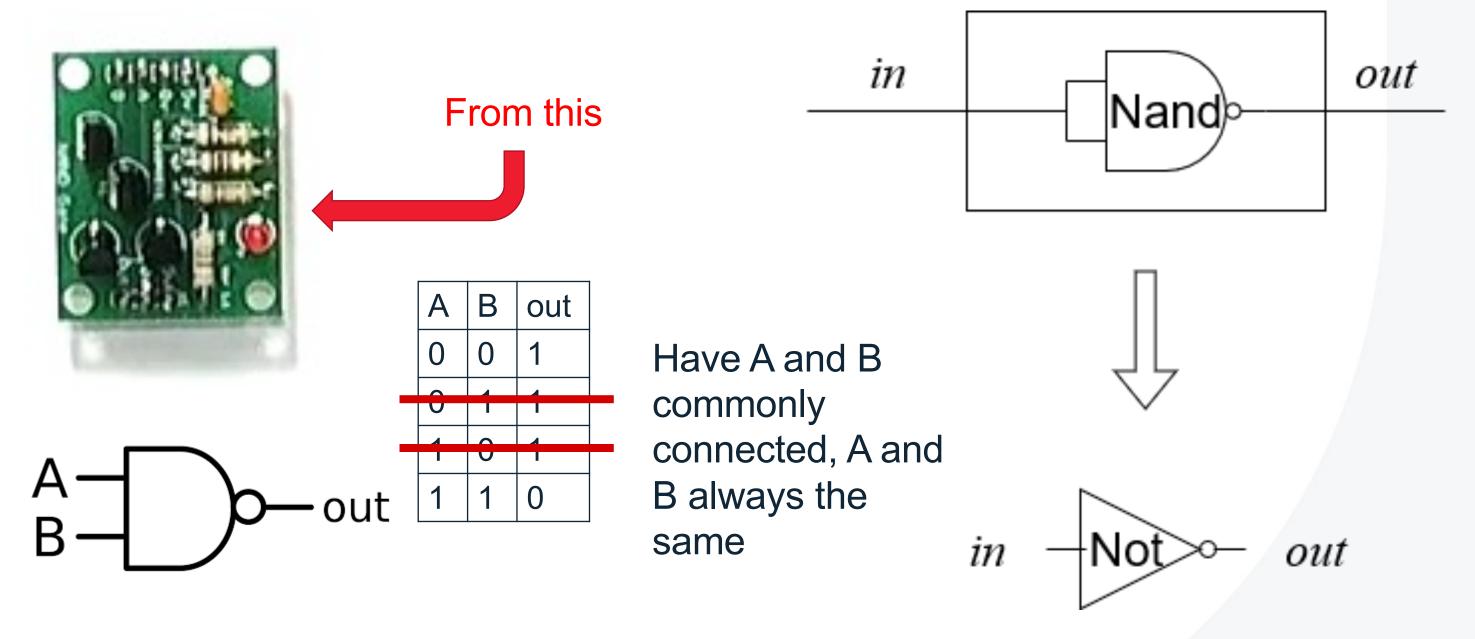




$$out = \overline{A \cdot B}$$



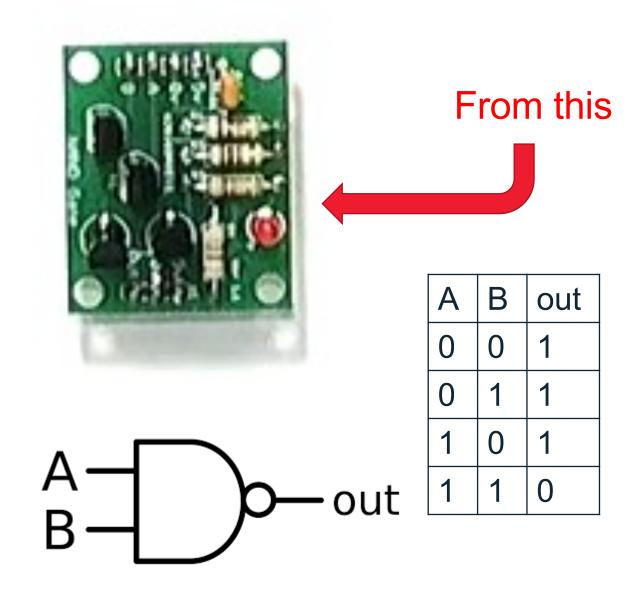
Formal proof in Textbook Appendix A1.3



 $out = \overline{A \cdot B}$

Formal proof in Textbook Appendix A1.3

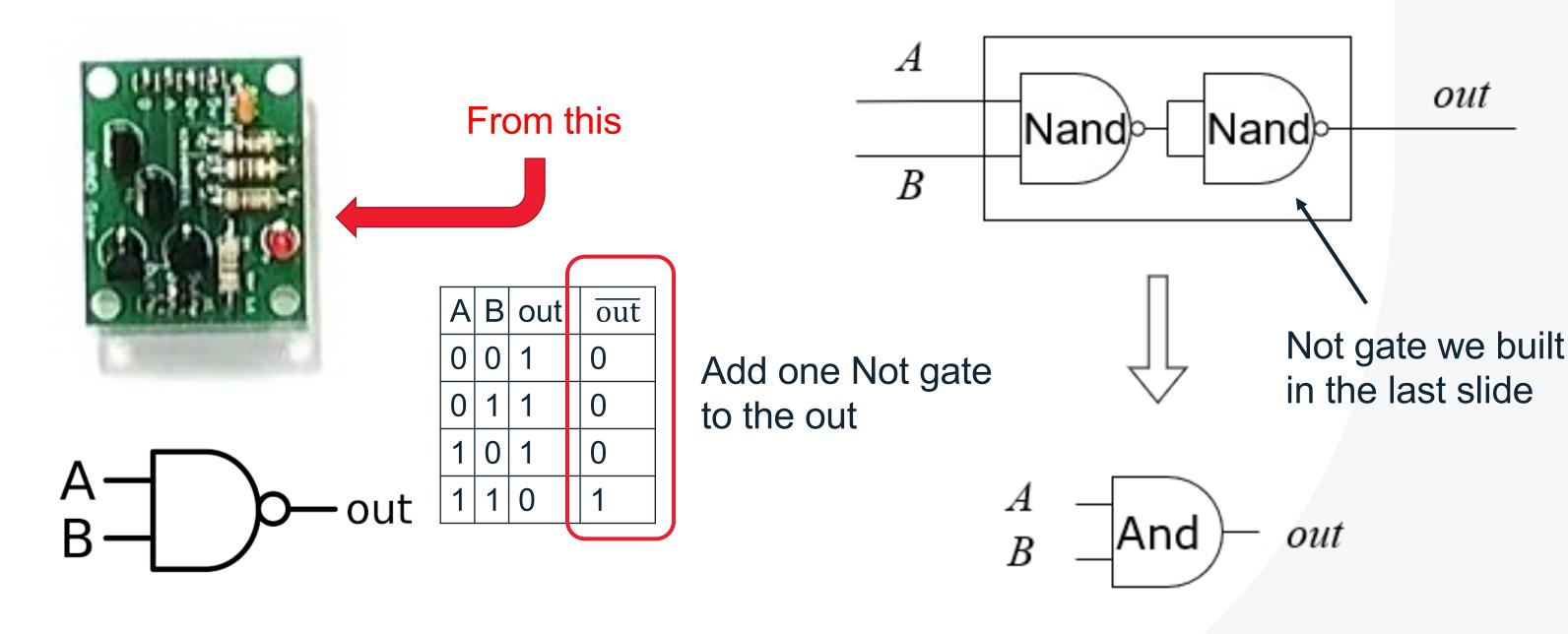




$$out = \overline{A \cdot B}$$



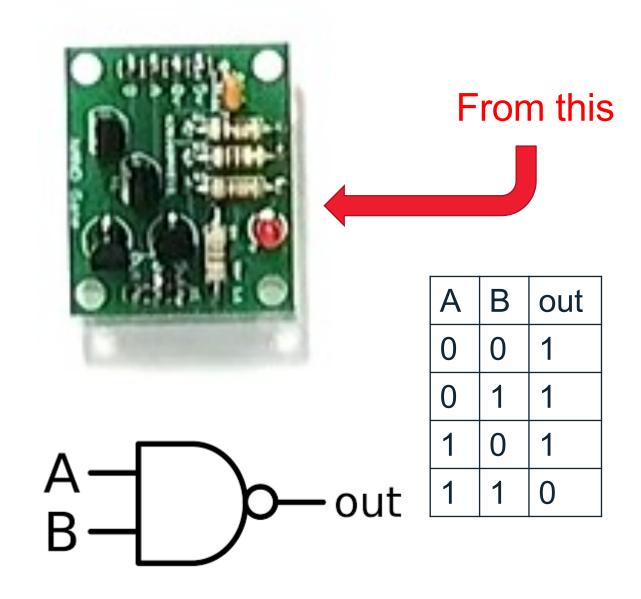
Formal proof in Textbook Appendix A1.3



$$out = \overline{A \cdot B}$$

Formal proof in Textbook Appendix A1.3

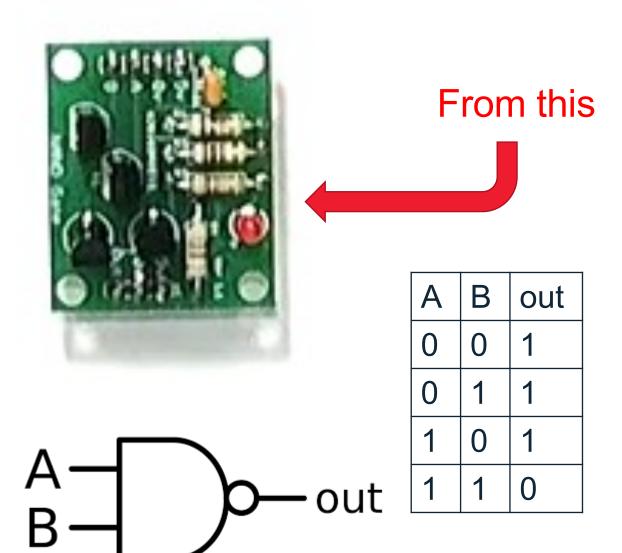




$$out = \overline{A \cdot B}$$



Formal proof in Textbook Appendix A1.3



In propositional logic and Boolean algebra, **De Morgan's laws**, ^{[1][2][3]} also known as **De Morgan's theorem**, ^[4] are a pair of transformation rules that are both valid rules of inference. They are named after Augustus De Morgan, a 19th-century British mathematician. The rules allow the expression of conjunctions and disjunctions purely in terms of each other via negation.

The rules can be expressed in English as:

- The negation of a disjunction is the conjunction of the negations
- The negation of a conjunction is the disjunction of the negations

or

- The complement of the union of two sets is the same as the intersection of their complements
- The complement of the intersection of two sets is the same as the union of their complements

or

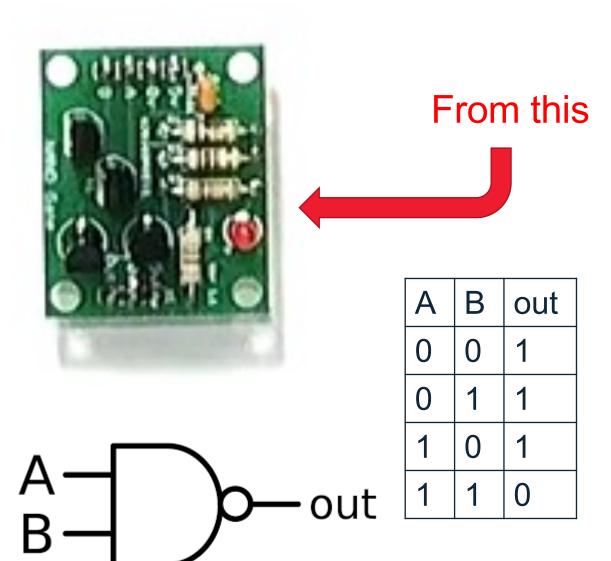
- not (A or B) = (not A) and (not B)
- not (A and B) = (not A) or (not B)

where "A or B" is an "inclusive or" meaning at least one of A or B rather than an "exclusive or" that means exactly one of A or B.

$$out = \overline{A \cdot B}$$

Formal proof in Textbook Appendix A1.3





In electrical and computer engineering, De Morgan's laws are commonly written as:

$$\overline{(A\cdot B)}\equiv (\overline{A}+\overline{B})$$

and

$$\overline{A+B}\equiv \overline{A}\cdot \overline{B},$$

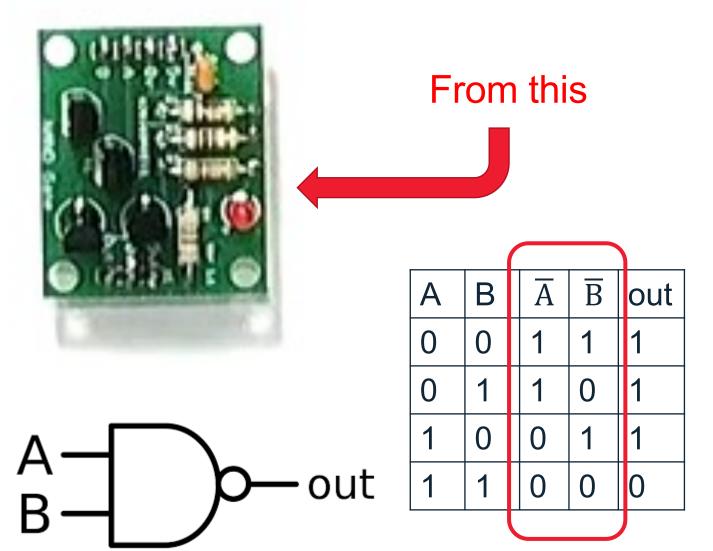
where:

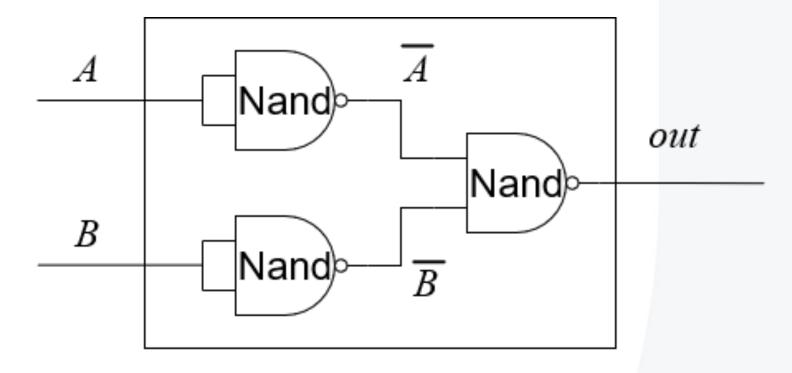
- · is the logical AND,
- + is the logical OR,
- the overbar is the logical NOT of what is underneath the overbar.

$$out = \overline{A \cdot B}$$

Formal proof in Textbook Appendix A1.3







Add one Not gate to each of the A and B

$$x+y = \overline{x}.\overline{y}$$

De Morgan's Theorem

$$out = \overline{A \cdot B}$$

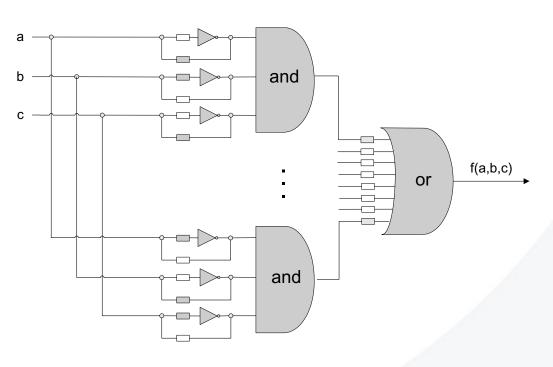
Formal proof in Textbook Appendix A1.3



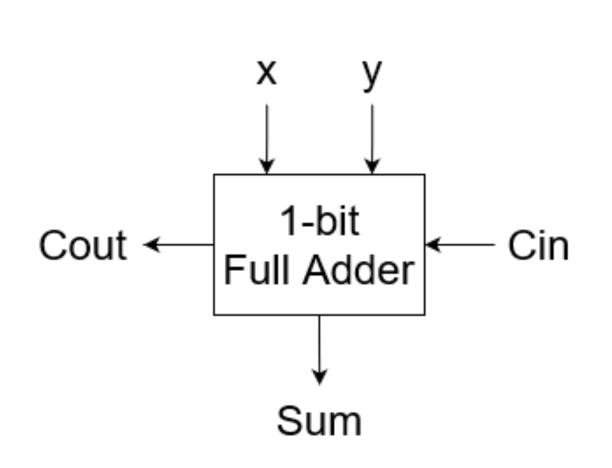
20

Boolean Functions: Summary

- Each Boolean function has a canonical representation
- The canonical representation is expressed in terms of <u>And, Or, Not</u>
- And, Not, Or can be expressed in terms of <u>Nand alone</u> (or Nor)
- Every Boolean function can be implemented by a standard circuit consisting of Nand gates only
- Mass production
- Universal building blocks, unique topology



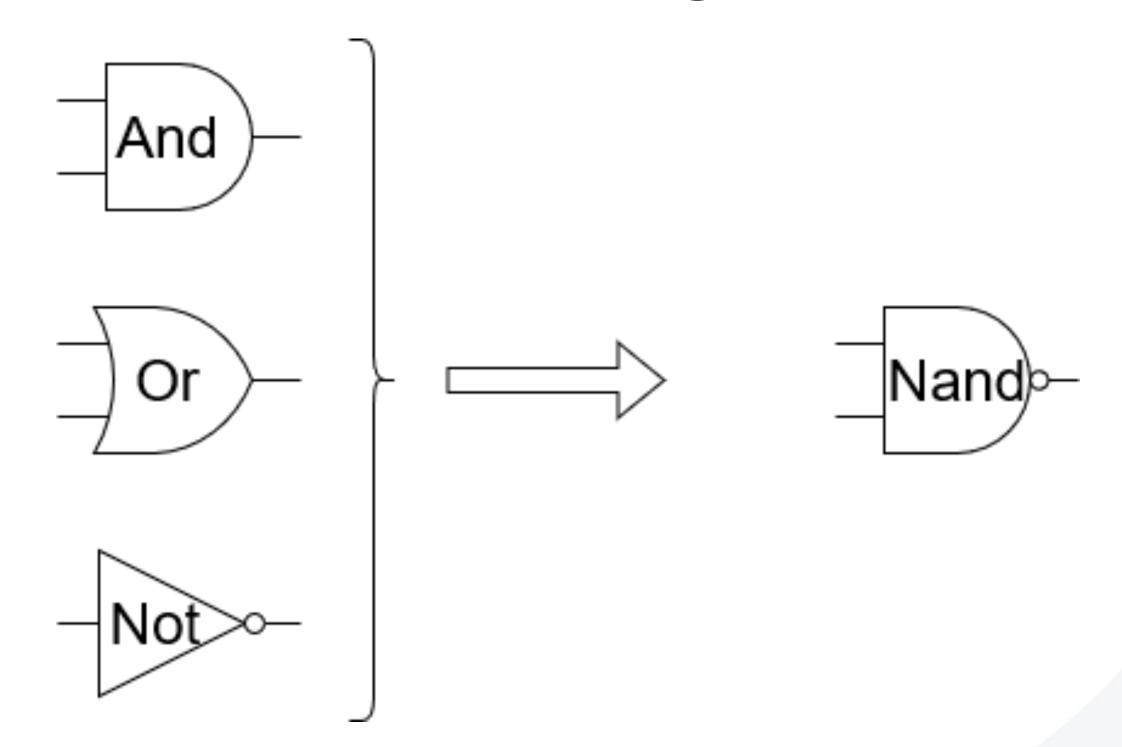
Exercise: Canonical Form Boolean Expression for 1-bit Full Adder



x	у	Cin	Sum	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

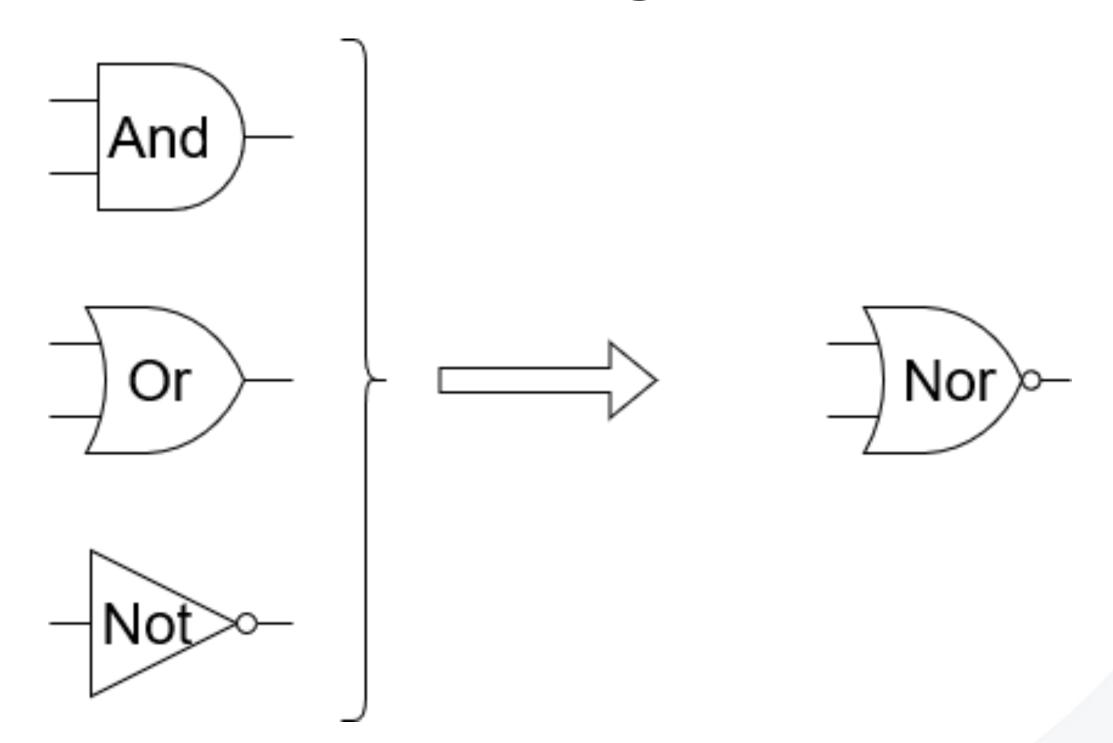


Exercise: Build Nand using And, Or, Not



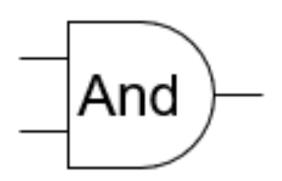


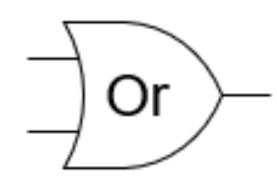
Exercise: Build Nor using And, Or, Not

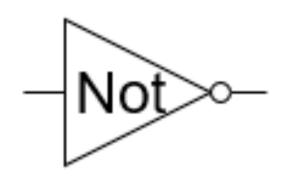




Exercise: Build Nor using And, Or, Not







In electrical and computer engineering, De Morgan's laws are commonly written as:

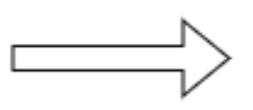
$$\overline{(A\cdot B)}\equiv (\overline{A}+\overline{B})$$

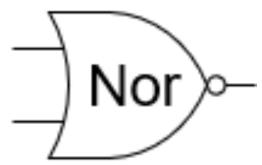
and

$$\overline{A+B}\equiv \overline{A}\cdot \overline{B},$$

where:

- · is the logical AND,
- + is the logical OR,
- the overbar is the logical NOT of what is underneath the overbar.







This Week

- Review <u>Chapter 1 of the Textbook</u>
- Start Assignment 1 (available Now)
- Workshops start in week 2
- Week 2 <u>quiz due before</u> Thursday lecture.
- Please use Piazza for communication
 - Email me for personal issues only (start subject with [CS])
 - Email me about prerequisites extensions