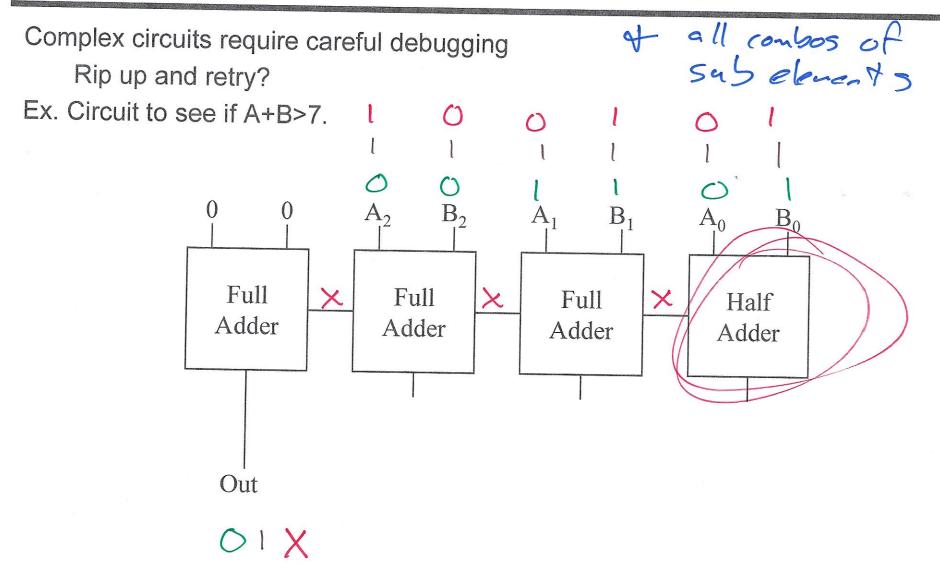
Review Problem 9

* What does the number 100011₂ represent?

Small: try all combos big: try all behaviors

Debugging Complex Circuits



Debugging Complex Circuits (cont.)

```
module fullAdd (Cout, S, A, B, Cin);
  output Cout, S; input A, B, Cin;
 assign Cout = (A&B) | (A&Cin) | (B&Cin);
 assign S = A^B^Cin;
endmodule
module halfAdd (Cout, S. A. B):
 output Cout, S; input A, B;
                                (10)
 fullAdd a1(.Cout, .S, .A, .B, .Cin);
endmodule
module greaterThan7 (Out, A, B);
 output Out; input [2:0] A, B; wire [3:0] C, S;
 halfAdd pos0(.Cout(C[0]), .S(S[0]), .A(A[0]), .B(B[0]));
 fullAdd pos1(.Cout(C[1]), .S(S[1]), .A(A[1]), .B(B[1]), .C(C[0]));
 fullAdd pos2(.Cout(C[2]), .S(S[2]), .A(A[2]), .B(B[2]), .C(C[1]));
 fullAdd pos3(.Cout(C[3]), .S(Out), .A(0), .B(0), .C(C[2]));
endmodule
```

Debugging Approach

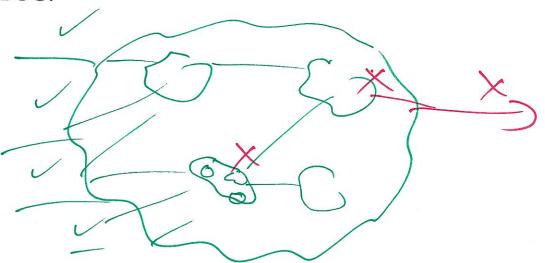
Test all behaviors.

All combinations of inputs for small circuits, subcircuits.

Identify any incorrect behaviors.

Examine inputs and outputs to find earliest place where value is wrong. Typically, trace backwards from bad outputs, forward from inputs. Look at values at intermediate points in circuit.

DO NOT RIP UP, DEBUG!



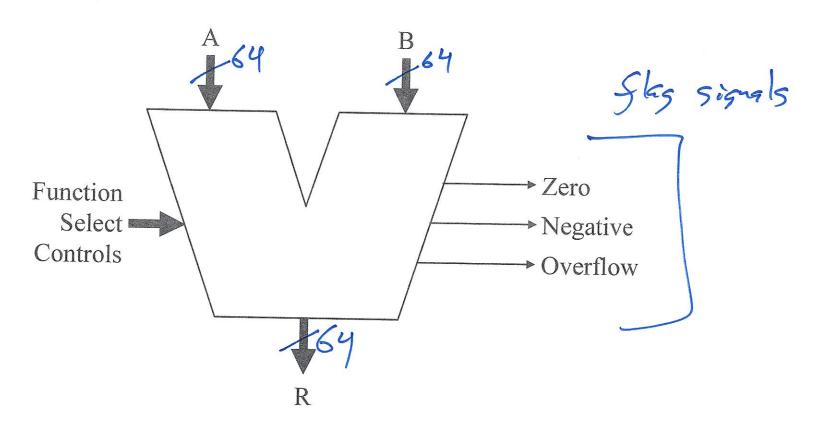
ALU: Arithmetic Logic Unit

Computes arithmetic & logic functions based on controls

Add, subtract

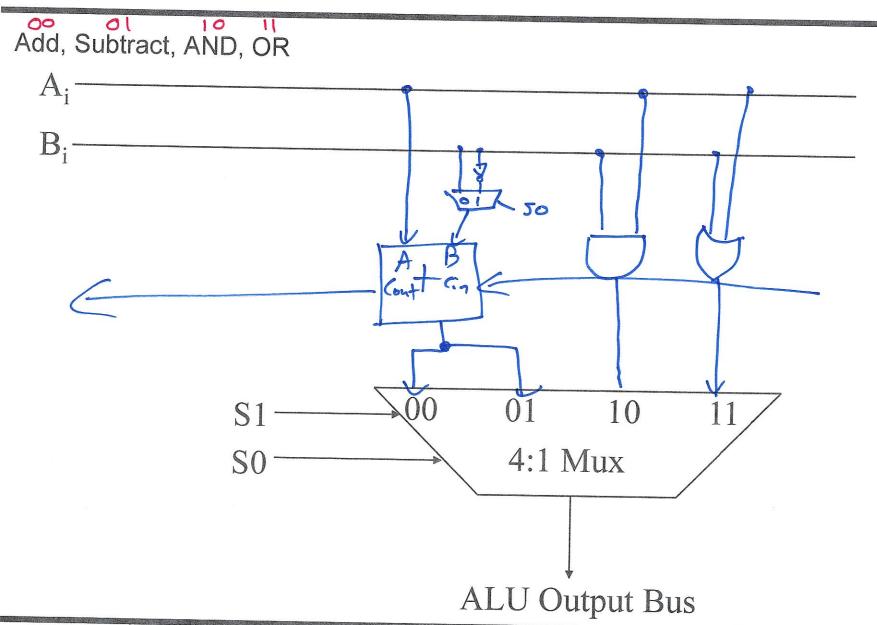
XOR, AND, NAND, OR, NOR

==, <, overflow, ...



ith 5:+

Bit Slice ALU Design



Bit Slice ALU Design (cont.)

