



ASPLOS Tutorial 2024

Janus 2.0: A Software Framework for Analyzing Optimizing and Implementing Quantum Circuit



Organizers: **Jianwei Yin, Liqiang Lu, Siwei Tan**

College of Computer Science and Technology
Zhejiang University (ZJU)

<https://janusq.github.io/tutorials/>



Self-introduction



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Jianwei Yin

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Dr. Jianwei Yin is a full professor at the College of Computer Science, Zhejiang University. He is the dean of the School of Software Technology. His interests lie in advanced computing and service computing and has published more than 300 papers in top international journals and conferences such as ASPLOS, MICRO, HPCA, DAC, VLDB, ICDE, TC, TSE, TKDE, TPDS, et al. He led the setup of two international standards and won many Best Paper Awards , such as ICSOC 2017 and ICWS 2019 .

- Dean of School of Software at Zhejiang University
- Director of Development & Planning Department of Z



Organizers



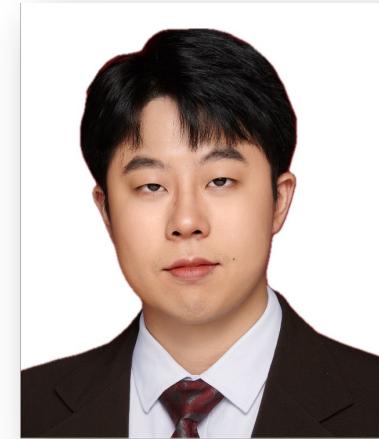
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Liqiang Lu
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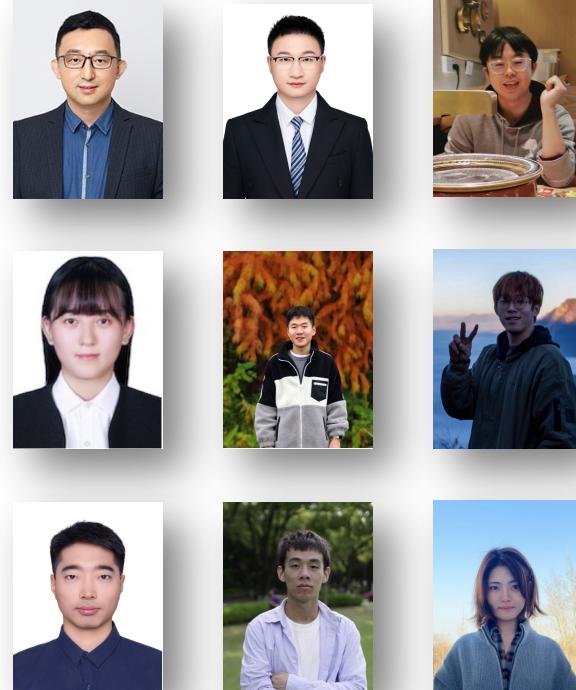
Siwei Tan
5-year PhD student
siweitan@zju.edu.cn





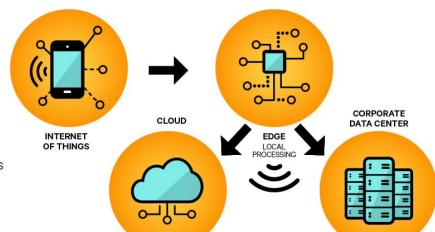
ACES Lab

ADVANCED COMPUTING AND EMERGING SERVICE LAB

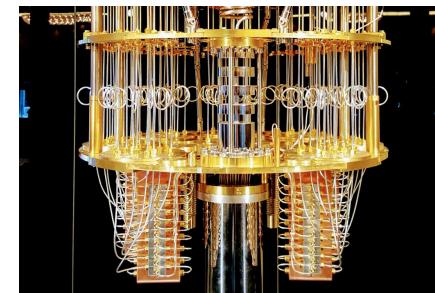


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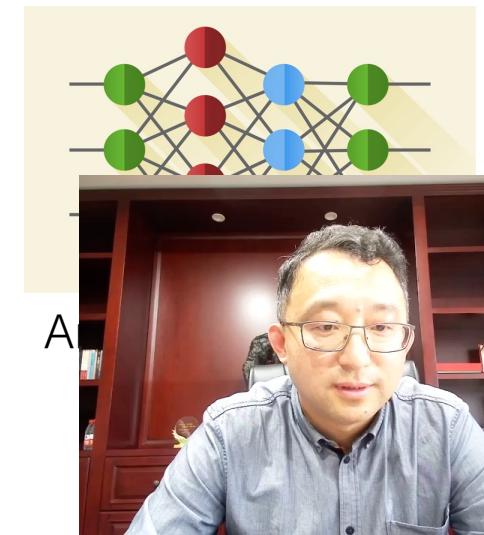
Advanced Computing and Emerging Service Lab (ACES Lab) in Zhejiang University is led by Professor Yin Jianwei. It consists of 28 faculty members and 170 students from the College of Computer Science and Technique, Zhejiang University. The laboratory focuses on **quantum computing, edge computing, and artificial intelligence**, as well as emerging services enabled by these advanced computing technologies.



Service computing



Quantum computing



A

Milestone of Janus Quantum



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The founding of Janus Quantum



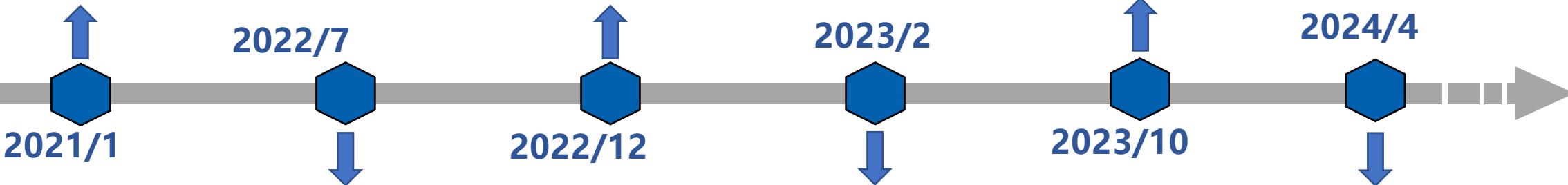
JanusQ Software 2.0

- Cluster I/O
- Quantum compilation



Many achievements were published in top conferences

- ICCAD & MICRO
- ICCAD quantum chemistry competition



Taiyuan-1 Quantum Cloud Platform

The first computational visualization and distributed scheduling platform.



Hybrid quantum-classical SAT solver

Revealed with JanusQ cloud in HPCA 2023



ASPLOS & Janus 2.0 tutorial

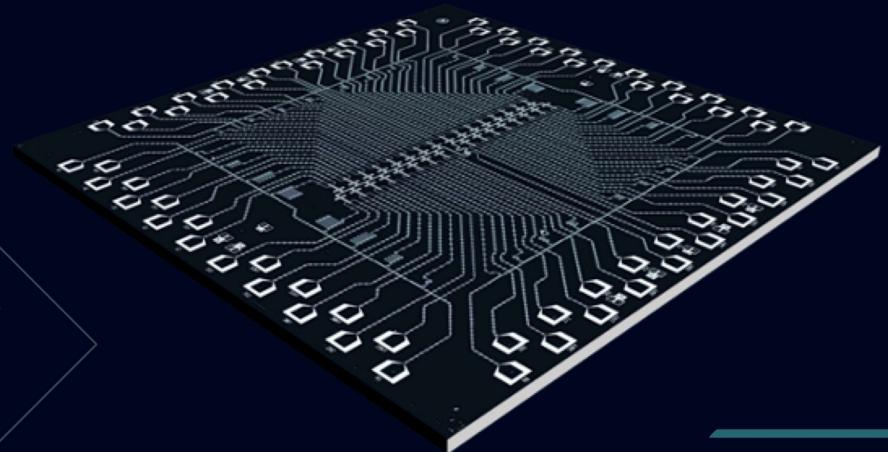
- Ultra-fast readout calibration
- China's first quantum tutorial



Quantum Processor on JanusQ

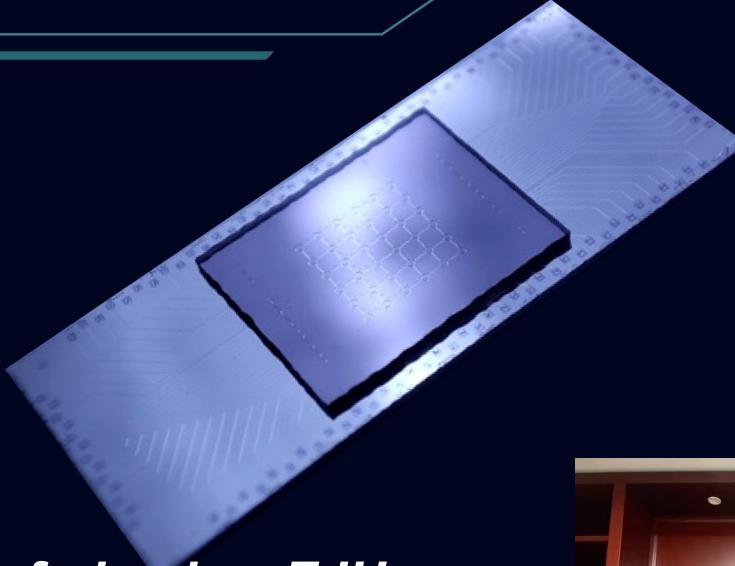


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Mogan

32 qubits/ 30 μ s relaxation time / Fully connected topology



Tianmu

36 qubits/ 100 μ s relaxation time/
60ns two-qubit gate time
CZ gate fidelity: 99%

Collaborate with the department of physics, ZJU



Coding interface of JanusQ

QuCode

Run Program
QFT manipulation
Export

```

var sender = qint.new(3, 'S')
var receiver = qint.new(3, 'R')
var ancillary = qint.new(1, 'A')

qc.write(0x0)

let label = 'genData'
qc.startLabel(label)
sender.ry(135, 0x2 | 0x4)
qc.disableDisplay(label)
qc.endLabel(label)

qc.nop()

label = 'InvQFT'
qc.startLabel(label)
sender.invQFT()
qc.endLabel(label)

qc.nop()
label = 'send'
qc.startLabel(label)
sender.exchange(receiver)
qc.endLabel(label)
qc.disableDisplay(label)
qc.nop()

label = 'QFT'
qc.startLabel(label)
receiver.QFT()

```

Program editor

Circuit reuse

Console

```

Freq 1 = 7
Freq 2 = 7
Freq 3 = 0
Freq 4 = 7

```

A

B

C

D

Quantum circuit view

Evolution view

Variable State

State view

Execution modes

switching mode

Choose mode

- Quantum cluster
- Quantum computer
- python simulator
- JavaScript simulator
- analysis

cancel
confirm

Provided quantum processors

Resources

计算机名字
N36U19_0

ONLINE
10
STATUS

QUBITS

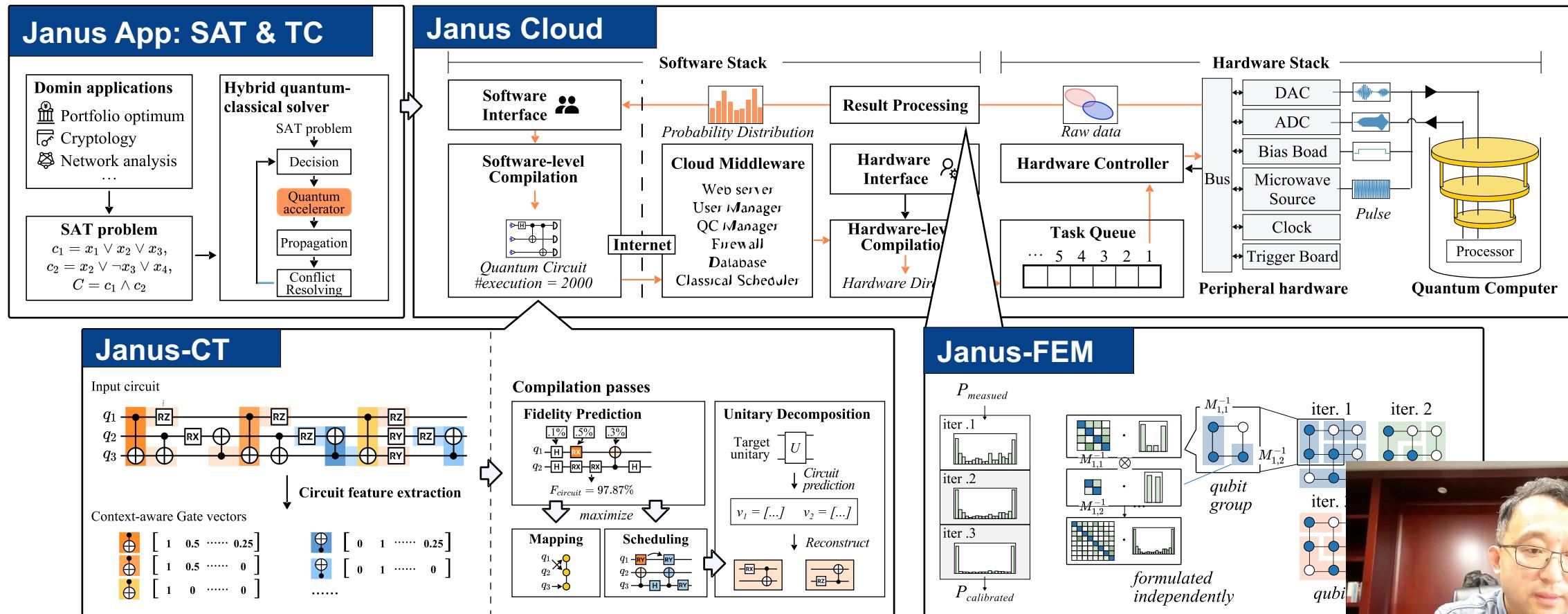
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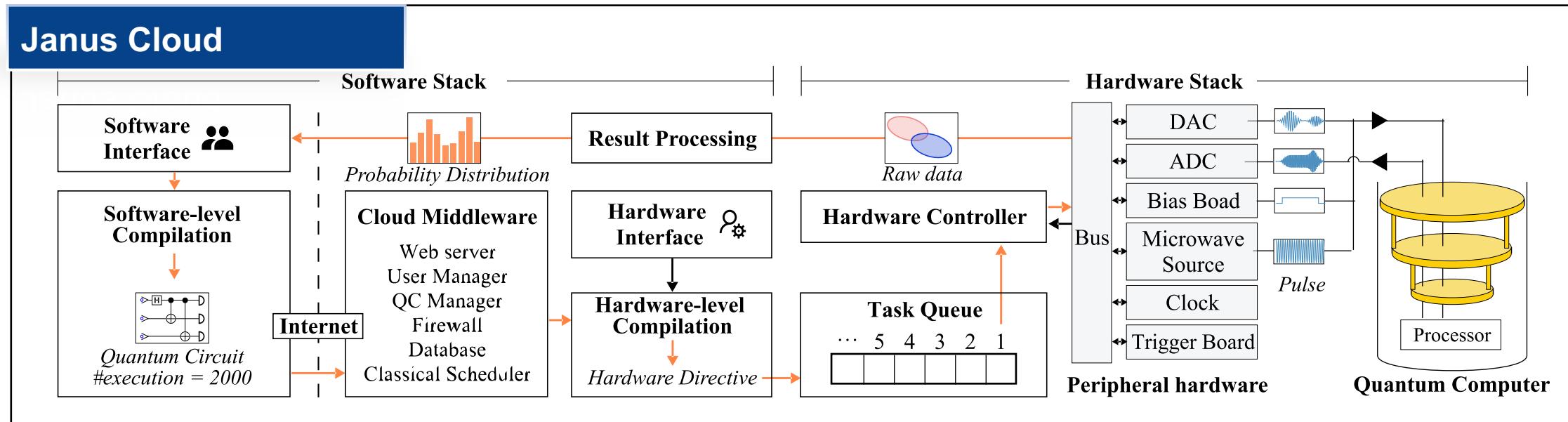
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STATUS

QUBITS



Janus 2.0: A Software Framework for Analyzing Optimizing and Implementing Quantum Circuit





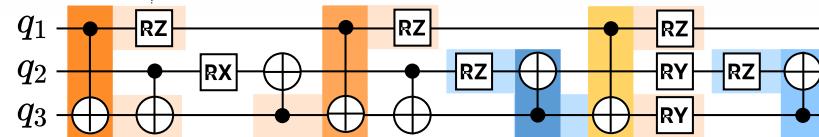
Janus Quantum Infrastructure:

- How do you use the code editor on the cloud platform?
- How do we submit the task to quantum hardware via API?
- How do we take advantage of JanusQ architecture?



Janus-CT. C: Contextual, T: Topological

Input circuit

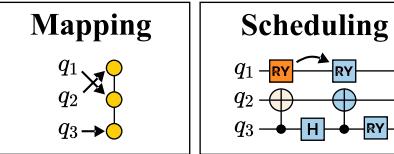
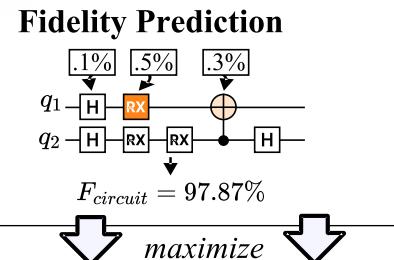


Circuit feature extraction

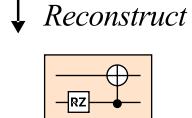
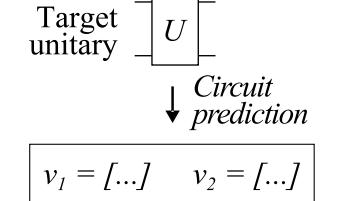
Context-aware Gate vectors

	$\begin{bmatrix} 1 & 0.5 & \dots & 0.25 \end{bmatrix}$		$\begin{bmatrix} 0 & 1 & \dots & 0.25 \end{bmatrix}$
	$\begin{bmatrix} 1 & 0.5 & \dots & 0 \end{bmatrix}$		$\begin{bmatrix} 0 & 1 & \dots & 0 \end{bmatrix}$
	$\begin{bmatrix} 1 & 0 & \dots & 0 \end{bmatrix}$	

Compilation passes



Unitary Decomposition



Janus-CT: Topology-aware quantum circuit optimizer (MICRO 2023)

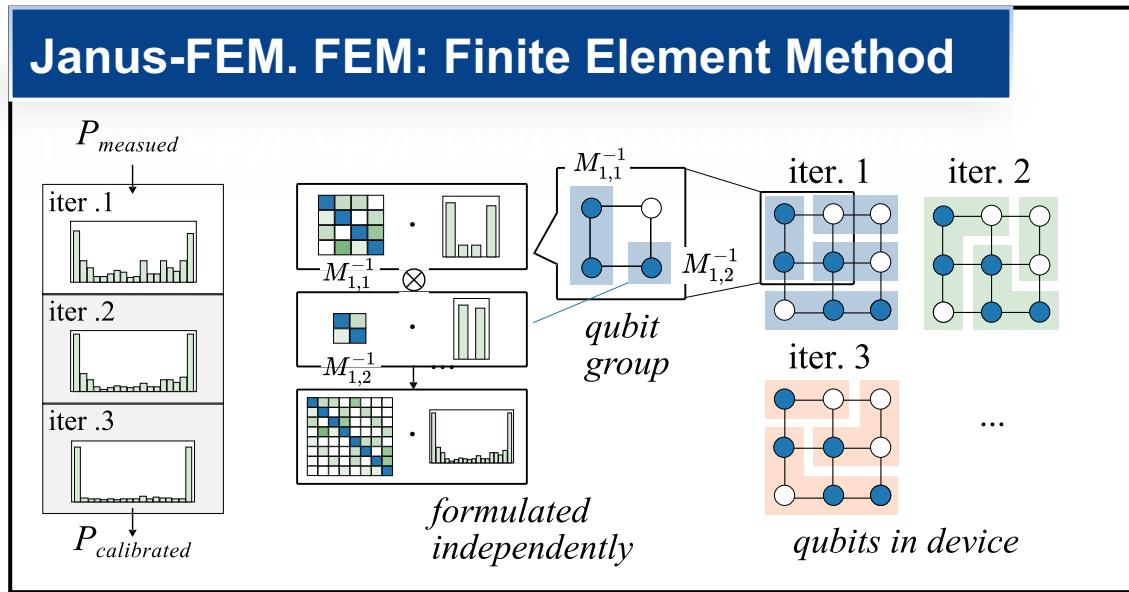
- Why is topological information important in circuit analysis and optimization?
- How do we use topological information to analyze and optimize noise?
- How can we speed up the unitary decomposition with an upstream model?

Related papers: QuCT: A Framework for Analyzing Quantum Circuit by Extracting Contextual and Topological Features. [MICRO 2023]





Janus-FEM. FEM: Finite Element Method

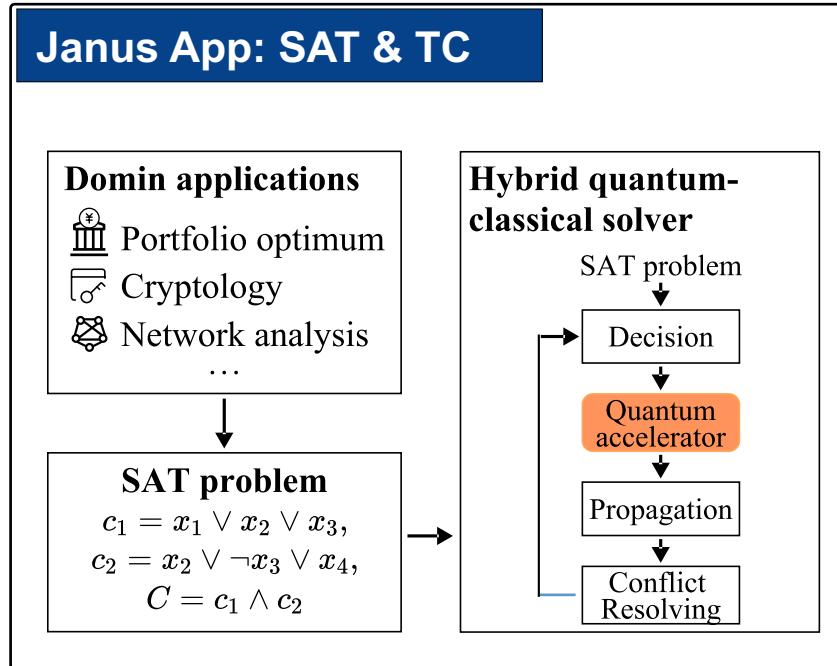


Janus-FEM: Readout Calibration based on finite element method (ASPLOS 2024)

- What is readout error and the difficulty of calibrating it?
- What is the finite element method?
- How do we mitigate the readout noise using the finite element method?

Related papers: QuFEM: Fast and Accurate Quantum Readout Calibration Using the Finite Element Method. [ASPLOS 2024]





Janus-SAT: Hybrid Quantum-Classical SAT solver (HPCA 2023)

- What is the SAT problem? Why is it important?
- How to achieve $4.2\times$ end-to-end speedup compared to the state-of-the-art SAT solver?

Janus-TC: Topological Time Crystal (Nature 2022)

- What is time crystal?
- How can a topological time crystal with high fidelity be simulated on JanusQ Cloud?

Related papers:

HyQSAT: A Hybrid Approach for 3-SAT Problems by Integrating Quantum Annealer with CDCL. [HPCA 2023]
Digital quantum simulation of Floquet symmetry-protected topological phases. [Nature 2022]



Outline



Topic	Presenter	Time
Topic-1. Overview of JanusQ 2.0 and Background of Quantum Computing	Jianwei Yin	0.25 hour
Topic-2. Installing JanusQ 2.0 and Using Janus quantum cloud platform	Siwei Tan	0.75 hour
Topic-3. Janus-CT to optimize (a) Vectorization model using Janus-CT and code examples (b) Fidelity optimization using gate vectors (c) Unitary decomposition using gate vectors (d) Extending the framework by yourself: other downstream tasks!	Liqiang Lu	(1.0 hour) 0.25 hour 0.25 hour 0.25 hour 0.25 hour
Take a break		0.25 hour
Topic-4. Janus-FEM (a) Characterization of readout error (b) Readout calibration using Janus-FEM	Hanyu Zhang	(0.5 hour) 0.25 hour 0.25 hour
Take a break		0.25 hour
Topic-5. Implementing quantum applications (a) Introduction of SAT problem and time crystal (b) End-to-end speedup in domain problems based on quantum SAT solver (c) Simulate time crystal on the Janus quantum platform	Siwei Tan	(1.25 hour) 0.5 hour
Topic-6. Q & A	Siwei Tan	
Total		





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Thanks !

