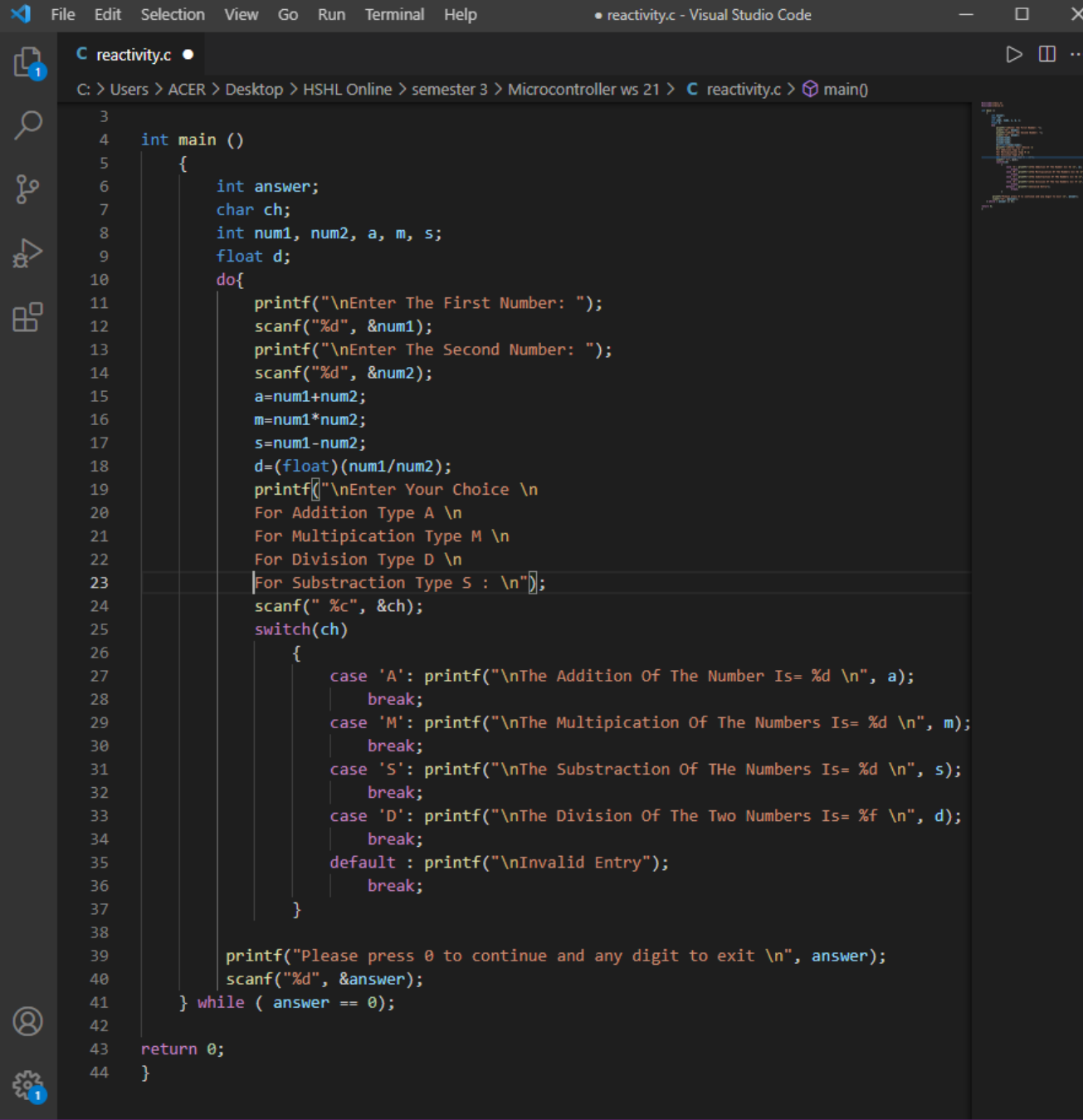


Microcontroller Assignments

01:

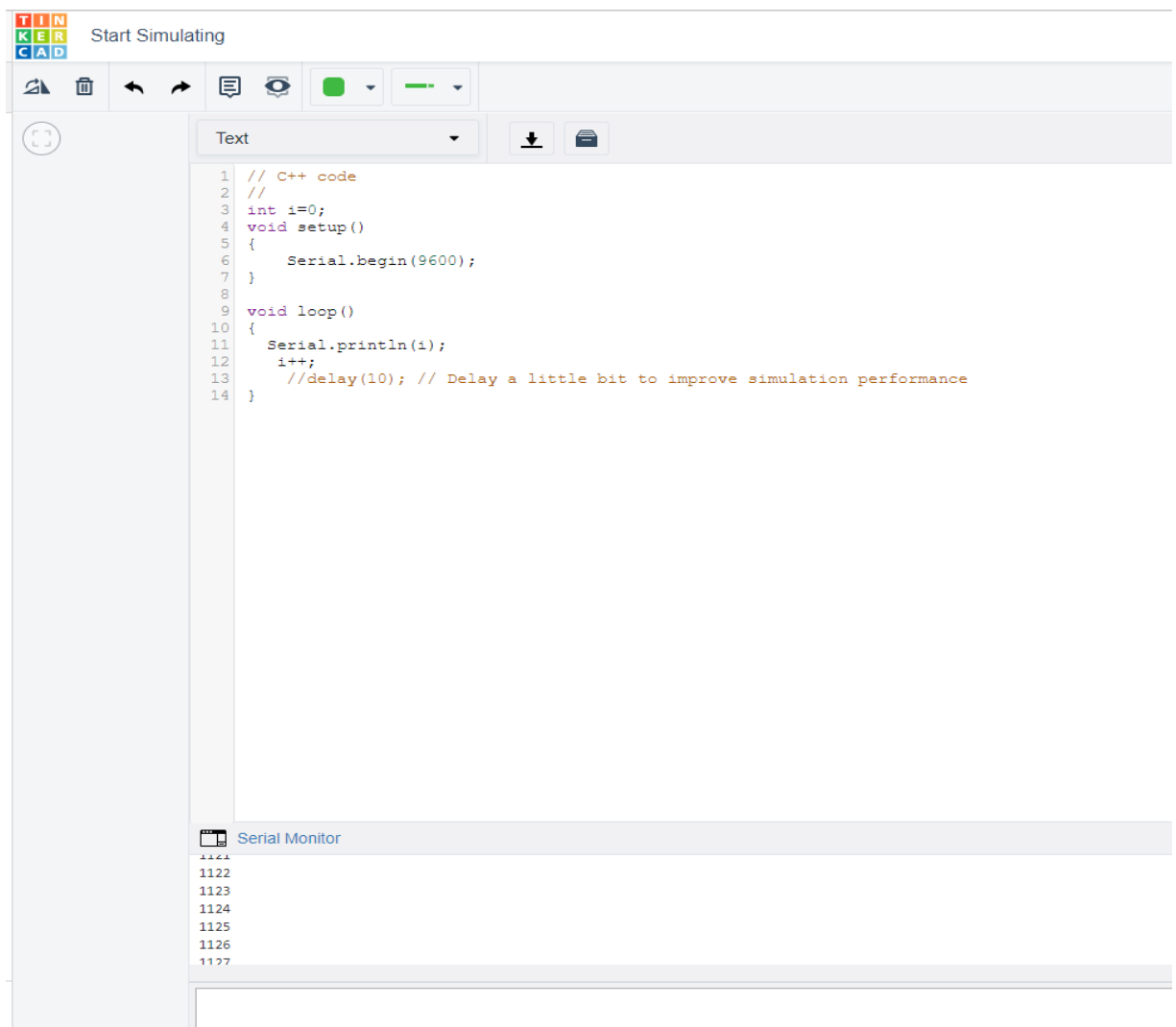
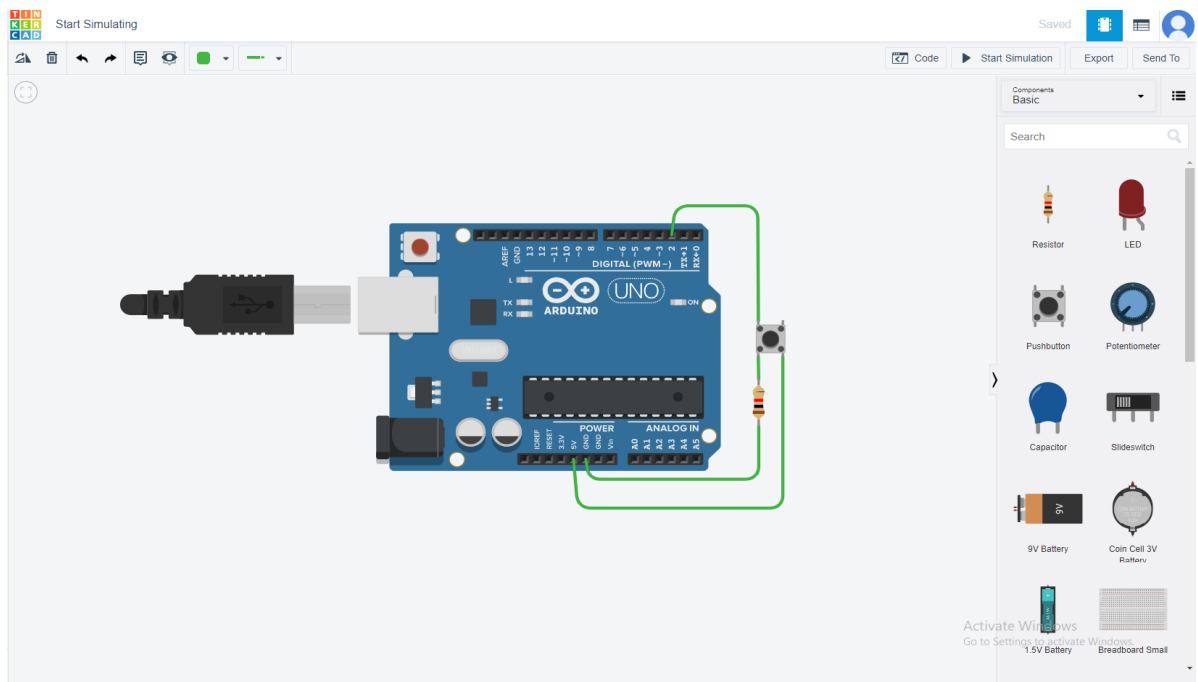
1. Reactive Systems' Implementation:

A. As a C++ Program:



```
3
4  int main ()
5  {
6      int answer;
7      char ch;
8      int num1, num2, a, m, s;
9      float d;
10     do{
11         printf("\nEnter The First Number: ");
12         scanf("%d", &num1);
13         printf("\nEnter The Second Number: ");
14         scanf("%d", &num2);
15         a=num1+num2;
16         m=num1*num2;
17         s=num1-num2;
18         d=(float)(num1/num2);
19         printf("\nEnter Your Choice \n
20         For Addition Type A \n
21         For Multiplication Type M \n
22         For Division Type D \n
23         For Substraction Type S : \n");
24         scanf(" %c", &ch);
25         switch(ch)
26         {
27             case 'A': printf("\nThe Addition Of The Number Is= %d \n", a);
28                         break;
29             case 'M': printf("\nThe Multiplication Of The Numbers Is= %d \n", m);
30                         break;
31             case 'S': printf("\nThe Substraction Of THE Numbers Is= %d \n", s);
32                         break;
33             case 'D': printf("\nThe Division Of The Two Numbers Is= %f \n", d);
34                         break;
35             default : printf("\nInvalid Entry");
36                         break;
37         }
38
39         printf("Please press 0 to continue and any digit to exit \n", answer);
40         scanf("%d", &answer);
41     } while ( answer == 0);
42
43     return 0;
44 }
```

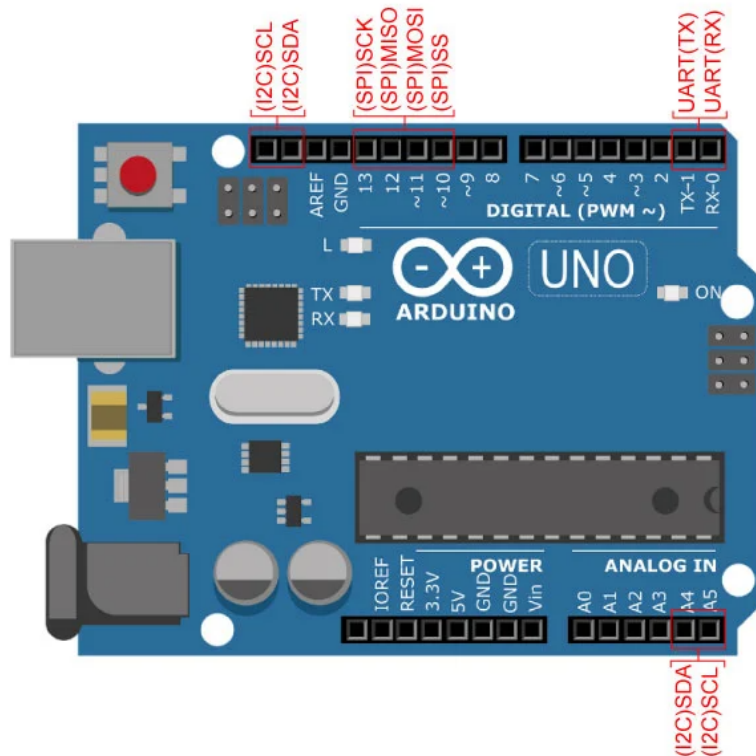
B. As an Arduino Program:



- **Communication protocols:**

There are several communication protocols for arduino and each one has different specifications:

- Universal Asynchronous Reception and Transmission(UART): It allows arduino to communicate with a serial device through a digital pin 0 and a digital pin 1 with another computer through USB port.
- Inter-integrated-circuit (I2C): It is a communication serial protocol dedicated to microcontrollers.
- Serial Peripheral Interface (SPI): It's a serial communication protocol that allows microcontrollers to communicate together.



- **Interrupts:**

An interrupt is a signal transmitted to the microprocessor to stop immediately an actual activity and execute a high priority task.

Interrupts could be:

- ❖ Timer interrupt
- ❖ External interrupt
- ❖ Pin-change interrupt

2. Deadlines

- A deadline is the time at which a specific task should be completed. There is two types of deadlines:
 - ❖ **Hard deadline:** in case of missing it could be catastrophic.
 - ❖ **Soft deadline:** if missed, the still will be functional but there will be a degradation in its quality.

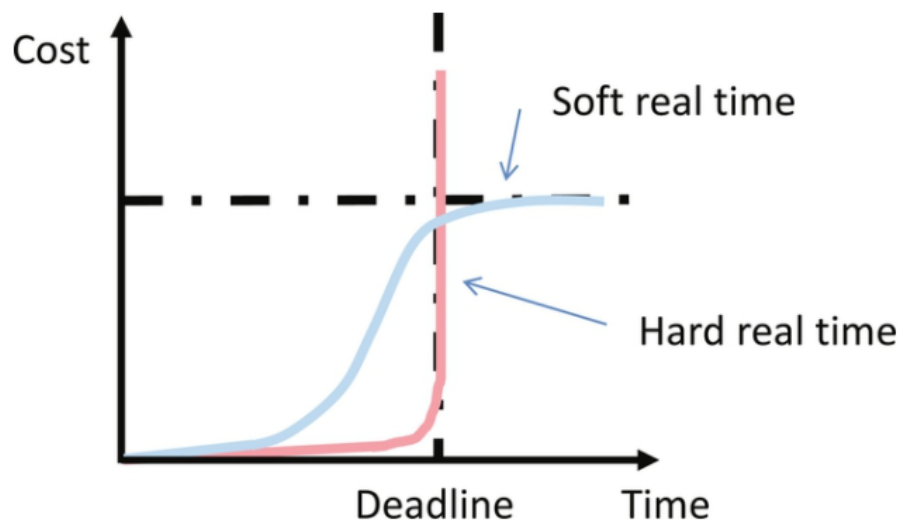


FIG. 10 A comparison between hard and soft real-time systems.

- **Tools and techniques that allows us to meet deadlines:**
 - ❖ **Continuous Testing:**

To make sure that the system is continuously behaving as it is required regardless of the code changes.
 - ❖ **Interrupts:** In order to execute an immediate function with high priority within a specific time.

3. Embedded Systems and HW platforms:

“An embedded system is a specialized computer system that is usually integrated as part of a larger system. An embedded system consists of a combination of hardware

and software components to form a computational engine that will perform a specific function.”

An embedded system performs in an environment that needs to be reactive as well as time-limited. An Embedded system is a combination of hardware (that provides the required performance of the system) and software (which contains the different features that will ensure a flexible use of the system).

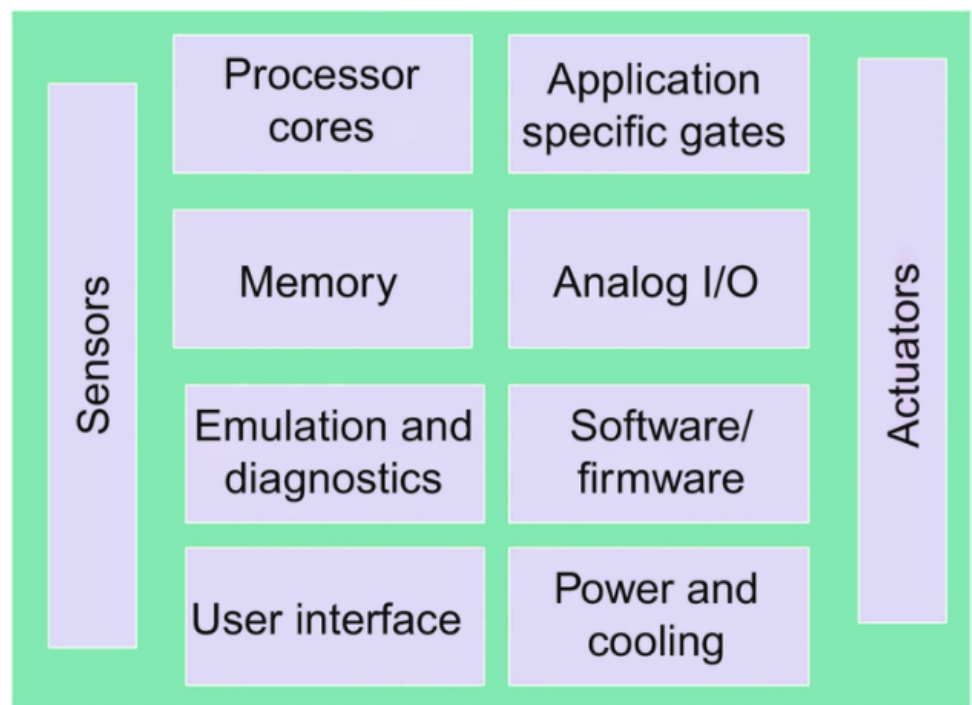
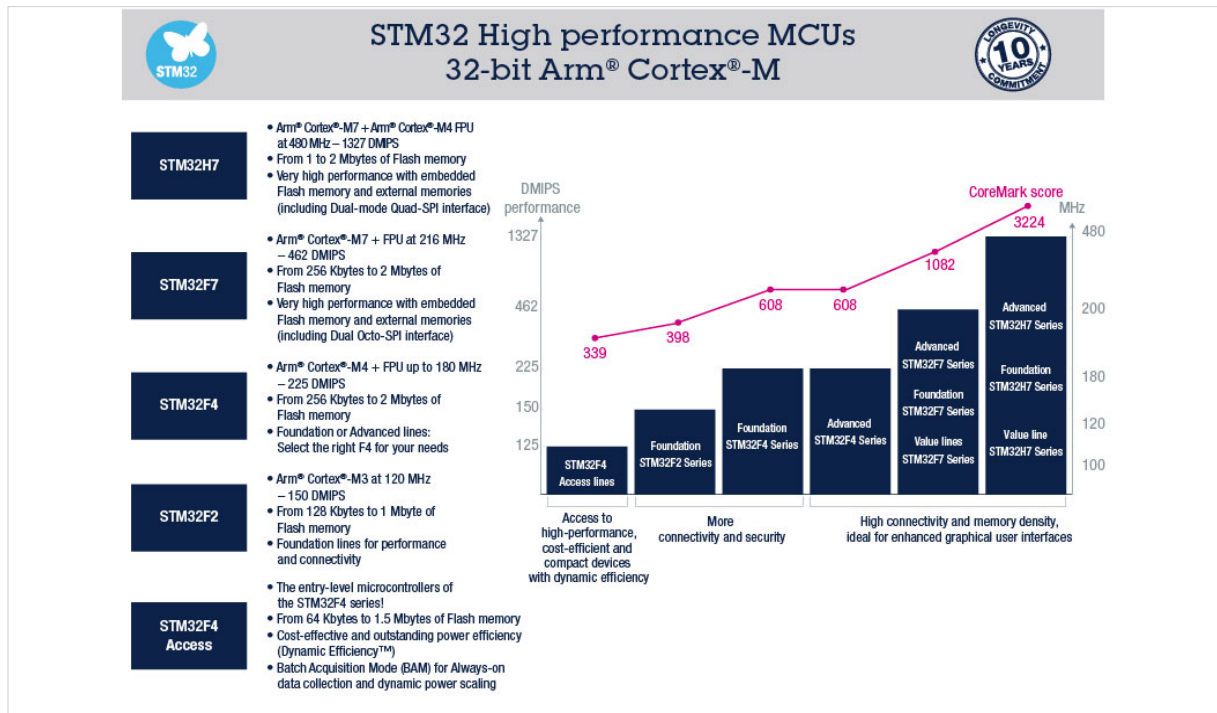


FIG. 5 Components of a typical embedded system.

- **The different characteristics of an embedded system:**
 - Monitoring and reacting to the environment
 - Control the environment.
 - Processing information
 - Application specific.
 - Optimized for the application
 - Resource constrained
 - Real time.
 - Multirate
- **Hw Platforms:**
 - **STM32 Family:**
 - **STM32 High Performance MCUs:**
 - STM32F Series
 - STM32F4 series

■ STM32F2 series



➤ STM32 Mainstream MCUs:

- STM32F0 Series
- STM32G0 Series
- STM32F1 Series
- STM32F3 Series
- STM32G4 Series



STM32 Mainstream MCUs 32-bit Arm® Cortex®-M



STM32G4	<ul style="list-style-type: none"> Arm Cortex-M4 + FPU at 170 MHz – 213 DMIPS From 32 to 512 Kbytes of Flash memory Mixed-signal MCUs with ART, CCM SRAM, Mathematical accelerator, high-res timer
STM32F3	<ul style="list-style-type: none"> Arm Cortex-M4 + FPU at 72 MHz – 90 DMIPS From 16 to 512 Kbytes of Flash memory Mixed-signal MCUs with CCM SRAM, 16-bit sigma-delta ADCs, high-res timer
STM32F1	<ul style="list-style-type: none"> Arm Cortex-M3 at 72 MHz – 61 DMIPS From 16 Kbytes to 1 Mbyte of Flash memory STM32 Foundation line
STM32G0	<ul style="list-style-type: none"> Arm Cortex-M0+ at 64 MHz – 59 DMIPS From 16 to 512 Kbytes of Flash memory Entry-level MCU for cost-sensitive applications, first 8-pin STM32 High integration with 2.5 MSPS ADC, 2xcpu timers, rich connectivity, USB-C Power Delivery and maximum RAM
STM32F0	<ul style="list-style-type: none"> Arm Cortex-M0 at 48 MHz – 38 DMIPS From 16 to 256 Kbytes of Flash memory Entry-level MCU for cost-sensitive applications Rich connectivity with crystal-less USB 2.0 and CAN bus interface



➤ STM32 Arm Cortex MPUs:

■ STM32MP1 Series

STM32 MPUs

32-bit Arm® Cortex®-A & -M

STM32 Learning

	STM32MP151D	STM32MP151F	STM32MP153D	STM32MP153F	STM32MP157D	STM32MP157F
	1520 + 260 DMIPS 800 MHz Cortex-A7 209 MHz Cortex-M4	- - - Security	3040 + 260 DMIPS 800 MHz 2x Cortex-A7 209 MHz Cortex-M4 CAN FD	- - - Security	3040 + 260 DMIPS 800 MHz 2x Cortex-A7 209 MHz Cortex-M4 CAN FD - 3D GPU - DSI	- - - Security
	1235 + 260 DMIPS 650 MHz Cortex-A7 209 MHz Cortex-M4	- - - Security	2470 + 260 DMIPS 650 MHz 2x Cortex-A7 209 MHz Cortex-M4 CAN FD	- - - Security	2470 + 260 DMIPS 650 MHz 2x Cortex-A7 209 MHz Cortex-M4 CAN FD - 3D GPU - DSI	- - - Security

Arm® Cortex® core
Cortex-A7 + Cortex-M4
Dual Cortex-A7 + Cortex-M4

STM32 MPU Wiki


STM32 GitHub

STM32 MPU Community

STM32 Education


➤ STM8 8-bit MCUs:

- STM8S, mainstream MCUs
- STM8L, ultra-low-power MCUs
- STM8AF and STM8AL, automotive MCUs









STM8 8-bit MCUs

Core up to 24 MHz




STM8 Ecosystem

 Mainstream	Industrial, consumer and mass market	Robust and reliable Up to 125 °C	STM8S Data EEPROM, 3 and 5 V families, precise RC	<div style="background-color: #002060; color: white; text-align: center; padding: 5px;">  Software tools </div> <div style="border: 1px solid #002060; padding: 2px; margin-bottom: 2px;">STM8CubeMX Configuration tool</div> <div style="border: 1px solid #002060; padding: 2px; margin-bottom: 2px;">Integrated Development Environments (IDE)</div> <div style="border: 1px solid #002060; padding: 2px; margin-bottom: 2px;">STM Studio Monitoring tool</div> <div style="background-color: #002060; color: white; text-align: center; padding: 2px;">▶ More software tools</div>
 Ultra-low-power	Ideal combination of low-power performance and features	High-end analog IPs Active Halt < 1 µA	STM8L Data EEPROM, 1.65 and 3 V families, strong analog, LCD drivers, low-leakage technology	
 Automotive	Long-term guarantee	AEC-Q100 Up to 150 °C	STM8AF Data EEPROM, 3 and 5 V families, precise RC, LIN, CAN, grade 0	
	Long-term guarantee	AEC-Q100 Up to 125 °C	STM8AL Data EEPROM, 1.65 and 3 V families, strong analog, LCD drivers, low-leakage technology	



Join the STM8 Community!

<http://community.st.com/stm8>


 **Embedded software**

Standard Peripheral Library for STM8L (8kb)

Standard Peripheral Library for STM8L/AL (64kb)

Standard Peripheral Library for STM8A/S

▶ More embedded software

 **Hardware tools**

STM8 Discovery kits, Nucleo and evaluation boards

ST-LINK in-circuit debugger/programmer

4. Continuous real world example to a discrete example : ?

02:

1. Fulfilling Embedded Systems Characteristics:

- **Reactive systems**

Continuously interacting with the environment ,Non terminating,
Stimulus/response

interaction (must be able to respond to interrupts)

- **Real-time systems**

Means the response of a Real-Time system must take place within a specific time frame.

- **Continuous/discrete/hybrid systems**

A discrete system ,in which the state variable changes only at a discrete set of points in time.

A continuous system ,in which the state variable changes continuously over time.

Hybrid: Continuous and discrete elements are valid.

- **Embedded systems**

It is a software embedded into technical system interact with physical components (Actuators, sensors) to control specific hardware.

- **Dependable systems**

Can rely on the system in two aspects, that it performs according to its service specification and that the system avoids hazards.

- **Distributed systems**

It contains multiple nodes that are physically separate but linked together using the network

2. Attributes of dependability:

- **reliability**

Reliability is the probability that the system will conform to its specification throughout a period of duration t .

- **Availability**

The availability of a system is the probability that the system will be functioning correctly at any given time.

if a system is unavailable it is not delivering the specified system services.

- **Safety**

Safety is a property of a system that will not endanger human life or the environment.

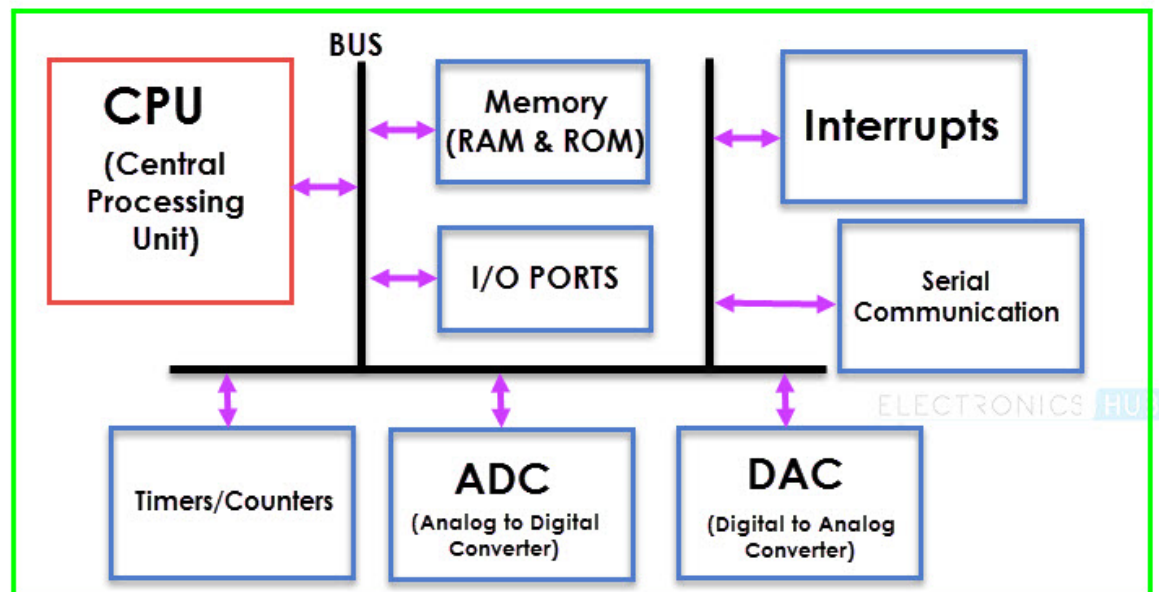
- **Security**

Prevention of or protection against

- access to information by unauthorized recipients or
- intentional but unauthorized destruction or alteration of that information.

- dependability with respect to prevention of unauthorized access and/or handling of information
- The ability of the system to protect itself against accidental or deliberate intrusion
- Security is an essential prerequisite for availability, reliability and safety, If a system is a networked system and is insecure then it's unreliable.

3. Elements of Microcontroller:

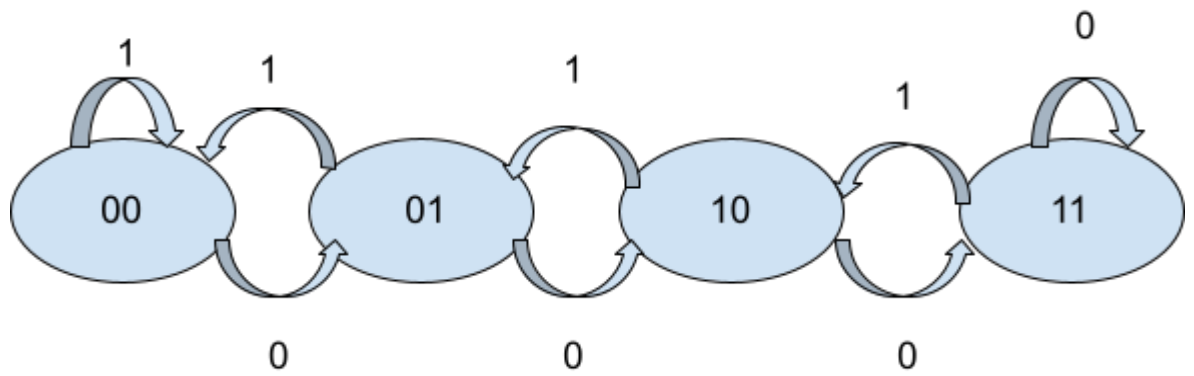


4. Typical processors for microcontrollers:

- **ARM Based Processors:**
 - ARMv7-M (Cortex-M3): 2^{32} , 32-bit RISC, 1,25 DMIPS/MHZ
 - ARMv8-A (Apple's iPhone/ Cortex-A): 64 bit, 1,5-2,5 GHZ

Cortex Families: Cortex-A (for complex computations), Cortex-M (low-cost embedded market), Cortex-R (High performance)

5. 2-bit predictor:



E (Event): 1 (taken)/ 0 (not taken)

State 00 & 11: Strongly predicted

State 01 & 10: Weakly predicted

I1	I2	E	O1	O2
0	0	0	0	0
0	0	1	0	1
0	1	0	0	0
0	1	1	1	0
1	0	0	0	1
1	0	1	1	1
1	1	0	1	0
1	1	1	1	1

- **C program:**

```
#include <stdio.h>
#define stronglyTaken 100
#define weaklytaken 101
#define weaklyNotTaken 110
#define stronglyNotTaken 111
#define taken 1
#define notTaken 0

int event, answer;

int state = stronglyTaken;
int main ()
{
    int event, answer;
    for (;;)
    {
        printf("choose event: 0 for taken and 1 for not taken \n\n", event, taken,
notTaken);
        scanf("%d", &event);
        switch (state)
        {
            case stronglyTaken:
                if (event == taken)

                    state = stronglyTaken;
                else
                    if (event == notTaken)
                        state = weaklytaken;

                break;
            case weaklytaken:
                if (event == taken)

                else
                    if (event == notTaken)
                        state = weaklyNotTaken;

                break;
            case weaklyNotTaken:
                if (event == taken)

                state = weaklytaken;
            else
                if (event == notTaken)
                    state = stronglyNotTaken;
```

```

break;
case stronglyNotTaken:
if (event == taken)

else
    if (event == notTaken)
        state = stronglyNotTaken;

break;

default:
    break;
}
printf (" The new state is = %d \t", state);
printf ("\n\n");
}

return 0;
}

```

● **Circuit:**

$$Q_1 = \bar{I}_1 \cdot I_2 \cdot E + I_1 \cdot \bar{I}_2 \cdot E + I_1 \cdot I_2 \cdot \bar{E} + I_1 \cdot I_2 \cdot E$$

$$Q_2 = \bar{I}_1 \cdot \bar{I}_2 \cdot E + I_1 \cdot \bar{I}_2 \cdot \bar{E} + I_1 \cdot \bar{I}_2 \cdot E + I_1 \cdot I_2 \cdot E$$

Q_1 K-map

	$I_1 I_2$ 00	01	10	11
E 0	0	0	0	1
E 1	0	1	1	1

$Q_1 = E + I_1 I_2$

Q_2 K-map

	$I_1 I_2$ 00	01	10	11
E 0	0	0	1	0
E 1	1	0	1	1

$Q_2 = I_1 \cdot \bar{I}_2 + E$

03:

Waterfall Model:

1. Requirements:

The KOA team needs to meet the customer in order to define the different requirements of the project and document them in a requirement specification document.

2. Program Design:

Study the requirement specifications and prepare a design of the system. During the phase KOA team will define the overall architecture of the system.

3. Implementation:

By using the output of the previous step the team will divide the system into small programs. Each developed unit will be tested for its functionality.

4. Integration and Testing:

Integrate all the developed units into a system, then test the entire system to detect failures if they exist.

5. Deployment of system:

Once the entire testing process is done (function/ non-functional testing). The system is deployed in the other company's environment.

6. Maintenance:

Enhance the system for better versions and fix issues once they appear. These changes need to be delivered to the customer.

V model:

1. Requirements Analysis:

Define the exact requirements and prepare the acceptance test design planning.

2. System Design:

Design the complete system.

3. Architecture Design:

Understand and design the architectural specifications. Break down the system design into modules according to the different functionalities (High Level Design).

4. Module Design:

Specify the detailed internal design for all the modules of the system (Low Level Design).

5. Coding:

Develop the different modules after choosing the suitable programming language.

6. Unit Testing:

Test at the code level and eliminate bugs.

7. Integration Testing:

Test the coexistence and communication of the internal modules within the system.

8. System Testing:

Test the entire system functionality and the communication of the system under development with external systems.

9. Acceptance Testing:

Test the product in the other company's environment.

Additional phases of V model:

- **Architecture Design**
- **Module Design**
- **Unit testing**
- **Integration Testing**
- **Acceptance Testing**

The advantage of V model:

In the V model defects are identified during the early stages because the development cycle is directly associated with a testing phase.

Process Models Assessment:

Model	Size of team	Complexity of the project	Known Requirements	Change Of Requirement	Time to market	Knowledge of IT	Average number of iteration
Waterfall	Small -Mid	Low-Mid	Known	Stable	Fast -Mid	No	0
V model	Mid	Mid	Known	Stable	Mid	No	0
Agile	Small	Low-Mid	-----	Unstable	Fast	Yes	Many
RUP	Big	High	Unknown	Unstable	Slow	No	Many
Spiral model	Big	High	Unknown	Unstable	Slow	No	Many

04:

Interrupt:

Arduino supports different Interrupts.

The function is: `attachInterrupt()`. The first parameter is the interrupt number using: `digitalPinToInterrupt(pin)`.

- Syntax of Interrupt:
`attachInterrupt(digitalPinToInterrupt(pin), ISR, mode):` recommended.
or
`attachInterrupt(pin, ISR, mode)`
- Parameters:
 - Interrupt: the number of the interrupt. Allowed data types: `int`
 - pin: the Arduino pin number.
 - ISR: to call when the interrupt occurs; this function must take no parameters and return nothing. This function is sometimes referred to as an interrupt service routine.
 - Mode: defines when the interrupt should be triggered. Four constants are predefined as valid values:
 - `LOW` to trigger the interrupt whenever the pin is low,
 - `CHANGE` to trigger the interrupt whenever the pin changes value
 - `RISING` to trigger when the pin goes from low to high,
 - `FALLING` for when the pin goes from high to low.The Due, Zero and MKR1000 boards allow also:
 - `HIGH` to trigger the interrupt whenever the pin is high
- Implementation:

<https://www.tinkercad.com/things/dBtfgvA2hiM>