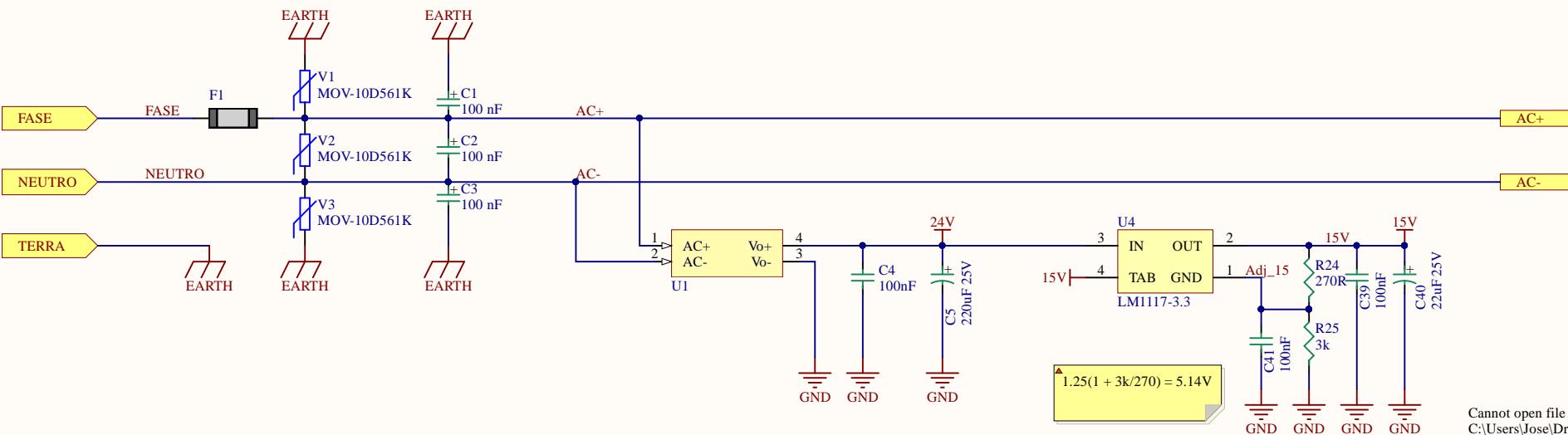


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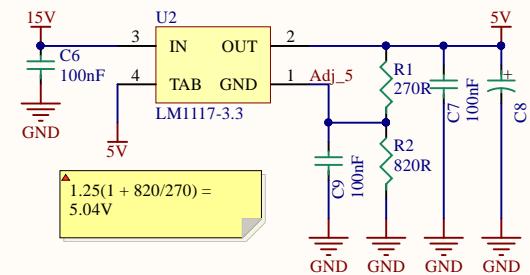
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B

B



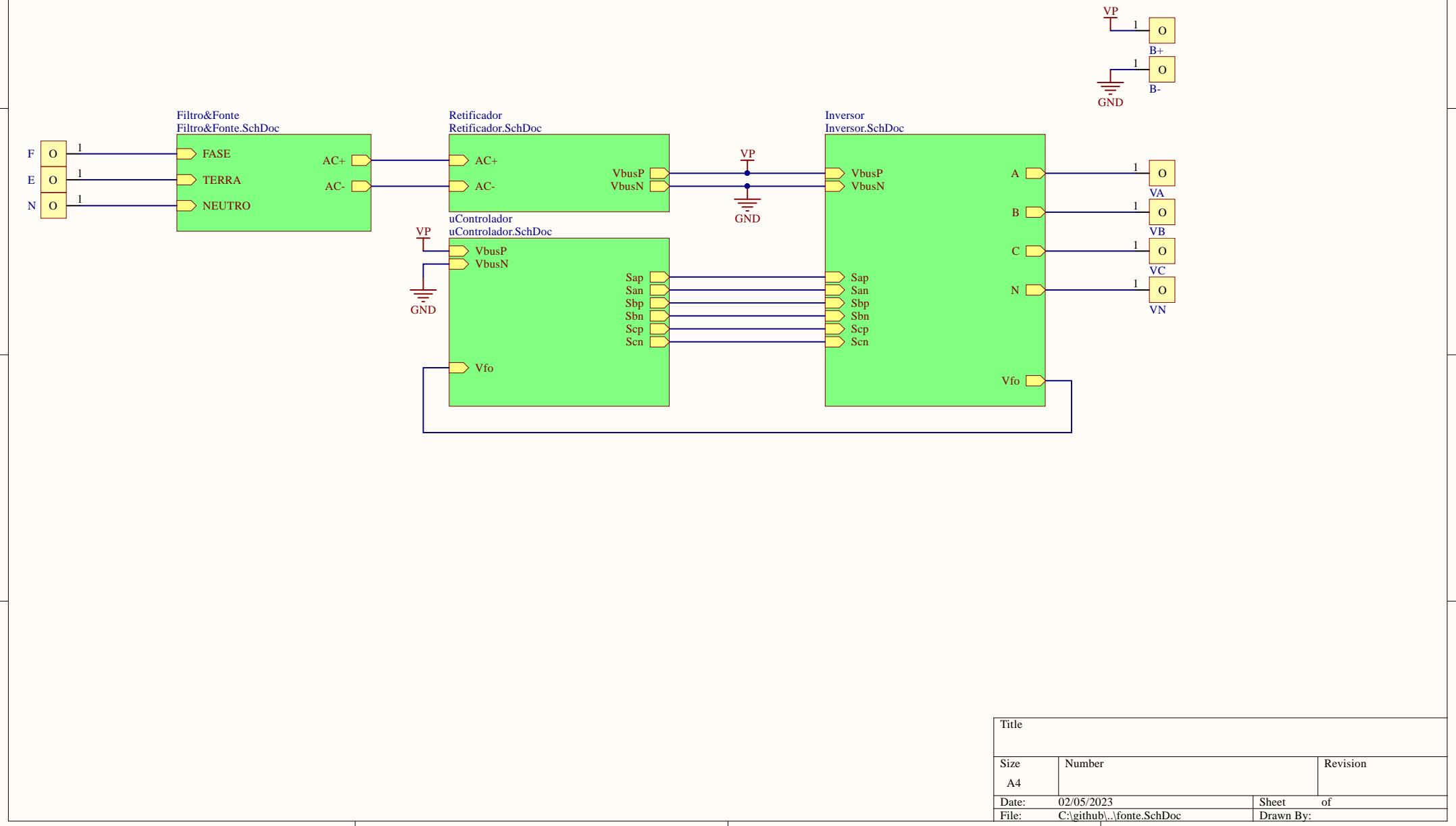
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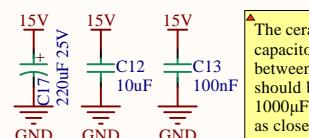
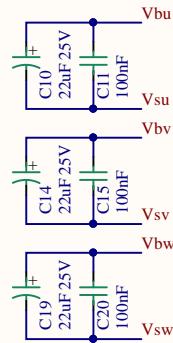
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The ceramic bootstrap capacitor placed between VCC - COM should be over 22 μ F + 100nF and mounted as close to the pins

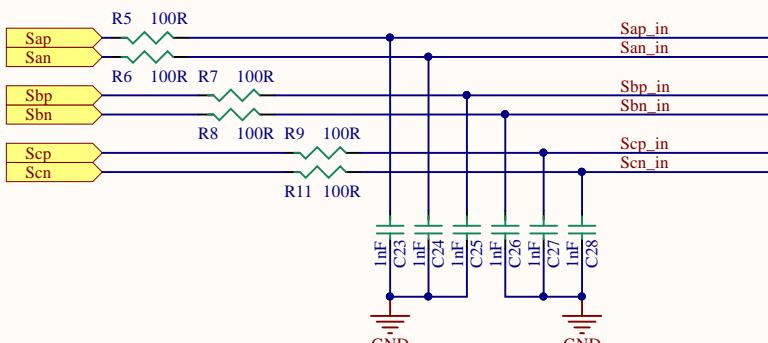


The ceramic capacitor placed between VCC - COM should be over 1 μ F + 1000 μ F and mounted as close to the pins

To prevent surge destruction, the wiring between the smoothing capacitor and the P & GND pins should be as short as possible. The use of a high-frequency non-inductive capacitor of around 0.1 - 0.22 μ F between the P & GND pins is recommended.



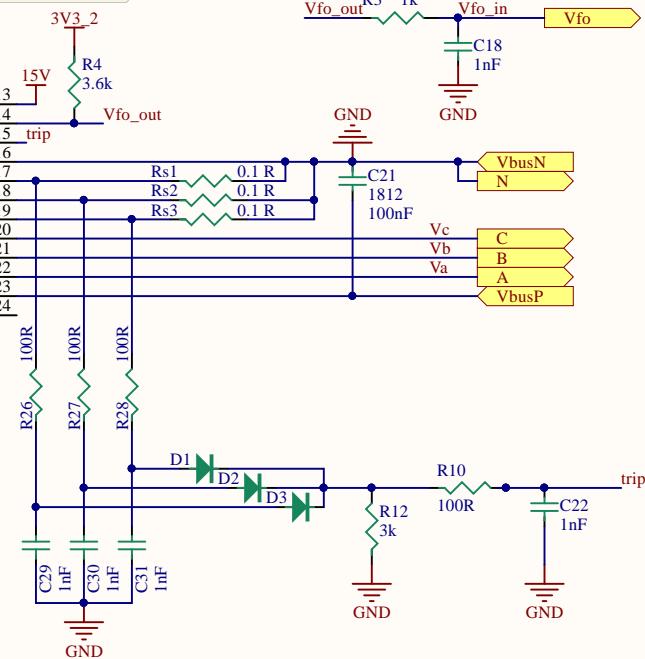
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IC1

Vsu	1	VDD
Vbu	2	VB(U)
Vsv	3	VFO
Vbv	4	VS(V)
Vsw	5	VB(V)
Vbw	6	VSS
Vsw	7	NW
Sap_in	8	NV
Sbp_in	9	HIN(U)
Scp_in	10	HIN(V)
San_in	11	HIN(W)
Sbn_in	12	LIN(U)
Scn_in		LIN(V)
		LIN(W)
		NU
		W
		V
		U
		P
		NC

IKCM15F60GA



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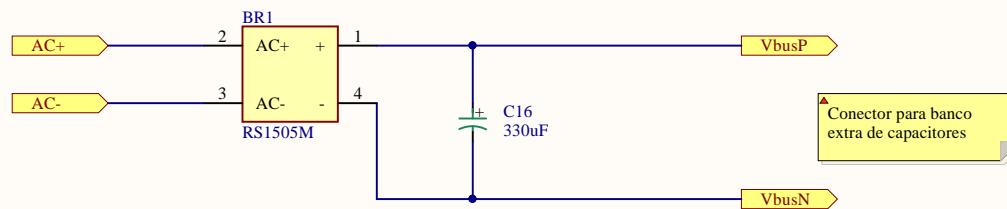
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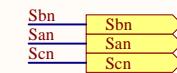
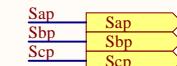
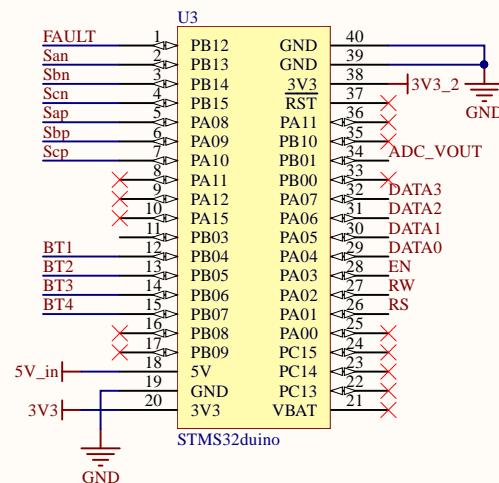
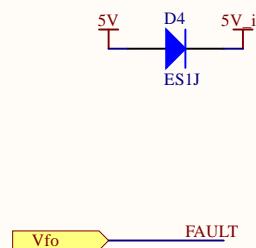
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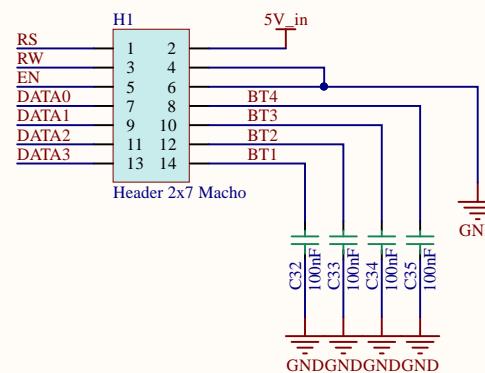
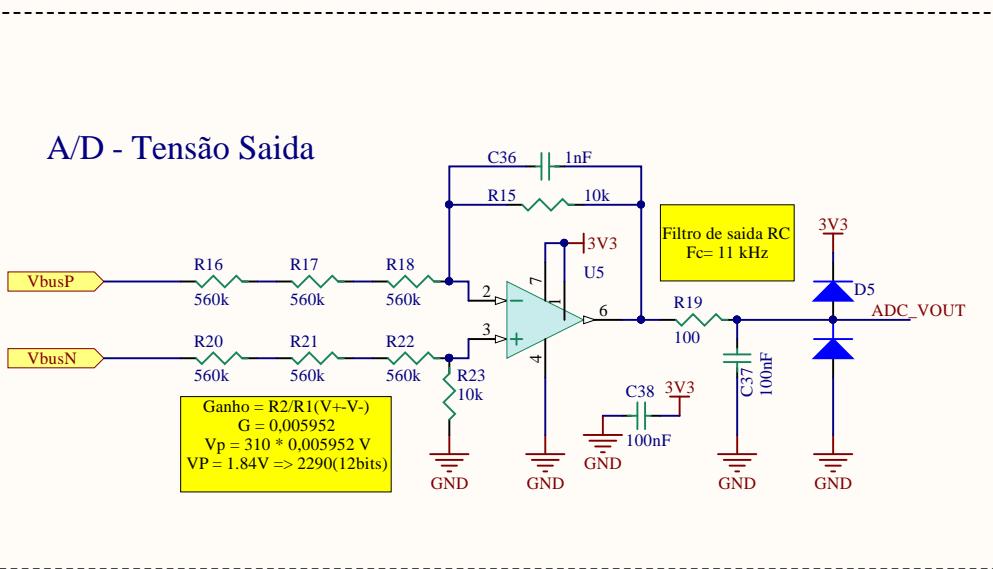


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A/D - Tensão Saída



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