JK - RS - D FlipFlops
Wave Form Diagrams

A

B

CLK

A —o S   Q

B —o R $\overline{Q}$

assume starts here

A — J   Q

—o CLK

B — K $\overline{Q}$

D — D   Q

clk — $\overline{Q}$

PS

D

A B C D E F G

Clock

CLR

D   Q

clk $\overline{Q}$
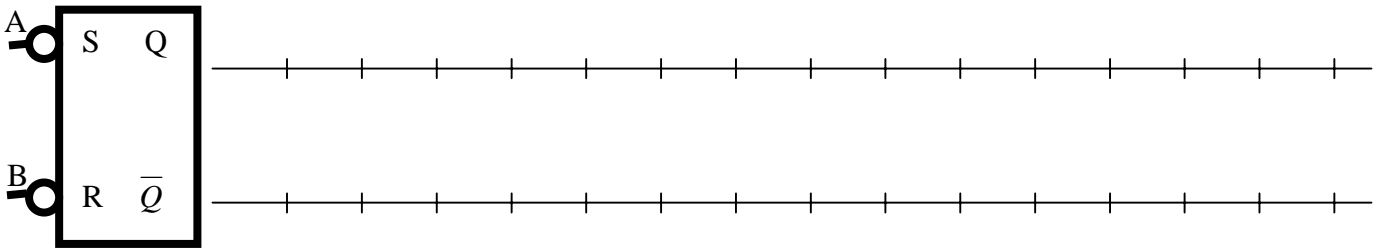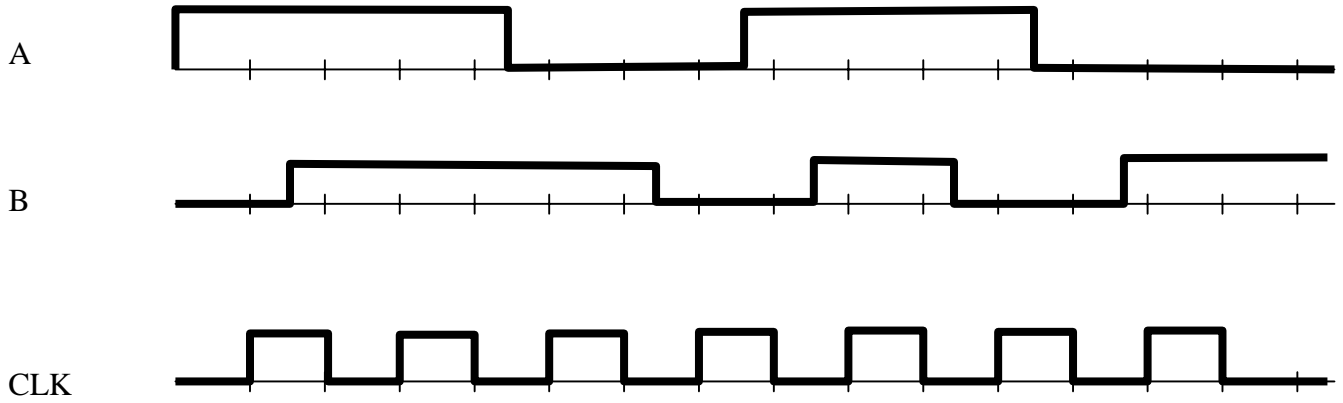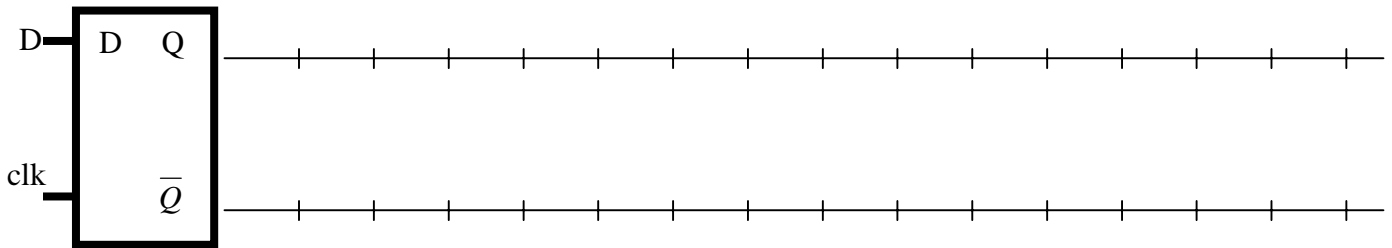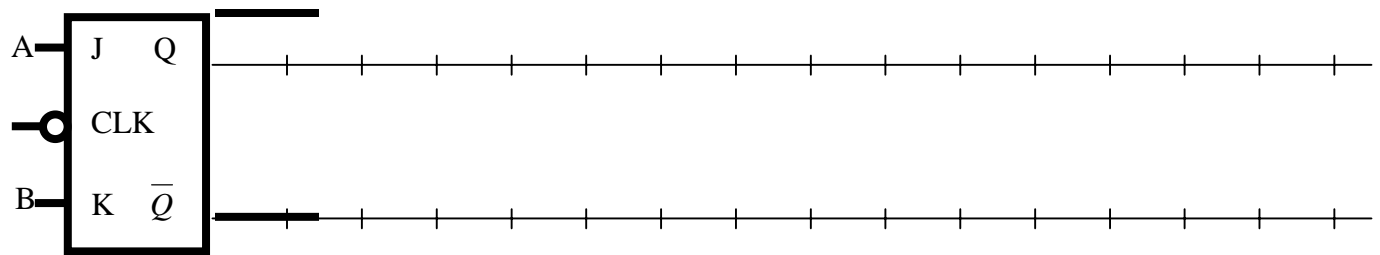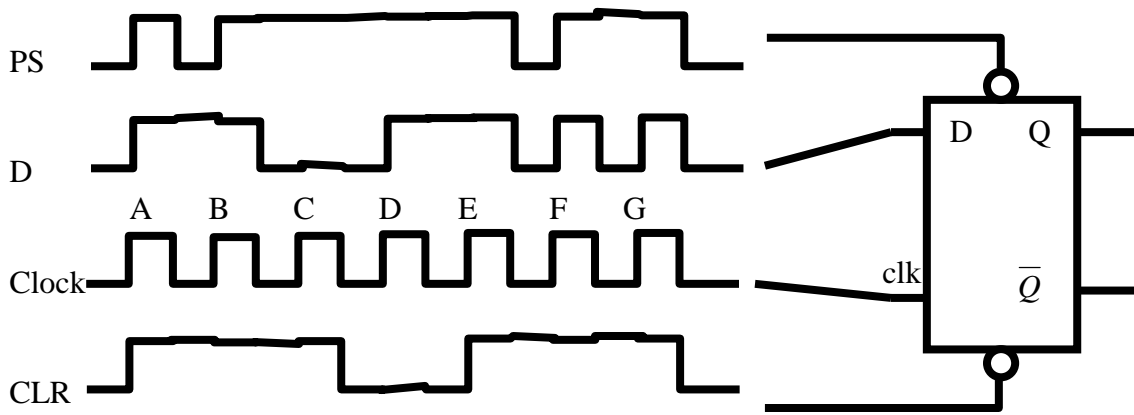
For each pulse of the clock give the logical value (0 or 1) of both Q and $\overline{Q}$. Also state the mode of operation.

Pulse	Q	$\overline{Q}$	Mode
A			
B			
C			
D			
E			
F			
G			

Complete the truth table for the following logic symbol.

| Mode of Operation | INPUTS | | | | | OUTPUTS | |
| | Asynchronous | | Synchronous | | | | |
	PS	CLR	CLK	J	K	Q	$\overline{Q}$