

The diagram is a control flow graph (CFG) for the LLVM compiler, showing various basic blocks and their transitions. The graph starts with a 'start' block and ends with 'llvm TargetLowering SimplifyDemandedBits'. It includes many blocks with names like 'Builder BuildFile', 'Worker CreateDefault', 'EquivalenceClass Add', and 'llvm LiveRegMatrix assign'. Each block contains statistics such as '0 (0.0%) of 20 (100.0%)'.

```

graph TD
    start["start  
0 (0.0%)  
of 20 (100.0%)"] -- 20 --> _lib_start_main["_lib_start_main  
0 (0.0%)  
of 20 (100.0%)"]
    _lib_start_main -- 20 --> main["main  
0 (0.0%)  
of 20 (100.0%)"]
    main -- 19 --> Worker_CreateDefault["Worker  
CreateDefault  
0 (0.0%)  
of 19 (95.0%)"]
    main -- 1 --> Builder_BuildFile["Builder  
BuildFile  
0 (0.0%)  
of 1 (5.0%)"]
    Worker_CreateDefault -- 13 --> Lws_RestOfStack["Lws  
RestOfStack  
0 (0.0%)  
of 13 (100.0%)"]
    Worker_CreateDefault -- 4 --> ModuleAppend["ModuleAppend  
outLwsEnd  
0 (0.0%)  
of 4 (100.0%)"]
    Builder_BuildFile -- 2 --> Worker_Create["Worker  
Create  
0 (0.0%)  
of 2 (100.0%)"]
    Builder_BuildFile -- 1 --> Worker_Mutate["Worker  
Mutate  
0 (0.0%)  
of 1 (5.0%)"]
    Lws_RestOfStack -- 13 --> Lws_AddBasis["Lws  
AddBasis  
0 (0.0%)  
of 13 (100.0%)"]
    ModuleAppend -- 4 --> PackageLoad["PackageLoad  
LoadEndBasis  
0 (0.0%)  
of 4 (100.0%)"]
    Worker_Create -- 2 --> Worker_Load["Worker  
Load  
0 (0.0%)  
of 2 (100.0%)"]
    Worker_Mutate -- 1 --> RefCodeGen_Mutate["RefCodeGen  
Mutate  
0 (0.0%)  
of 1 (5.0%)"]
    Lws_AddBasis -- 13 --> BuilderWorker_AddBasis["BuilderWorker  
AddBasis  
0 (0.0%)  
of 13 (100.0%)"]
    PackageLoad -- 4 --> EtlPackage_Load["EtlPackage  
Load  
0 (0.0%)  
of 4 (100.0%)"]
    Worker_Load -- 2 --> Lws_Load["Lws  
Load  
0 (0.0%)  
of 2 (100.0%)"]
    RefCodeGen_Mutate -- 1 --> RefCodeGen_MakeModule["RefCodeGen  
MakeModule  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_AddBasis -- 13 --> BuilderWorker_Regions["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Load -- 4 --> EtlPackage_Disassemble["EtlPackage  
Disassemble  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load -- 2 --> Lws_Load2["Lws  
Load  
0 (0.0%)  
of 2 (100.0%)"]
    RefCodeGen_MakeModule -- 1 --> RefCodeGen_verifyModuleAndWrite["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions -- 13 --> BuilderWorker_Regions2["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Disassemble -- 4 --> EtlPackage_Write["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load2 -- 1 --> Lws_Load3["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite -- 1 --> RefCodeGen_verifyModuleAndWrite2["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions2 -- 13 --> BuilderWorker_Regions3["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write -- 4 --> EtlPackage_Write2["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load3 -- 1 --> Lws_Load4["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite2 -- 1 --> RefCodeGen_verifyModuleAndWrite3["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions3 -- 13 --> BuilderWorker_Regions4["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write2 -- 4 --> EtlPackage_Write3["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load4 -- 1 --> Lws_Load5["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite3 -- 1 --> RefCodeGen_verifyModuleAndWrite4["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions4 -- 13 --> BuilderWorker_Regions5["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write3 -- 4 --> EtlPackage_Write4["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load5 -- 1 --> Lws_Load6["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite4 -- 1 --> RefCodeGen_verifyModuleAndWrite5["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions5 -- 13 --> BuilderWorker_Regions6["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write4 -- 4 --> EtlPackage_Write5["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load6 -- 1 --> Lws_Load7["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite5 -- 1 --> RefCodeGen_verifyModuleAndWrite6["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions6 -- 13 --> BuilderWorker_Regions7["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write5 -- 4 --> EtlPackage_Write6["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load7 -- 1 --> Lws_Load8["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite6 -- 1 --> RefCodeGen_verifyModuleAndWrite7["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions7 -- 13 --> BuilderWorker_Regions8["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write6 -- 4 --> EtlPackage_Write7["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load8 -- 1 --> Lws_Load9["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite7 -- 1 --> RefCodeGen_verifyModuleAndWrite8["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions8 -- 13 --> BuilderWorker_Regions9["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write7 -- 4 --> EtlPackage_Write8["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load9 -- 1 --> Lws_Load10["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite8 -- 1 --> RefCodeGen_verifyModuleAndWrite9["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions9 -- 13 --> BuilderWorker_Regions10["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write8 -- 4 --> EtlPackage_Write9["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load10 -- 1 --> Lws_Load11["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite9 -- 1 --> RefCodeGen_verifyModuleAndWrite10["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions10 -- 13 --> BuilderWorker_Regions11["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write9 -- 4 --> EtlPackage_Write10["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load11 -- 1 --> Lws_Load12["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite10 -- 1 --> RefCodeGen_verifyModuleAndWrite11["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions11 -- 13 --> BuilderWorker_Regions12["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write10 -- 4 --> EtlPackage_Write11["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load12 -- 1 --> Lws_Load13["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite11 -- 1 --> RefCodeGen_verifyModuleAndWrite12["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions12 -- 13 --> BuilderWorker_Regions13["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write11 -- 4 --> EtlPackage_Write12["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load13 -- 1 --> Lws_Load14["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite12 -- 1 --> RefCodeGen_verifyModuleAndWrite13["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions13 -- 13 --> BuilderWorker_Regions14["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write12 -- 4 --> EtlPackage_Write13["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load14 -- 1 --> Lws_Load15["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite13 -- 1 --> RefCodeGen_verifyModuleAndWrite14["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions14 -- 13 --> BuilderWorker_Regions15["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write13 -- 4 --> EtlPackage_Write14["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load15 -- 1 --> Lws_Load16["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite14 -- 1 --> RefCodeGen_verifyModuleAndWrite15["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]
    BuilderWorker_Regions15 -- 13 --> BuilderWorker_Regions16["BuilderWorker  
Regions  
0 (0.0%)  
of 13 (100.0%)"]
    EtlPackage_Write14 -- 4 --> EtlPackage_Write15["EtlPackage  
Write  
0 (0.0%)  
of 4 (100.0%)"]
    Lws_Load16 -- 1 --> Lws_Load17["Lws  
Load  
0 (0.0%)  
of 1 (5.0%)"]
    RefCodeGen_verifyModuleAndWrite15 -- 1 --> RefCodeGen_verifyModuleAndWrite16["RefCodeGen  
verifyModuleAndWrite  
0 (0.0%)  
of 1 (5.0%)"]

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