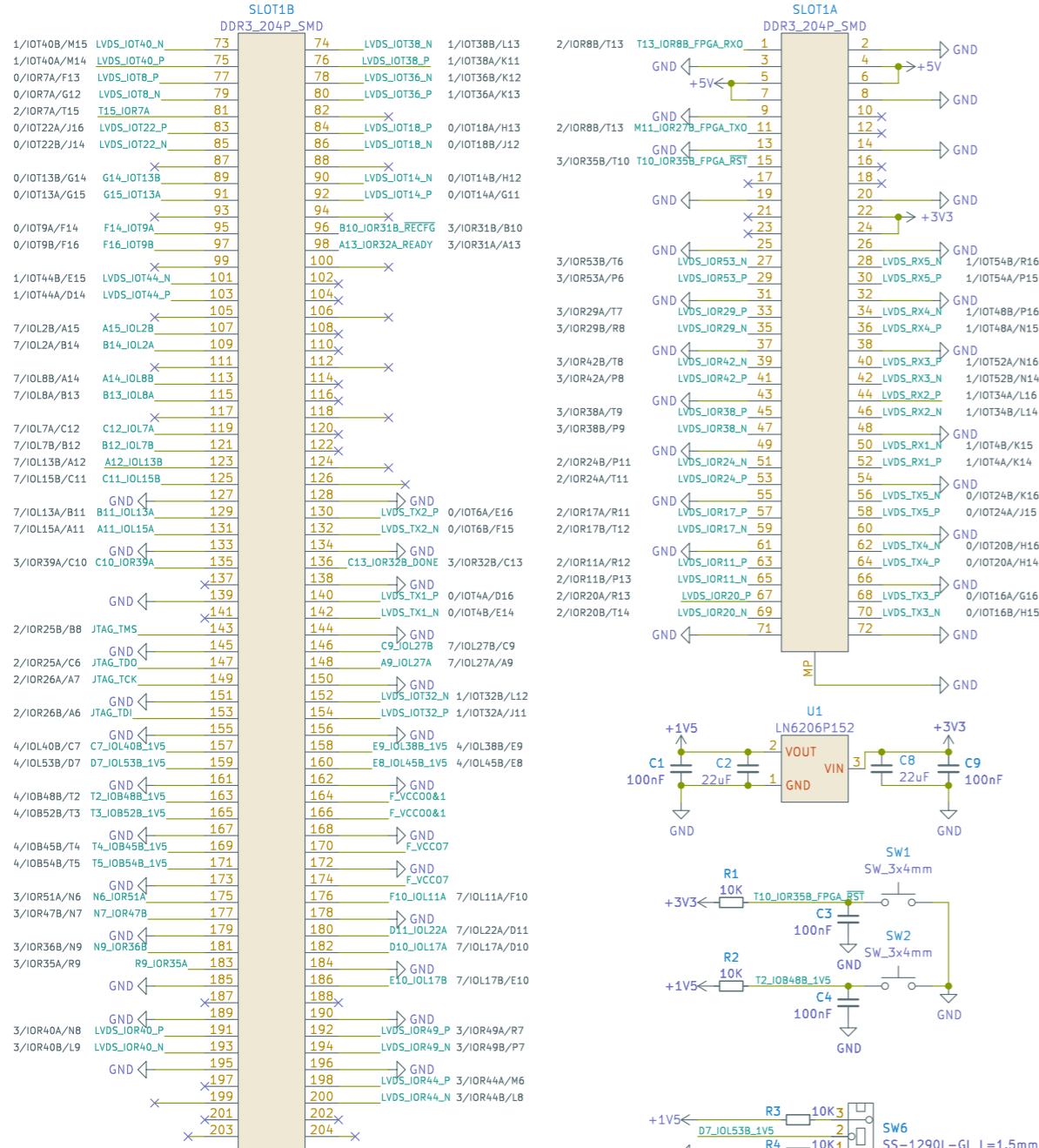
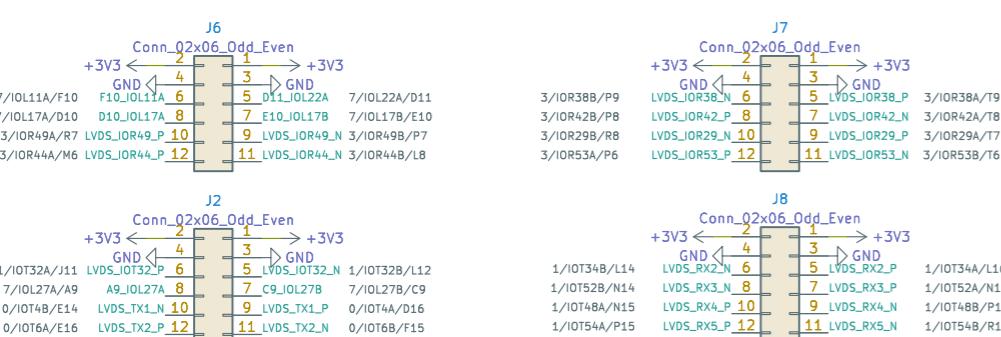


204P DDR3 SODIMM CONNECTOR



H1 MountingHole H3 MountingHole

H2 MountingHole H4 MountingHole



Vcc Bank0/1 = 3.3V

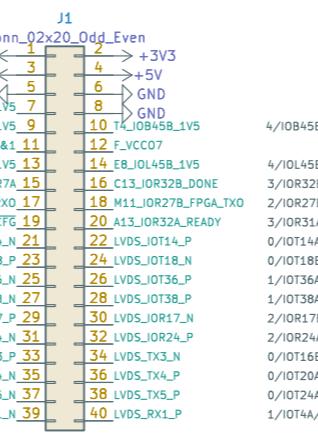
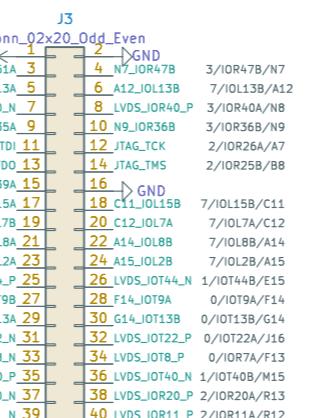
Vcc Bank2 = 3.3V

Vcc Bank2 = 3.3V

Vcc Bank3 = 3.3V

Vcc Bank4/5/6 = 1.5V

Vcc Bank7 = 3.3V



SiPEED

Sheet: / File: Tang_Primer_20K_Dock_Lite_3711.kicad_sch Title: Tang_Primer_20K_Dock_Lite_Schematic

Size: A3 Date: 2024-06-18
KiCad E.D.A. 8.0.3

Rev: 1.2
Id: 1/1