

Flip-flops & Registers 2

J-K flip-flop: Same as S-R flip-flop, but has toggle feature. Toggle feature inverts the J & K values. In S-R flip-flop both high means flip-flop is not used. Both high on J & K becomes toggle.

Clock inputted flip-flop have edge-triggered. Rising edge / positive edge means when input goes from low to high event happens. Falling edge / negative edge means when input goes from high to low event happens.

Edge-triggered allows events to happen at a precise instant of time.

Flip flops with master slave variety.

These flip flops have 2 latches. A master S-R latch & a slave S-R latch.

Clock waveform

