

## Revision History

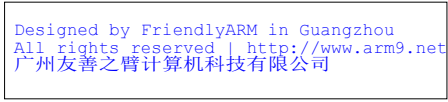
Revision	Date	Comments
1311	2013-11	初次版本

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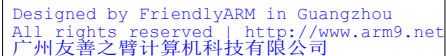
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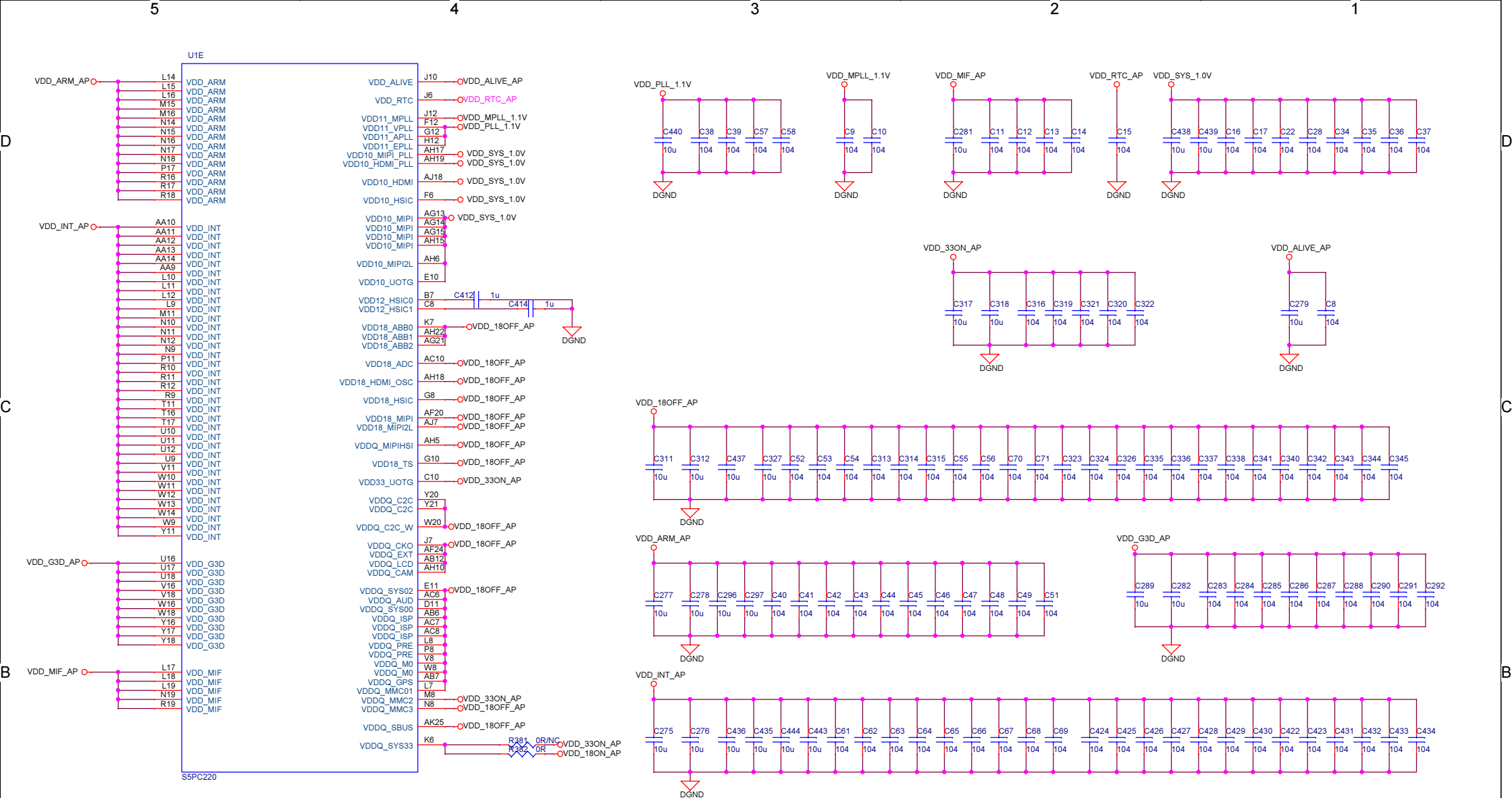
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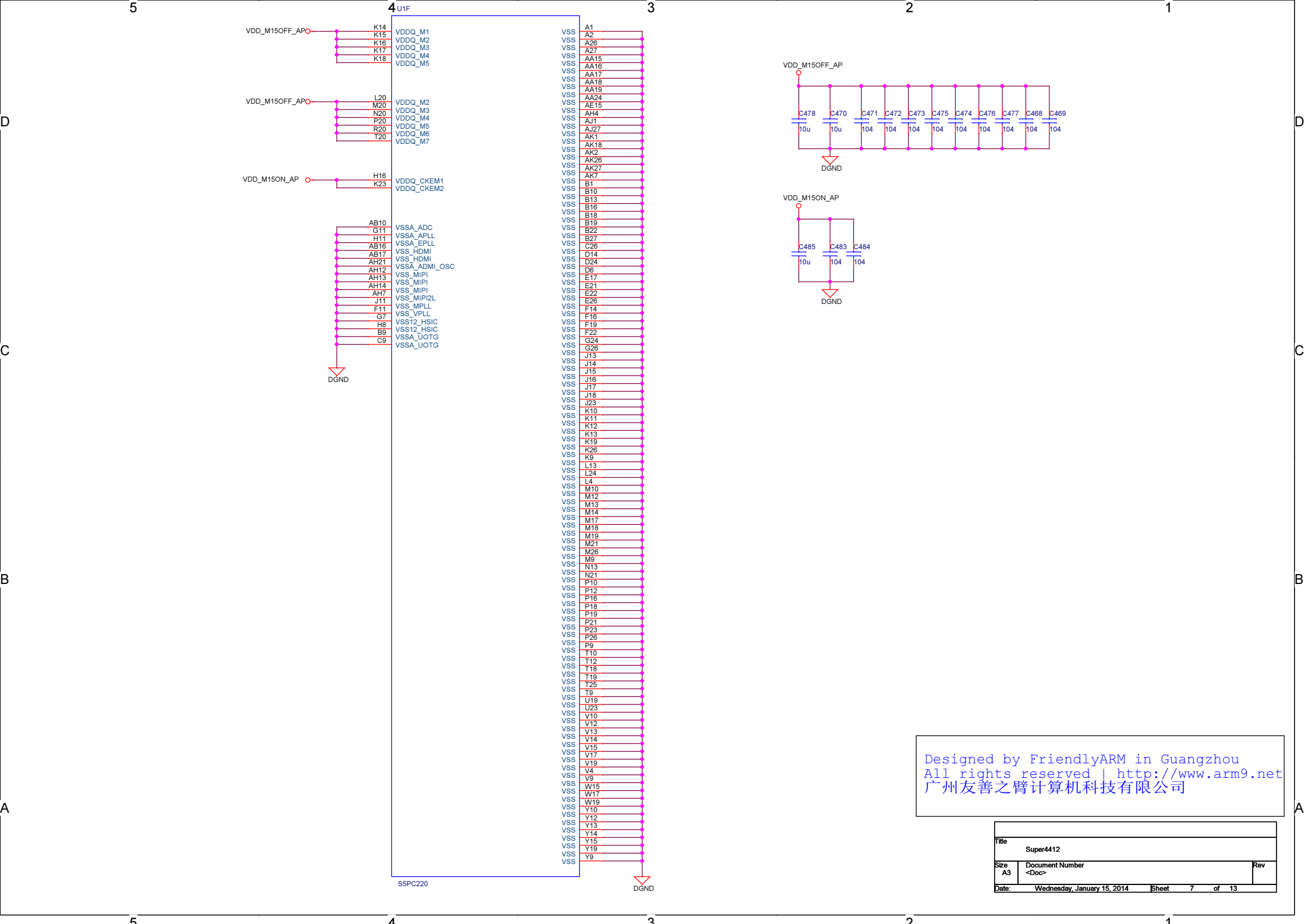


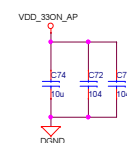




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D

C

B

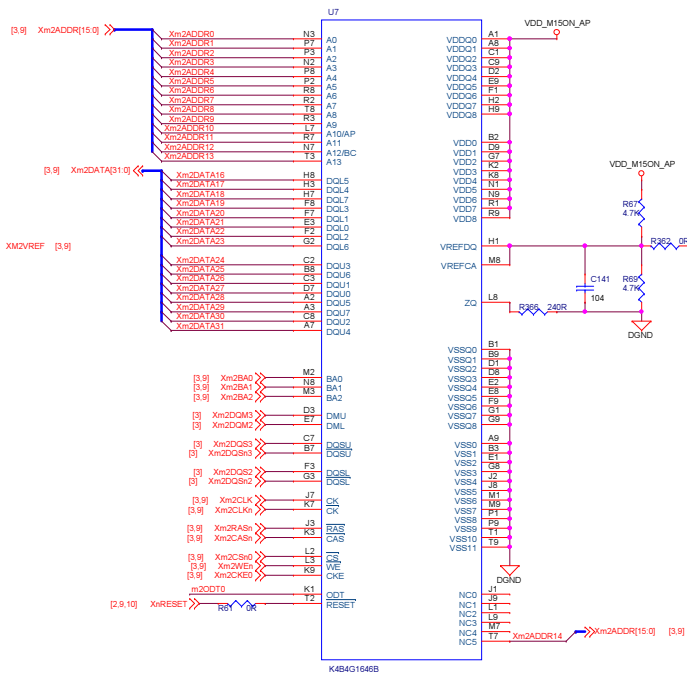
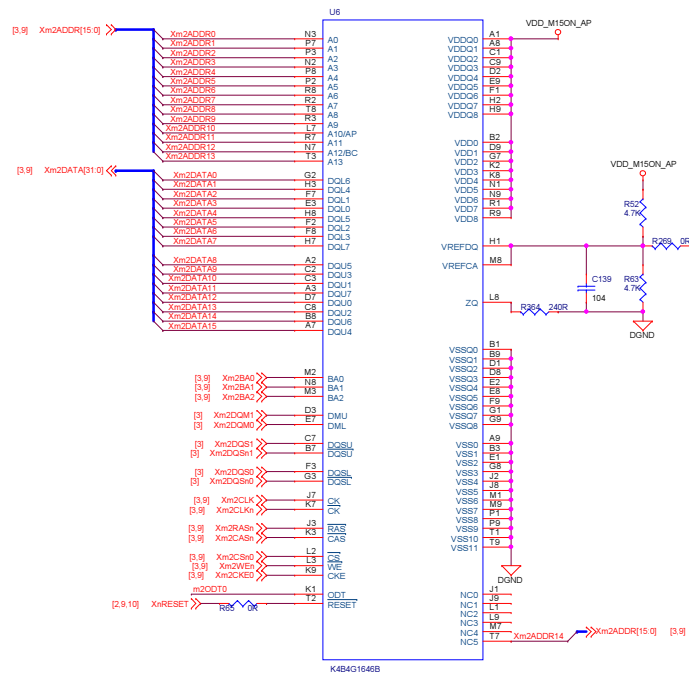
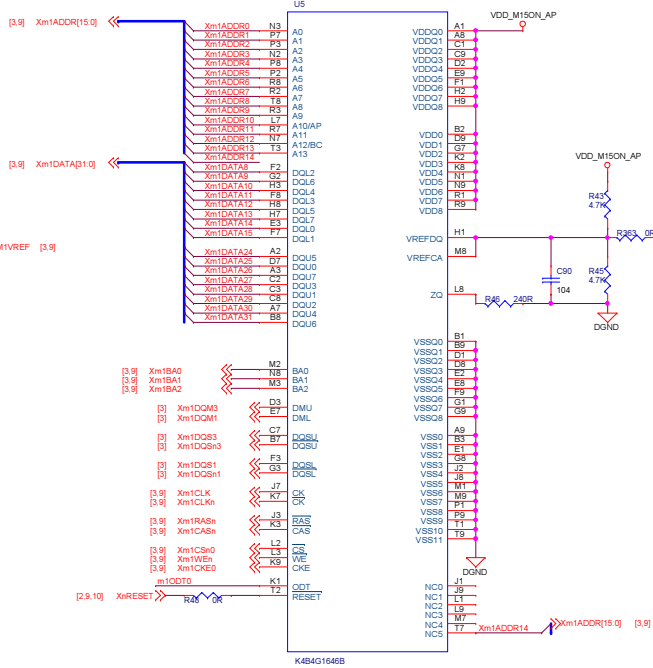
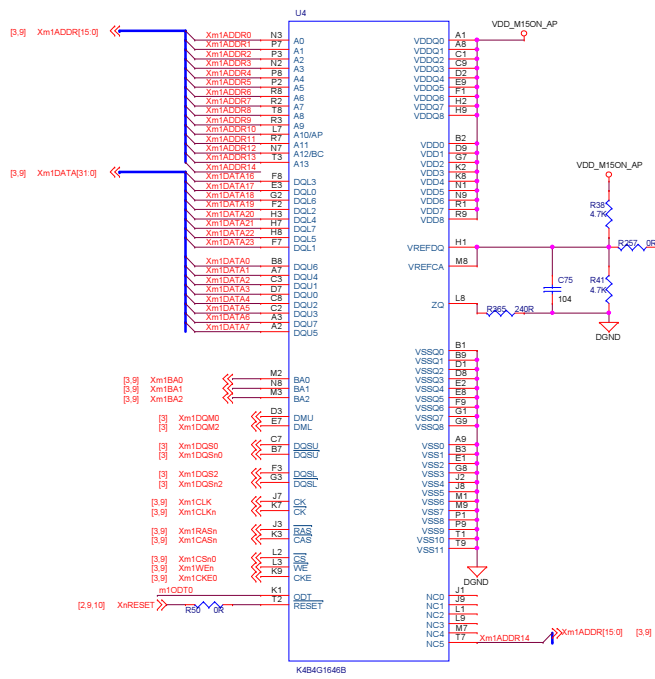
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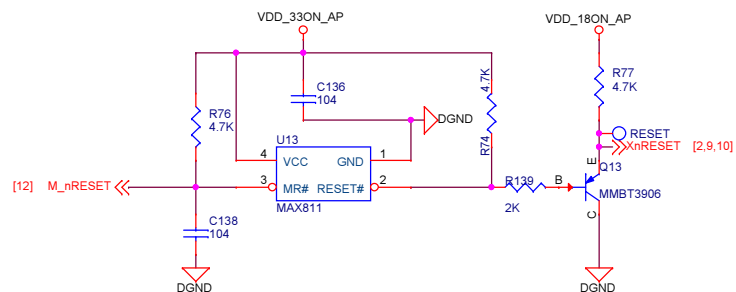
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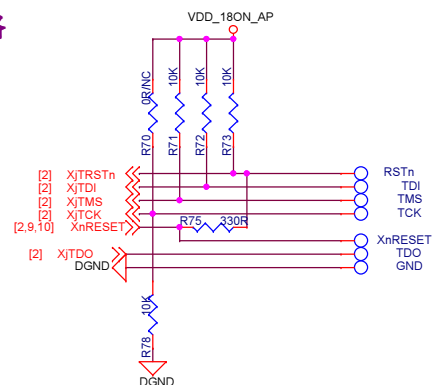
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## 复位电路



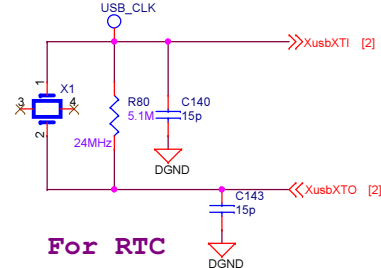
## JTAG电路



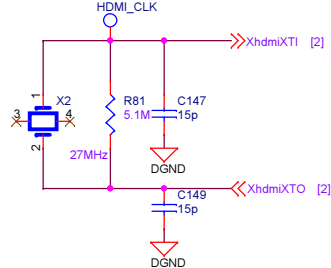
## For PLL



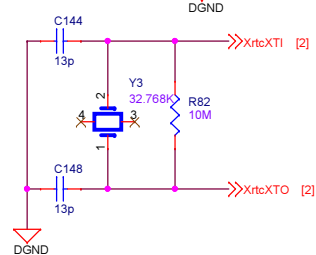
## For USB HOST



## For VEDIO



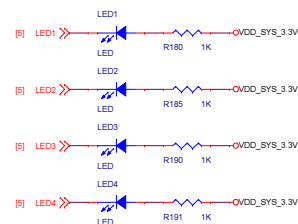
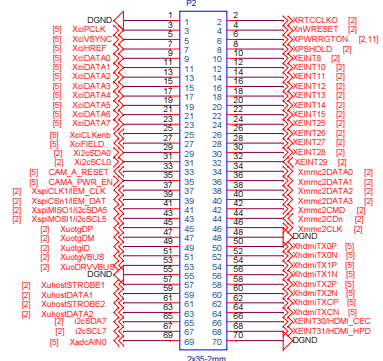
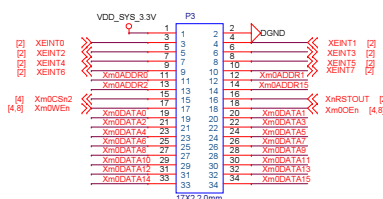
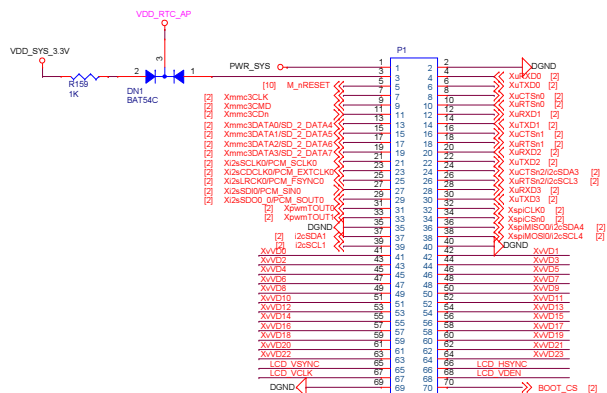
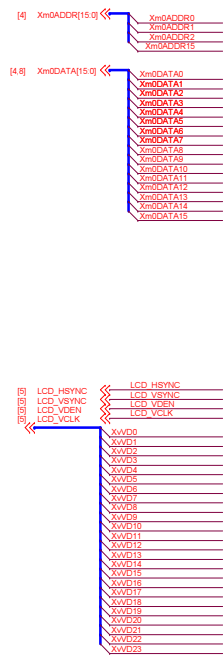
## For RTC



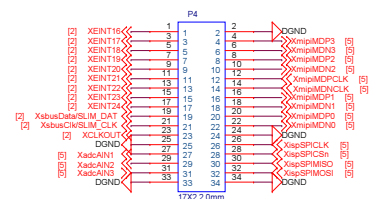
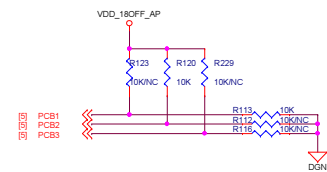
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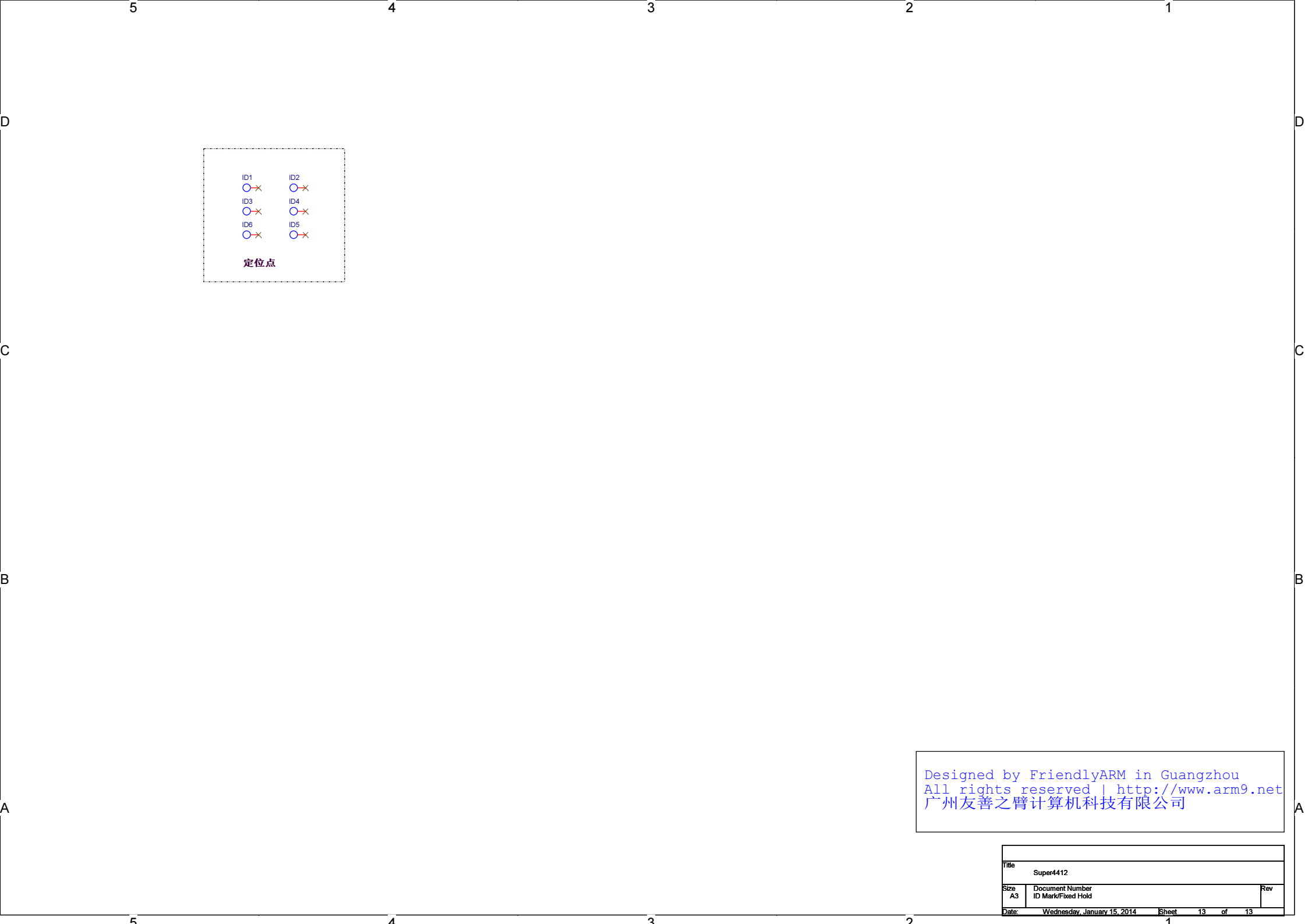




## PCB 版本控制



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ID1

ID2

ID3

ID4

ID6

ID5

定位点

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