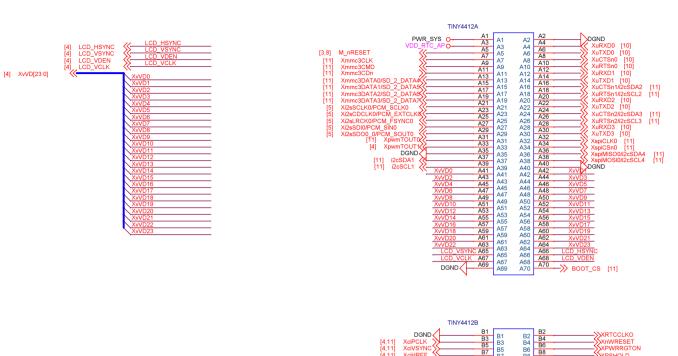
# Tiny4412ADK 1312B

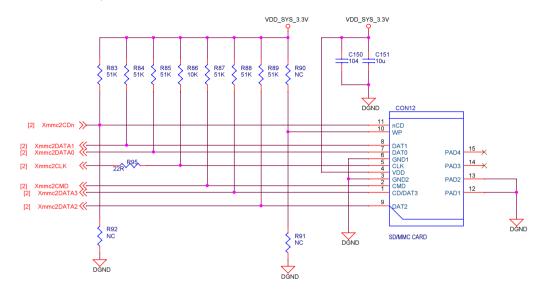
FriendlyARM http://www.arm9.net

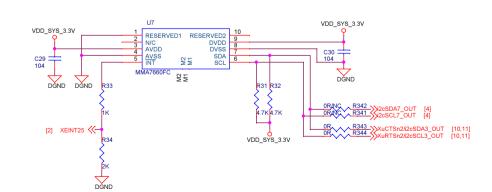


| DGND <                 | B1  | D                  | B2  | ->>XRTCCLKO               |
|------------------------|-----|--------------------|-----|---------------------------|
| [4,11] XciPCLK         | B3  | B1 B2              | B4  | XnWRESET                  |
| [4,11] XCIVSYNC        | B5  | B3 B4              | B6  | SXPWRRGTON                |
|                        | B7  | B5 B6              | B8  |                           |
|                        | B9  | B7 B8              | B10 | XPSHOLD                   |
| [11] XciDATA0          | B11 | B9 B10             | B12 | XEINT8 [11]               |
| [11] XciDATA1          | B13 | B11 B12            | B14 | XEINT10 [11]              |
| [11] XciDATA2          | B15 | B13 B14            | B16 | XEINT11 [11]              |
| [11] XciDATA3          | B17 | B15 B16            | B18 | XEINT12 [11]              |
| [11] XciDATA4          | B19 | B17 B18            | B20 | XEINT13 [11]              |
| [11] XciDATA5          | B21 | B19 B20            | B22 | ->>XEINT14 [11]           |
| [11] XciDATA6          | B23 | B21 B22            | B24 | XEINT15 [11]              |
| [11] XciDATA7 🗲        | B25 | B23 B24            | B26 | XEINT25 [3]               |
| [4,11] XciCLKenb (     | B27 | B25 B26            | B28 | >>XEINT26 [11]            |
| [4,11] XciFIELD        |     | B27 B28            |     | XEINT27 [11]              |
| [5,11] Xi2cSDA0 >      | B29 | B29 B30            | B30 | -SXEINT28 [11]            |
| [5,11] Xi2cSCL0        | B31 | B31 B32            | B32 | XEINT29 [11]              |
| [4,11] CAM A RESET     | B33 | B33 B34            | B34 | Xmmc2DATA0 [3]            |
| [6,7] GPM2 4 USBRST    | B35 | B35 B36            | B36 | Xmmc2DATA1 [3]            |
| [11] XspiCLK1/IEM CLK  | B37 | B37 B38            | B38 | Xmmc2DATA2 [3]            |
| [11] XspiCSn1/IEM DAT  | B39 | B39 B40            | B40 | Xmmc2DATA2 [3]            |
|                        | B41 | B41 B42            | B42 |                           |
|                        | B43 | B41 B42<br>B43 B44 | B44 | Xmmc2CMD [3] Xmmc2CDn [3] |
| [11] XspiMOSI1/i2cSCL5 | B45 |                    | B46 |                           |
| [11] XuotgDP           | B47 | B45 B46            | B48 | Xmmc2CLK [3]              |
| [11] XuotgDM           | B49 | B47 B48            | B50 |                           |
| [11] XuotglD 🤇         | B51 | B49 B50            | B52 |                           |
| [11] XuotgVBUS S       | B53 | B51 B52            | B54 |                           |
| [11] XuoDRVVBUS        | B55 | B53 B54            | B56 | XhdmiTX1P [4]             |
| DGND                   | B57 | B55 B56            | B58 | XhdmiTX1N [4]             |
| [6] XuhostSTROBE1 🤇    | B59 | B57 B58            | B60 | XhdmiTX2P [4]             |
| [6] XuhostDATA1 🔇      | B61 | B59 B60            | B62 | XhdmiTX2N [4]             |
| XuhostSTROBE2 <        | B63 | B61 B62            | B64 | XhdmiTXCP [4]             |
| XuhostDATA2 <          | B65 | B63 B64            | B66 | XhdmiTXCN [4]             |
| [4] i2cSDA7 💸          | B67 | B65 B66            | B68 | XEINT30/HDMI_CEC          |
| [4] i2cSCL7 🔇          | B69 | B67 B68            | B70 | XEINT31/HDMI_HPD          |
| [3] XadcAIN0 <         |     | B69 B70            | 510 | DGND                      |
|                        | ,   |                    |     | V                         |

TINY441ASDK Size A3 Document Number Rev 1312 <Doc> Wednesday, December 25, 2013 Sheet

## SD卡接口电路 ,启动端口(mmc2)

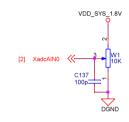




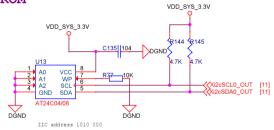
## 复位按键电路

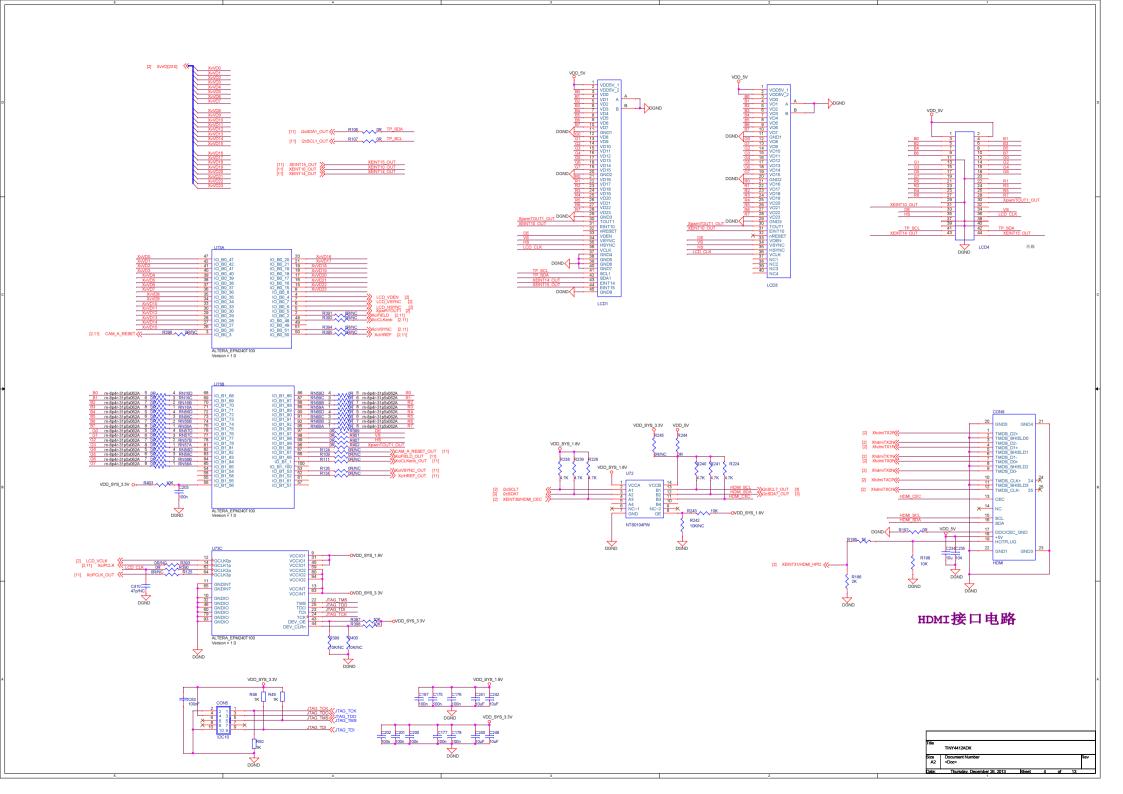


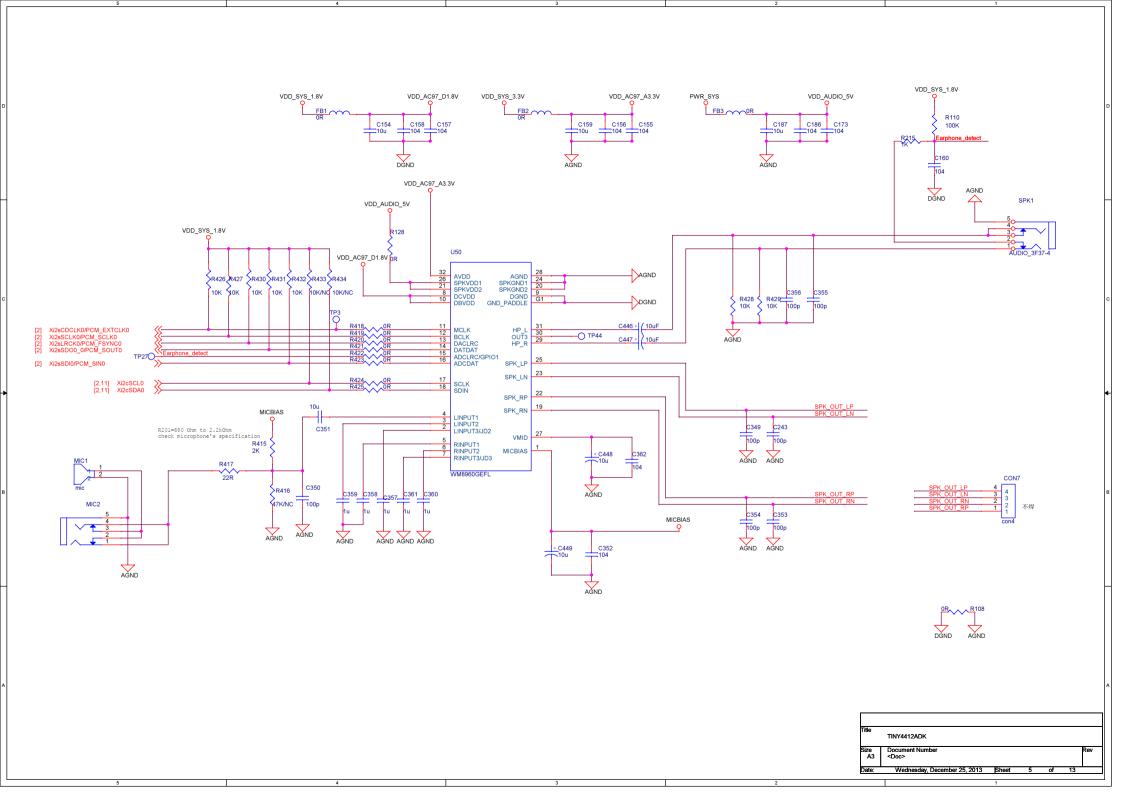
### AD CONVERT



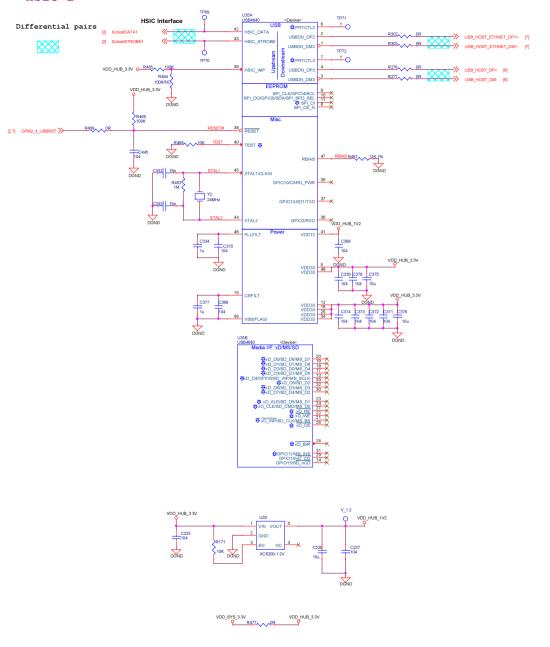
#### EEPROM







HSIC 1





USB ETHNET NO USE DGND VDD\_SYS\_3.3V VDD\_5V VDD\_SYS\_3.3V VDD\_ETHNET\_1.8 VDD\_SYS\_3.3V R127 49.9R,1% 49.9R,1% C229 C390 C391 C394 C398 R479 10K/NC R478 47K C231 R131 R130 330R 330R U66 VDD\_SYS\_3.3V 加上RESET控制 DGND R486 6.98K DĞND TEST1 TXD0 TXD1 TEST2 RREF 2 BGRES R487 LEDG+ LEDG-LINKLED [2,6] GPM2\_4\_USBRST>>-LEDY+ LEDY-LANLED TX+ TX-[6] USB\_HOST\_ETHNET\_DP1+ [6] USB\_HOST\_ETHNET\_DM1-R480 75K M1 M2 M2 M2 NC G1 M3 X M4 G2 104 49.9R,1% R132 49.9R,1% R133 VDD\_ETHNET\_1.8 TXVDDOUT RXVDDOUT 104 DGND DGND C193 C194 104 104 DGND HR911105A LNK\_LED 29 28 28 USB\_LED 27 C147 C190 104 Y1 25MHz DGND DĞND DGND R137 C149 TINY4412ADK Size A3 Document Numbe <Doc>

