

# GN4124 core for PCIe FMC Carrier

DMA  
Interface

DMA Master

to GN4124

L2P\_DATA(15:0)  
L2P\_DFRAME  
L2P\_VALID  
L2P\_CLKp  
L2P\_CLKn  
+ buffer control signals

Wishbone  
Interface

Wishbone Master

from GN4124

wb\_adr\_o  
wb\_dat\_i  
wb\_dat\_o  
wb\_cyc\_o  
wb\_sel\_o  
wb\_stb\_o  
wb\_we\_o  
wb\_ack\_i

P2L\_DATA(15:0)  
P2L\_DFRAME  
P2L\_VALID  
P2L\_CLKp  
P2L\_CLKn  
+ buffer control signals