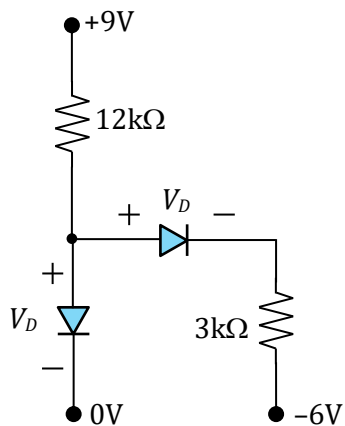


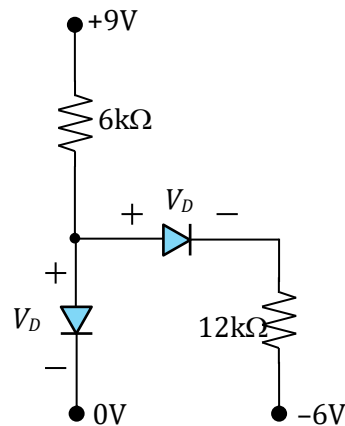
ECEN 325

Homework #3

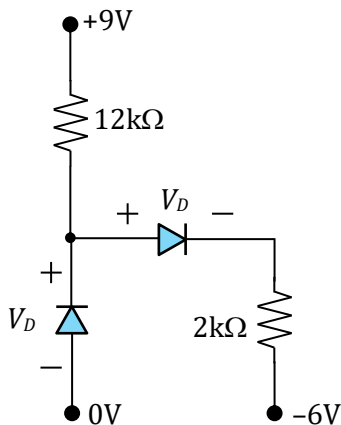
- Upload your solution in a single document in pdf or MS Word format only.
 - Do not upload separate pictures or separate files:
 - Use this word document to prepare your solution. Your options are:
 - Type your equations
 - Write your equations using stylus computer or tablet
 - Insert images of your paper and pencil notes using a scanner (or camera). Scanning is preferred as pictures often have shadows or were taken at an angle that makes it difficult to understand and therefore points may be subtracted when graded.
1. Find the current Voltage V_D and current I_D in the following circuit assuming (a) ideal diode. (b) Constant voltage drop model with $V_{on} = 0.75V$ (c) using the exponential model ($I_0 = 1 \times 10^{-14}$). Verify your results through Multisim simulation



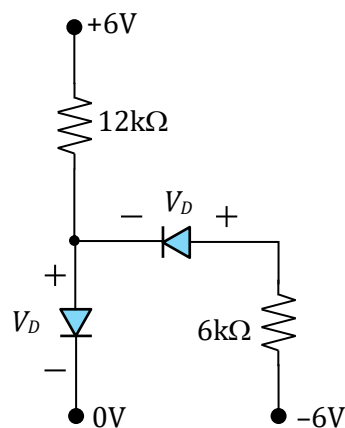
(a)



(b)



(c)

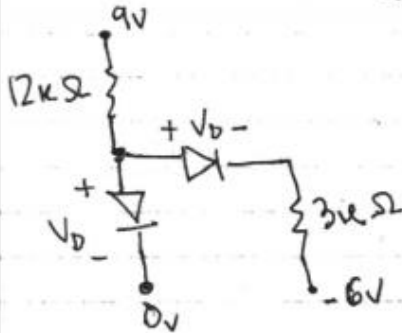


(d)

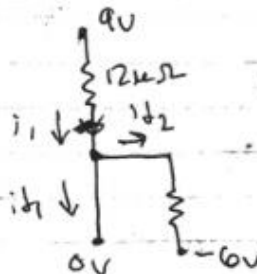
HW #3

PLSN 825

find V_D and I_D



ideal Config:

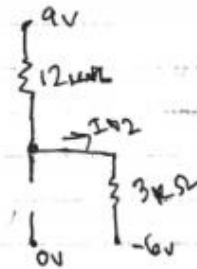


$$I_1 = \frac{9-0}{12k} = .75mA$$

$$I_2 = \frac{0-(-6)}{3k} = 2mA$$

$$I_{D1} = .75 - 2 = -1.25mA$$

reverse bias
so switch off.



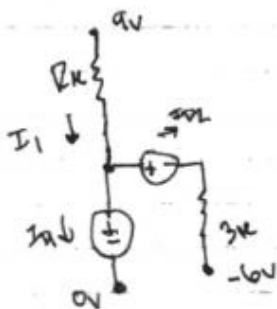
$$I_{D2} = \frac{9-(-6)}{15k} = 1mA$$

$$I_{D1} = 0A$$

$$V_{D2} = 0V$$

$$V_{D1} = \text{open circuit.}$$

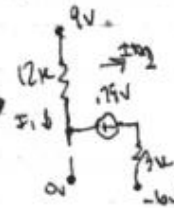
Now with $.75V = V_D$ (when on)



$$I_1 = \frac{9-.75}{12k} = .6875mA$$

$$I_{D2} = \frac{.75-(-6)}{3k} = 2.25mA$$

$$I_{D1} = -.6875mA \leftarrow \text{switch off.}$$



$$I_{D2} = \frac{9-.75-(-6)}{15k}$$

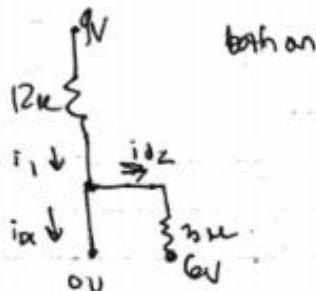
$$I_{D2} = .95mA$$

$$I_{D1} = 0A$$

$$V_{D2} = .75V$$

$$V_{D1} = \text{open circuit.}$$

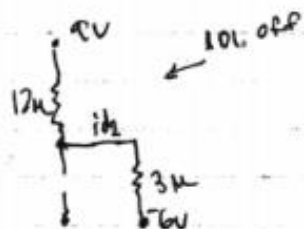
now with $I_0 = 1E-14$



$$i_1 = \frac{9 - 1E-14}{12k} = \frac{8.999}{12k} = .733 \text{ mA}$$

$$i_2 = \frac{1E-14 - -6}{3k} = 1.2 \text{ mA}$$

$$i_{d1} = \text{negative} = \text{off}$$



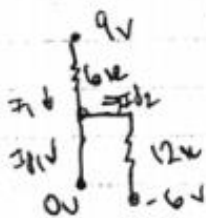
$$i_{d2} = \frac{9 - 1E-14 - 6}{15k} = .132 \text{ mA}$$

$$i_{d1} = 0 \text{ A}$$

$$V_{d2} = .732 \text{ V}$$

$$V_{d1} = \text{open circuit.}$$

b) both on and ideal



$$\frac{0.6}{12k}$$

$$I_1 = \frac{9 - 0}{6k} = 1.5 \text{ mA}$$

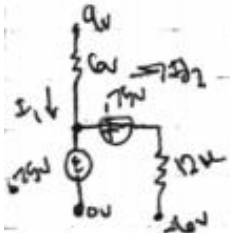
$$I_{D2} = .5 \text{ mA}$$

$$I_{D1} = 1 \text{ mA}$$

$$V_{D1} = 0 \text{ V}$$

$$V_{D2} = 0 \text{ V}$$

now with $V_D = .75$ (when on)



$$I_1 = \frac{9 - .75}{6k} = 1.375 \text{ mA}$$

$$I_{D2} = \frac{.75 - -6}{12k} = .508 \text{ mA}$$

$$I_{D1} = .812 \text{ mA}$$

$$V_{D1} = .75 \text{ V}$$

$$V_{D2} = .75 \text{ V}$$

now with $I_0 = 1 \text{E-14}$

both diode on.

$$I_1 = \frac{9 - 1 \text{E-14}}{6 \text{K}} = 1.49 \text{ mA}$$

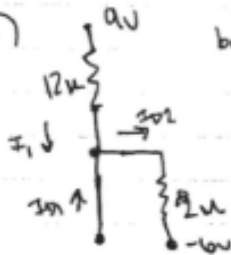
$$I_{D2} = \frac{1 \text{E-14} - -6}{12 \text{K}} = .512 \text{ mA}$$

~~$V_{D1} = 0$~~

$$I_{D1} = .968 \text{ mA}$$

$$V_{D1} = V_{D2} = 1 \text{E-14}$$

(7)



both on and ideal

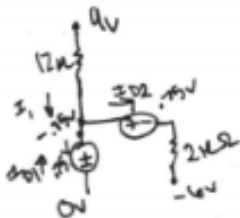
$$I_1 = \frac{9 - 0}{12 \text{K}} = .75 \text{ mA}$$

$$I_{D2} = \frac{0 - -6}{2 \text{K}} = 3 \text{ mA}$$

$$I_{D1} = -I_1 + I_{D2} = 2.25 \text{ mA}$$

$$V_{D1} = V_{D2} = 0 \text{ V}$$

both on and $V_D = .75$ (when on)



$$I_1 = \frac{9 - .75}{12 \text{K}} = .813 \text{ mA}$$

$$I_{D2} = \frac{-.75 - -6}{2 \text{K}} = 2.625 \text{ mA}$$

$$I_{D1} = 1.812 \text{ mA}$$

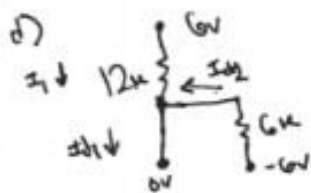
$$V_{D1} = V_{D2} = .75 \text{ V}$$

now with $I_0 = 1 \text{E-14}$

$$I_{D2} = 2.5490 \text{ mA}$$

$$I_{D1} = .799 \text{ mA}$$

$$V_{D1} = V_{D2} = 1 \text{E-14} \text{ V}$$



both on, ideal $V_D = 0V$

$$I_1 = \frac{6 - 0}{12k} = .5mA$$

$$I_{D2} = \frac{-6 - 0}{6k} = -1mA$$

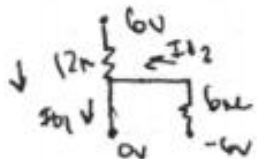
$$I_{D1} = I_1 + I_{D2} = -.5mA$$

turn both off

$$I_{D1} = 0A \quad I_{D2} = 0A$$

$$V_{D1} = \text{open} = V_{D2}$$

both on, .75 drop



$$I_1 = \frac{5.75 - .75}{12k} = .438mA$$

$$I_{D2} = \frac{-6 - .75}{6k} = -1.125mA$$

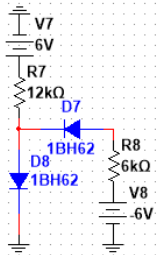
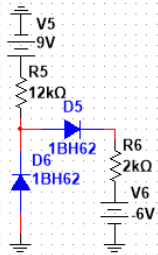
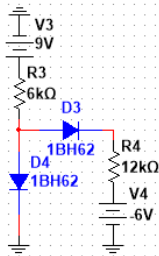
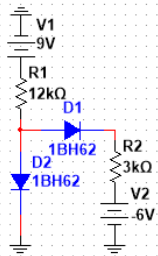
$$I_{D1} = \text{negative}$$

both will be off

$$I_{D1} = 0A \quad I_{D2} = 0A$$

$$V_{D1} = \text{open} = V_{D2}$$

same for
 $i_0 = 1E-14$



Grapher View

File Edit View Graph Trace Cursor Legend Tools Help

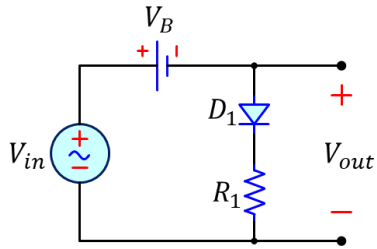
DC Operating Point

HW3 DC Operating Point Analysis

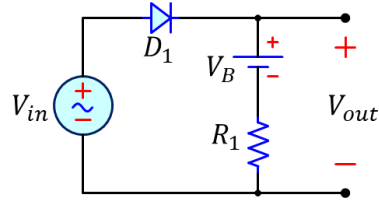
	Variable	Operating point value
1	I(D1[ID])	969.31511 u
2	I(D2[ID])	-5.95000 u
3	I(D3[ID])	505.04737 u
4	I(D4[ID])	907.47890 u
5	I(D5[ID])	2.39543 m
6	I(D6[ID])	1.59681 m
7	I(D7[ID])	-5.95001 u
8	I(D8[ID])	456.23293 u

Selected Diagram: DC Operating Point Analysis

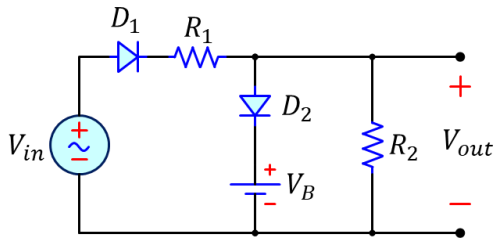
2. If the input is given by $V_{in} = 5 \cos(2\pi 1000 t)$, plot the output of each circuits shown as a function of time. Assume constant drop model ($V_{D,on} = 600 \text{ mV}$) and $V_B = 2.5\text{V}$, $R_1 = 3\text{k}\Omega$ and $R_2 = 6\text{k}\Omega$. Verify your result through Multisim simulation.



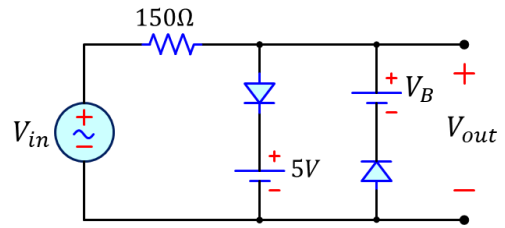
(a)



(b)



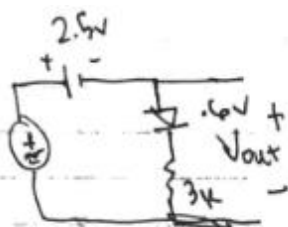
(c)



(d)

2) a)

$5\cos(2\pi 1000t)$

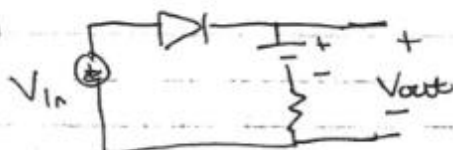


$$V_{con} = 0.6V \quad V_B = 2.5V$$

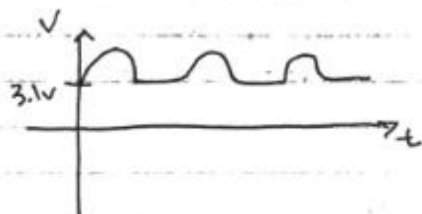
$$0.6 + 2.5 = 3.1V$$



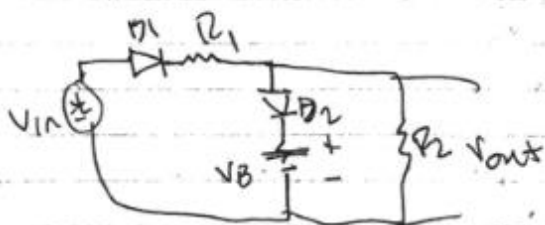
b)



diode turns off when
under 3.1V



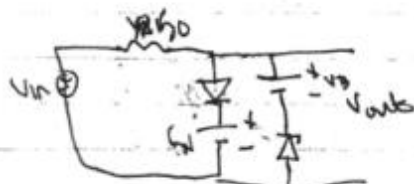
c)



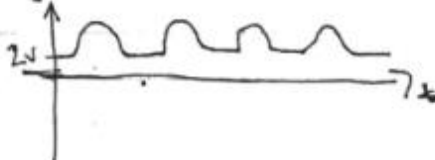
turns off at 0V.

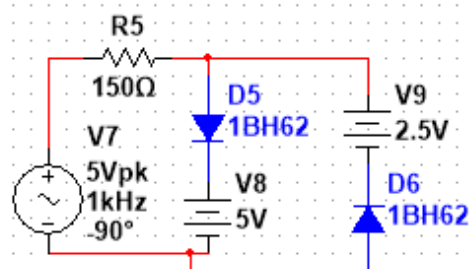
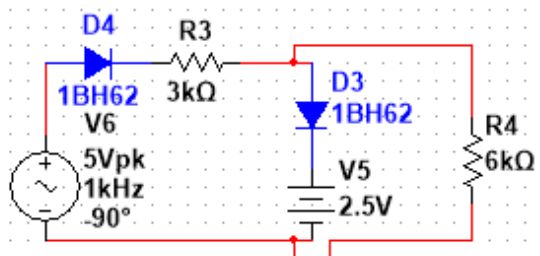
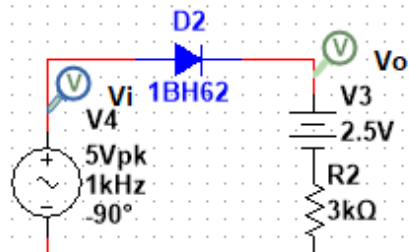
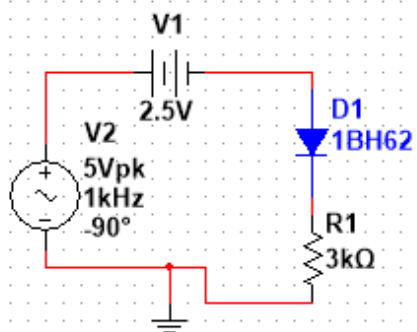


d)



turns off at 2V





3. Consider the following rectifiers

- a. While constructing a full-wave rectifier, a student mistakenly has swapped the terminals of D_3 as depicted in Fig. 3.82. Explain what happens to V_{in} , V_{out} , and I_{in}

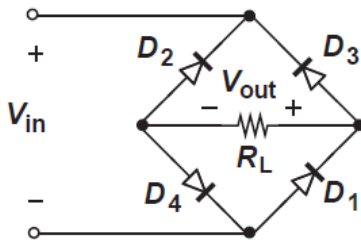


Figure 3.82

- b. The full-wave rectifier circuit shown was designed to have a maximum ripple of 1V, but is not operating properly. The measured waveforms at the three nodes are also shown next. What is wrong with the circuit.

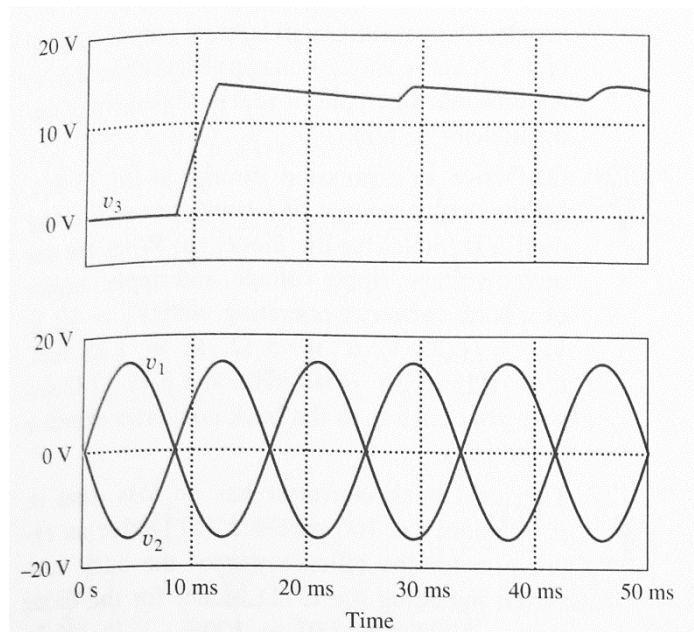
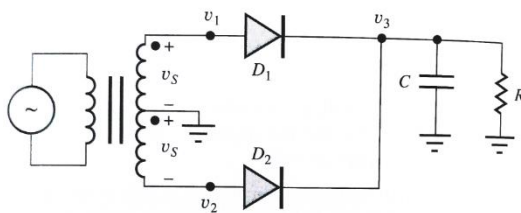
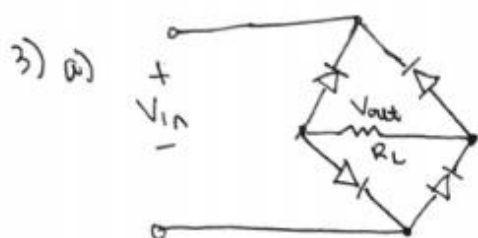


Figure 3.107(b) Waveforms for the circuit in Fig. 3.105(a).



The circuit does not work because during negative and positive inputs of V_{in} , V_o is always 0V.

If V_{in} is positive then Diode 2 and Diode 3 are off. $\rightarrow V_o = 0V$

If V_{in} is negative then Diode 1 and Diode 3 are on but Diode 2 and 4 are off (open circuit). $\rightarrow V_o = 0V$

b)

During the first ^{half} cycle, the output is 0V so this shows that Diode 1 is off. After the first half cycle, the diode turns on which then charges the output, which is shown in the output. So this is a half cycle rectifier, not a full wave.

4. The op amp in the circuit of Fig. P4.84 is ideal with output saturation levels of ± 5 V. The diodes exhibit a constant 0.6-V drop when conducting. Find v_m , v_A , and v_o for:
- $v_I = +1.5V$
 - $v_I = +2.5V$
 - $v_I = -1.5V$
 - $v_I = -2.5V$

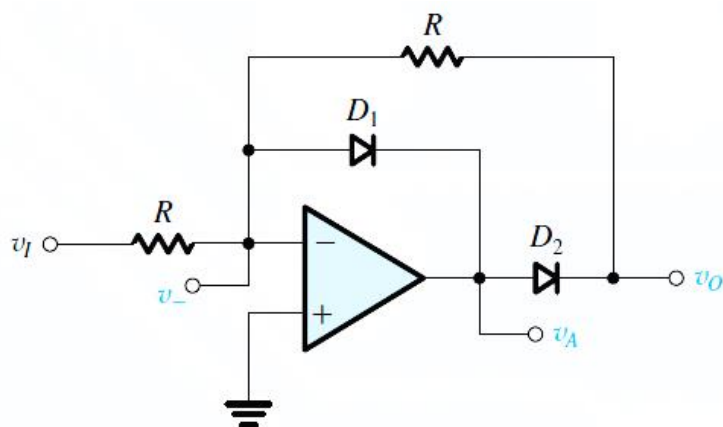
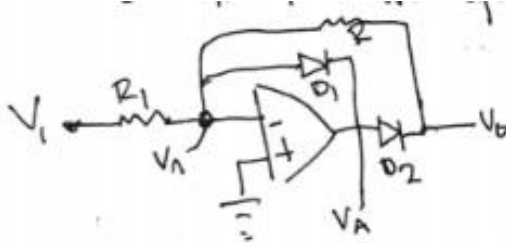


Figure P4.84

4)



$$V_i = 1.5V \rightarrow \begin{cases} V_n = 0V & V_A = 0 - .6 = -.6V \\ V_D = 0V \end{cases}$$

$$V_i = 2.5V \rightarrow \begin{cases} V_n = 0V & V_A = 0 - .6 = -.6V \\ V_D = 0V \end{cases}$$

Inverter
 $V_i = -V_i$

$$V_i = -1.5V \rightarrow \begin{cases} V_n = 0V & V_o = 1.5V \\ V_A = .6 + 1.5 = 2.1V \end{cases}$$

$$V_i = -2.5V \rightarrow \begin{cases} V_n = 0V & V_o = 2.5V \\ V_A = .6 + 2.5 = 3.1V \end{cases}$$