

Jason C. Glass

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Computer Engineering M.S. with experience in low-level systems engineering and high-performance microprocessor design. Knowledge of cache coherence, distributed, and shared programming paradigms. Seeking to leverage background in high-performance computing and networking to develop smarter and safer devices that contribute to the global technology community.

EDUCATION

Northwestern University, Robert R. McCormick School of Engineering and Applied Sciences

Computer Engineering B.S. – September 2017	GPA: 3.09/4.00
Computer Engineering M.S. – June 2018	GPA: 3.50/4.00

EMPLOYMENT EXPERIENCE

Becton Dickinson (BD), Franklin Lakes, NJ

July 2016–August 2016

Information Technology Leadership Program (ITLP) Intern – Enterprise Architecture (EA) Team

- Conducted over 50 hours of solution assessment on EA Management software suites and endorsed chosen solution for legacy system replacement
- Implemented Qlikview dashboards to consolidate metrics for all BD software projects, and identified potential for \$10,000's in savings by identifying duplicate licenses and solutions
- Enhanced vendor management skills by scheduling technology demos for EA team and participated in preliminary pricing negotiations to augment Business Relationship Management (BRM) acumen

ENGINEERING PROJECT/RESEARCH EXPERIENCE

Exploring Microprocessor Design Trends with Unsupervised Machine Learning *Spring Quarter 2018*

- Performed exploratory data mining on the Intel microprocessor catalog to examine the existence of unknown correlations and trends using Weka

Using OpenMP to Speedup Matrix Multiplication on a SMP *Spring Quarter 2018*

- Programmed with C-based OpenMP on an AMD Opteron 8218 to speedup Matrix Multiplication and achieved a total speedup of 7.5x and 95% efficiency on 8 cores

Exploring Trends in Microprocessor Design: Does Moore's Law have a cousin? *Winter Quarter 2018*

- Analyzed the Intel microprocessor catalog for existence of exponential and linear design trends

Fitbit Healthcare Monitoring Modification "Life-Alert" Project *Spring Quarter 2017*

- Combined COTS Fitbit capabilities with IoT networking to create cheap and effective proof-of-concept healthcare monitoring system

SKILLS AND COURSEWORK

Languages: C, C++, C#, VHDL, MATLAB, JavaScript, WebGL, Git

Software: Linux, PSpice, ModelSim, Altera Quartus, Cadence Virtuoso, QlikView, Power Bi, Tableau

Hardware: Oscilloscopes, Waveform Generators, Logic Analyzer

Relevant Coursework: Computer Networking, IoT, Digital Communications, Microprocessor System Design, Parallel Computer Architecture, VLSI CMOS Circuits Design, VLSI System Design, FPGA & ASIC Design