# Modelling of Software intensive systems

# **Assignment 2 CBD**

# **First Edition**

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# 1 | Integration Methods

In this section, we numerically approximate integration using multiple integration methods. These methods are implemented with Causal Block Diagrams.

### 1.1 | Backwards Euler

Formula:

$$\begin{split} I_0 &= f(t_0) \cdot \Delta t \\ I_1 &= f(t_1) \cdot \Delta t \\ I &= I_0 + I_1 \end{split}$$

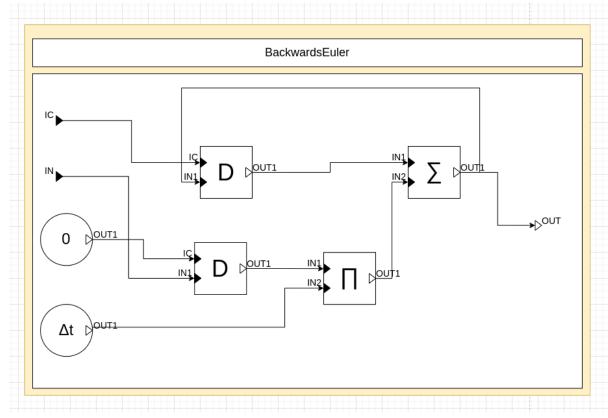


Figure 1: Backwards Euler diagram assets/part\_1/Backwards\_Euler\_drawio.png

This is an exact copy of the Backwards Euler implementation in pyCBD. It multiplies the input with  $\Delta t$  and adds it to the output of the previous iteration. The first delay block outputs the result of the previous iteration. The second one ensures the first iteration will always output the value of IC, by multiplying 0 with  $\Delta t$  and adding it to IC.

#### 1.2 | Forwards Euler

Formula:

$$\begin{split} I_0 &= f(t_1) \cdot \Delta t \\ I_1 &= f(t_2) \cdot \Delta t \\ I &= I_0 + I_1 \end{split}$$

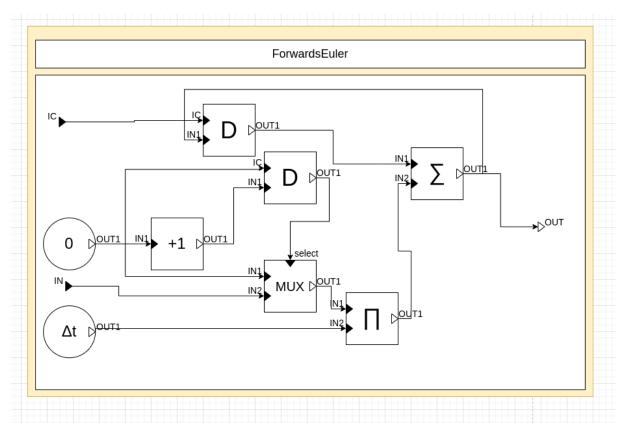


Figure 2: Forwards Euler diagram assets/part\_1/Forwards\_Euler\_drawio.png

While it looks very different to the Backwards Euler implementation, it is actually very similar. The second delay block is replaced by a multiplexer, and some logic is added for the multiplexer selection. The value of  $f(t_1)$  is used instead of  $f(t_0)$ , so we don't need a delay block. However, we need to ensure IC is the output in the first iteration, so we need to replace  $f(t_0)$  with 0. To do that, a multiplexer and a delay block is used. The delay block outputs 0 in the first iteration and 1 otherwise. The rest of the logic is exactly the same as in the Backwards Euler implementation.

As the input f(t) is not fed into a delay block, Forwards Euler will use the value of  $f(t_{i+1})$ , while Backwards Euler will use  $f(t_i)$  in the same iteration, just like the formula. As of writing this, I have realized I could have used a constant block instead of an add one block.

### 1.3 | Trapezoid Rule

#### Formula:

$$\begin{split} I_0 &= \frac{f(t_0) + f(t_1)}{2} \cdot \Delta t \\ I_1 &= \frac{f(t_1) + f(t_2)}{2} \cdot \Delta t \\ I &= I_0 + I_1 \end{split}$$

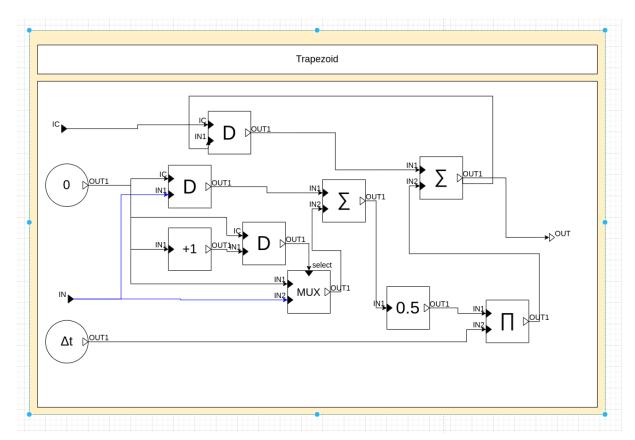


Figure 3: Trapezoid Rule diagram, some arrows have different colors for clarity purposes. assets/part\_1/Trapezoid\_drawio.png

Now we need both  $t_0$  and  $t_1$ . We get them using the same method as in the previous diagrams and combine them to get them both in the same iteration. Now we just add them up and divide by 2. The rest of the logic is the same as the previous two methods. Just like in Forwards Euler, a constant block could have been used instead of an add one block.

# 1.4 | g(t)

The function is represented as the following:

$$g(t) = \frac{t+2}{(t+3)^2}$$

The associated diagram is straightforward and does not need any additional explanations. However, one thing to note is that we multiplied (t+3) with itself instead of raising it to the power of 2 to achieve the same results.

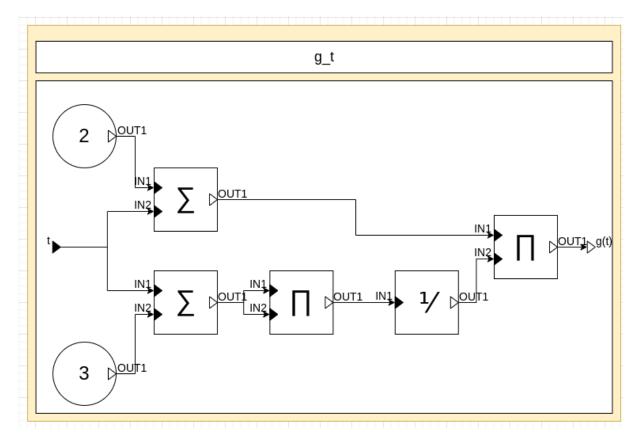


Figure 4: g(t) diagram assets/part\_1/g\_t\_drawio.png

## 1.5 | Comparison

We compare the integration methods with each other and analyze it numerically. We do so by calculating  $\int_0^{100} G(t) dt$  with each method and with different values of  $\Delta t$ .

The analytical value using Wolfram Alpha: 3.21249210409227...

Δt	Backwards Euler	Forwards Euler	Trapezoid Rule
0.1	3.222190908877023	3.2009310596645353	3.2115609842707813
0.01	3.213459296657502	3.2113332284618306	3.2123962625596754
0.001	3.212588796472303	3.212376188820784	3.2124824926465383

Figure 5: Integration simulation results table assets/part\_1/integration\_table.jpeg

We notice that Backwards Euler always overshoots, Forwards Euler always undershoots, and the Trapezoid Rule also undershoots, but less than the Forward Euler. This can be explained if we look at the graph of g(t).

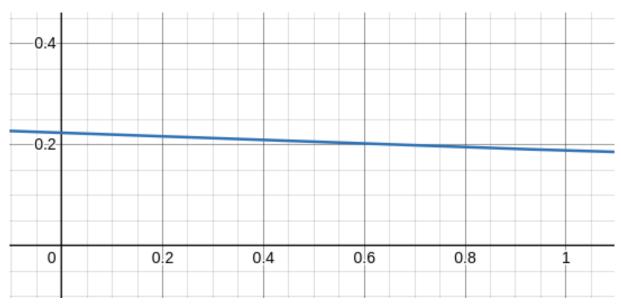


Figure 6: Graph of g(t) assets/part\_1/g\_t\_graph.png

It is clearly a downwards slope. The Backwards Euler approximates by using a smaller t, which leads to it overshooting the actual value, as g(t) is a downwards slope for  $t \geq 0$ . The exact opposite happens with Forward Euler, leading to it overshooting. If g(t) was an upwards slope, however, then Backwards Euler would undershoot and Forwards Euler would overshoot.

Now let's look at the error of the integration approximations compared to the analytical result.

Δt	Backwards Euler Error	Forwards Euler Error	Trapezoid Rule Error
0.1	0.00969880478475282359	0.01156104442773475682	0.00093111982148874617
0.01	0.00096719256523192243	0.00115887563043948205	0.00009584153259467598
0.001	0.00009669238003295888	0.00011591527148624436	0.00000961144573174977

Figure 7: Integration simulation results error table assets/part\_1/integration\_error.jpeg

Higher precision leads to lower errors, which is obvious. Forwards Euler has the highest error, although Backwards Euler has a similar error. Trapezoid Rule has much smaller error, around 10 times lower.

Considering the accuracy, using the Trapezoid Rule may be the optimal choice. However, if we consider the computation time, that may not be the case. These are the average computation times of Backwards Euler, Forwards Euler and Trapezoid Rule respectively: 2.5 seconds, 3 seconds, 3.5 seconds. Notice that Trapezoid Rule took 40% longer than

Backwards Euler. Forwards Euler also took longer, while having similar precision. Note that our implementation of Forwards Euler and Trapezoid Rule is not optimal. However, the results will not be much different even if our implementations are optimal.

We conclude that Backwards Euler is the most efficient method out of the three, sacrificing a little bit of accuracy. If precision is most important, Trapezoid Rule is the best, with higher computation.

# 2 Co-simulation

We start by exporting the FMU model of the plant, and we will make use of this model to cosimulate with our CBD model.

## 2.1 | CBD model making use of Drawio

We will make use of Drawio to draw our CBD model. It will look something like this:

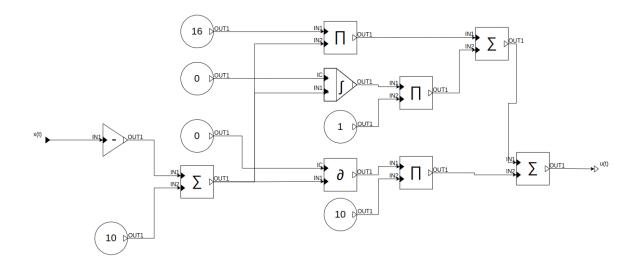


Figure 8: PID circuit assets/part\_2/PID.png

Here we will reuse the values we got from the previous assignment and make use of  $K_i=1$  instead of 0. Notice how we use Product Blocks and Constant Blocks to multiply with a constant. We realized later that we could have used Gain Blocks to make it simpler, but we have already exported the diagrams to FMU. As it actually does not matter that much, we spared the effort and left it as is.

We can turn this circuit also in a block and this block will look as follows:

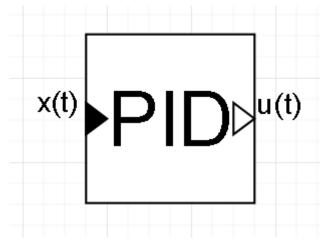


Figure 9: PID block assets/part\_2/PID\_block.png

With a simple input of x(t) and simple output of the control signal u(t).

The full drawio schema makes use of a class diagram, this will look something like this:

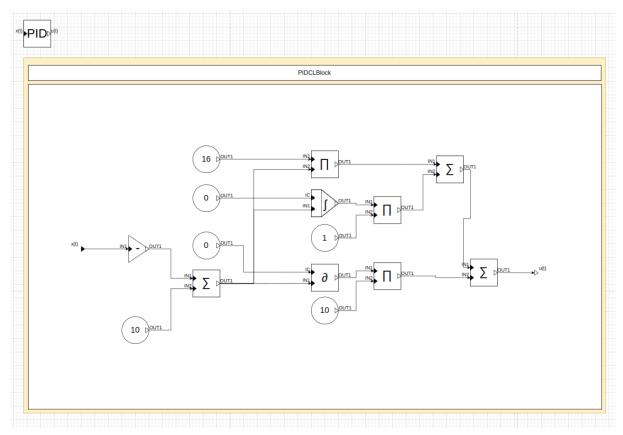


Figure 10: PID full schema assets/part\_2/PID\_full.png

We will call this block PIDCLBLock. This is important to know for later.

#### 2.2 | Controller FMU from Controller CBD

The description.xml will have the following contents:

```
<?xml version="1.0" encoding="UTF-8"?>
<fmiModelDescription fmiVersion="2.0" modelName="PIDCLBlock"</pre>
  guid="{1c15c3ee-a843-4c31-b39d-9fe699fc1e63}"
  description="" generationTool="CBD2FMU">
  <ModelExchange modelIdentifier="PIDCLBlock">
    <SourceFiles>
      <File name="all.c"/>
      <File name="lsolve.c"/>
    </SourceFiles>
  </ModelExchange>
  <CoSimulation modelIdentifier="PIDCLBlock"
canHandleVariableCommunicationStepSize="true">
    <SourceFiles>
      <File name="all.c"/>
      <File name="lsolve.c"/>
    </SourceFiles>
  </CoSimulation>
```

```
<!-- Unit definitions: Not required/used in CBD -->
  <!-- Log categories: Not required/used in CBD -->
  <DefaultExperiment startTime="0.0"</pre>
    stopTime="20"
    stepSize="0.004"
    tolerance="1e-06" />
  <!-- Vendor Annotations: Not required/used in CBD -->
  <ModelVariables>
    <ScalarVariable name="PIDCLBlock.x(t)" valueReference="0"</pre>
      causality="input" variability="continuous">
      <Real start="0"/>
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.u(t)" valueReference="1" initial="calculated"</pre>
      causality="output" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCms1og_-4.IN1" valueReference="2"</pre>
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobElhvCmslog -4.0UT1" valueReference="3"</pre>
initial="calculated"
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCmslog -7.IN1" valueReference="4"</pre>
initial="calculated"
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCms1og_-7.IN2" valueReference="5"</pre>
initial="calculated"
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCq6ToobE1hvCmslog -7.0UT1" valueReference="6"</pre>
initial="calculated"
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCq6ToobE1hvCms1oq -11.0UT1" valueReference="7"</pre>
initial="exact"
      causality="local" variability="constant">
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    <ScalarVariable name="PIDCLBlock.DzCg6ToobElhvCmslog -21.IN1" valueReference="8"</pre>
initial="calculated"
      causality="local" variability="continuous">
      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobElhvCmslog_-21.IN2" valueReference="9"</pre>
initial="calculated"
```

```
causality="local" variability="continuous">
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    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCmslog_-21.0UT1"</pre>
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      causality="local" variability="continuous">
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    <ScalarVariable name="PIDCLBlock.IC_d.OUT1" valueReference="18" initial="exact"</pre>
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      <Real start="0"/>
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    <ScalarVariable name="PIDCLBlock.dqvIs03bip9LbBkwujtJ-2.IN1" valueReference="20"</pre>
initial="calculated"
      causality="local" variability="continuous">
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    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.dqvIs03bip9LbBkwujtJ-2.IN2" valueReference="21"</pre>
```

```
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      causality="local" variability="continuous">
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      causality="local" variability="continuous">
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    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.K_i.OUT1" valueReference="23" initial="exact"</pre>
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    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.K d.OUT1" valueReference="24" initial="exact"</pre>
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      causality="local" variability="continuous">
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    </ScalarVariable>
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      causality="local" variability="continuous">
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    </ScalarVariable>
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```

```
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    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCms1og -13 multDelta.OUT1"</pre>
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    </ScalarVariable>
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valueReference="39" initial="calculated"
      causality="local" variability="continuous">
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    </ScalarVariable>
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    </ScalarVariable>
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      causality="local" variability="continuous">
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    </ScalarVariable>
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      causality="local" variability="continuous">
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    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCms1og_-17_delay.0UT1"</pre>
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      <Real />
    </ScalarVariable>
    <ScalarVariable name="PIDCLBlock.DzCg6ToobE1hvCmslog_-17_neg2.IN1"</pre>
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```

```
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    </ScalarVariable>
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    </0utputs>
    <InitialUnknowns>
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    </InitialUnknowns>
  </ModelStructure>
</fmiModelDescription>
```

The C-code will look something like this:

```
#include "lsolve.h"
#include "version.h"
#include "model.h"
#include <stdio.h>
#include <math.h>
void initialEquations(CBD* cbd) {
  Real delta = 0.0001;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__4_IN1 = _PIDCLBlock_x(t);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__4_OUT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCmslog__4_IN1);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__11_0UT1 = 10;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__7_IN2 = _PIDCLBlock_DzCg6ToobE1hvCms1og__11_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__7_IN1 = _PIDCLBlock_DzCg6ToobE1hvCmslog__4_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__7_OUT1 = (_PIDCLBlock_DzCg6ToobE1hvCmslog__7_IN1 +
PIDCLBlock DzCg6ToobE1hvCmslog 7 IN2);
  _{PIDCLBlock\_K\_p\_0UT1} = 16;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN2 = _PIDCLBlock_DzCg6ToobE1hvCms1og__7_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN1 = _PIDCLBlock_K_p_0UT1;
 _PIDCLBlock_DzCg6ToobE1hvCms1og__29_0UT1 = (_PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN1
* PIDCLBlock DzCg6ToobE1hvCmslog 29 IN2);
  PIDCLBlock DzCq6ToobE1hvCmslog 13 zero OUT1 = 0;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 delayIn IC =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_zero_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayIn_OUT1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayIn_IC;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_delayIn_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_delta_t_0UT1 = delta;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 multDelta IN2 =
PIDCLBlock DzCg6ToobE1hvCmslog 13 delta t OUT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 multDelta IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayIn_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_multDelta_0UT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_IN1 *
_PIDCLBlock_DzCg6ToobE1hvCms1og__13_multDelta_IN2);
  _PIDCLBlock_IC_i_OUT1 = 0;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_IC = _PIDCLBlock_IC_i_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_0UT1 =
PIDCLBlock DzCq6ToobE1hvCmslog 13 delayState IC;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_delayState_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_sumState_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_OUT1;
   PIDCLBlock DzCg6ToobE1hvCmslog 13 sumState IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_OUT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 sumState OUT1 =
( PIDCLBlock_DzCg6ToobE1hvCms1og__13_sumState_IN1 +
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_IN2);
  PIDCLBlock K i OUT1 = 1;
  _PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_IN2 = _PIDCLBlock_K_i_OUT1;
  PIDCLBlock dqvIs03bip9LbBkwujtJ 2 IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_OUT1;
  PIDCLBlock dqvIs03bip9LbBkwujtJ 2 OUT1 = ( PIDCLBlock dqvIs03bip9LbBkwujtJ 2 IN1 *
PIDCLBlock dqvIs03bip9LbBkwujtJ 2 IN2);
  _PIDCLBlock_DzCg6ToobE1hvCms1og__21_IN2 = _PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_0UT1;
```

```
_PIDCLBlock_DzCg6ToobE1hvCmslog__21_IN1 = _PIDCLBlock_DzCg6ToobE1hvCmslog__29_0UT1;
  PIDCLBlock DzCq6ToobE1hvCmsloq 21 OUT1 = ( PIDCLBlock DzCq6ToobE1hvCmsloq 21 IN1
+ PIDCLBlock DzCg6ToobE1hvCmslog 21 IN2);
  _PIDCLBlock_IC_d_OUT1 = 0;
 _PIDCLBlock_DzCg6ToobE1hvCmslog__17_delta_t_0UT1 = delta;
  PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN2 =
PIDCLBlock DzCg6ToobE1hvCmslog 17 delta t OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN1 = _PIDCLBlock_IC_d_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__17_multIc_0UT1 =
(_PIDCLBlock_DzCg6ToobE1hvCms1og__17_multIc_IN1 *
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN2);
  PIDCLBlock DzCg6ToobE1hvCmslog 17 neg1 IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_neg1_OUT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_neg1_IN1);
  PIDCLBlock DzCg6ToobE1hvCmslog 17 sum1 IN2 =
PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg1_0UT1;
  PIDCLBlock_DzCg6ToobE1hvCms1og__17_sum1_0UT1 =
( PIDCLBlock DzCg6ToobE1hvCmslog 17 sum1 IN1 +
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_IN2);
  PIDCLBlock DzCg6ToobE1hvCmslog 17 delay IC =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_delay_OUT1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_delay_IC;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_delay_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__7_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg2_IN1 =
PIDCLBlock DzCg6ToobE1hvCmslog 17 delay OUT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 17 neg2 OUT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg2_IN1);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__17_sum2_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg2_0UT1;
  PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_OUT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_IN1 +
PIDCLBlock DzCg6ToobE1hvCmslog 17 sum2 IN2);
  PIDCLBlock DzCq6ToobElhvCmslog 17 inv IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_delta_t_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_inv_0UT1 = 1.0/
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_inv_IN1;
  PIDCLBlock_DzCg6ToobE1hvCms1og__17_mult_IN2 =
PIDCLBlock DzCg6ToobE1hvCmslog 17 inv OUT1;
  PIDCLBlock_DzCg6ToobE1hvCmslog__17_mult_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_sum2_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_mult_OUT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__17_mult_IN1 *
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_mult_IN2);
  _PIDCLBlock_K_d_OUT1 = 10;
  _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN2 = PIDCLBlock K d OUT1;
  PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN1 =
PIDCLBlock DzCg6ToobE1hvCmslog 17 mult OUT1;
  _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_0UT1 = (_PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN1
* _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN2);
```

```
_PIDCLBlock_DzCg6ToobE1hvCmslog__25_IN2 = _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_0UT1;
 PIDCLBlock DzCq6ToobElhvCmsloq 25 IN1 = PIDCLBlock DzCq6ToobElhvCmsloq 21 OUT1;
 _PIDCLBlock_DzCg6ToobE1hvCms1og__25_0UT1 = (_PIDCLBlock_DzCg6ToobE1hvCms1og__25_IN1
+ _PIDCLBlock_DzCg6ToobE1hvCmslog__25_IN2);
 _PIDCLBlock_u(t) = _PIDCLBlock_DzCg6ToobE1hvCms1og__25_0UT1;
  cbd->time last = cbd->time;
}
void calculateEquations(CBD* cbd) {
  Real delta = cbd->time - cbd->time_last;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__4_IN1 = _PIDCLBlock_x(t);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__4_0UT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCmslog__4_IN1);
  PIDCLBlock DzCg6ToobE1hvCmslog 11 OUT1 = 10;
 _PIDCLBlock_DzCg6ToobE1hvCmslog__7_IN2 = _PIDCLBlock_DzCg6ToobE1hvCmslog__11_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__7_IN1 = _PIDCLBlock_DzCg6ToobE1hvCmslog__4_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__7_OUT1 = (_PIDCLBlock_DzCg6ToobE1hvCms1og__7_IN1 +
_PIDCLBlock_DzCg6ToobE1hvCms1og__7_IN2);
  PIDCLBlock K p OUT1 = 16;
 _PIDCLBlock_DzCg6ToobE1hvCmslog__29_IN2 = _PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN1 = _PIDCLBlock_K_p_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__29_0UT1 = (_PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN1
* _PIDCLBlock_DzCg6ToobE1hvCms1og__29_IN2);
  PIDCLBlock DzCq6ToobE1hvCmslog 13 delayIn OUT1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayIn_IN1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_delta_t_0UT1 = delta;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_IN2 =
PIDCLBlock DzCg6ToobElhvCmslog 13 delta t OUT1;
  PIDCLBlock DzCq6ToobE1hvCms1oq 13 multDelta IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__13_delayIn_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_multDelta_OUT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_IN1 *
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_IN2);
  PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_OUT1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_IN1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__13_delayState_OUT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 sumState IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_multDelta_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_0UT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_IN1 +
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_IN2);
  _PIDCLBlock_K_i_OUT1 = 1;
 _PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_IN2 = _PIDCLBlock_K_i_0UT1;
  PIDCLBlock dqvIs03bip9LbBkwujtJ 2 IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_OUT1;
  _PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_0UT1 = (_PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_IN1 *
PIDCLBlock dqvIs03bip9LbBkwujtJ 2 IN2);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__21_IN2 = _PIDCLBlock_dqvIs03bip9LbBkwujtJ_2_0UT1;
 _PIDCLBlock_DzCg6ToobE1hvCmslog__21_IN1 = _PIDCLBlock_DzCg6ToobE1hvCmslog__29_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__21_OUT1 = (_PIDCLBlock_DzCg6ToobE1hvCmslog__21_IN1
+ PIDCLBlock DzCg6ToobE1hvCmslog 21 IN2);
  _PIDCLBlock_DzCg6ToobE1hvCms1og__17_delay OUT1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_delay_IN1;
```

```
PIDCLBlock DzCg6ToobE1hvCmslog 17 neg2 IN1 =
PIDCLBlock DzCq6ToobE1hvCmslog 17 delay OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_neg2_OUT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg2_IN1);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__7_0UT1;
  PIDCLBlock DzCg6ToobE1hvCms1og 17 sum2 IN1 =
PIDCLBlock DzCg6ToobE1hvCmslog 17 neg2 OUT1;
   PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_OUT1 =
(_PIDCLBlock_DzCg6ToobE1hvCms1og__17_sum2_IN1 +
PIDCLBlock DzCg6ToobE1hvCmslog 17_sum2_IN2);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_delta_t_0UT1 = delta;
  PIDCLBlock_DzCg6ToobE1hvCmslog__17_inv_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_delta_t_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_inv_0UT1 = 1.0/
PIDCLBlock DzCg6ToobE1hvCmslog 17 inv IN1;
   PIDCLBlock DzCq6ToobE1hvCms1og 17 mult IN2 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_inv_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_mult_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum2_OUT1;
   PIDCLBlock DzCg6ToobE1hvCmslog 17 mult OUT1 =
( PIDCLBlock DzCq6ToobE1hvCms1oq 17 mult IN1 *
PIDCLBlock DzCg6ToobE1hvCmslog 17 mult IN2);
  _PIDCLBlock_K_d_0UT1 = 10;
 _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN2 = _PIDCLBlock_K_d_0UT1;
   PIDCLBlock dqvIs03bip9LbBkwujtJ 76 IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_mult_OUT1;
_PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_0UT1 = (_PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN1 * _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_IN2);
 _PIDCLBlock_DzCg6ToobE1hvCmslog__25_IN2 = _PIDCLBlock_dqvIs03bip9LbBkwujtJ_76_0UT1;
  PIDCLBlock DzCq6ToobElhvCmsloq 25 IN1 = PIDCLBlock DzCq6ToobElhvCmsloq 21 OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__25_0UT1 = (_PIDCLBlock_DzCg6ToobE1hvCms1og__25_IN1
+ PIDCLBlock_DzCg6ToobE1hvCmslog 25_IN2);
 _PIDCLBlock_IC_d_OUT1 = 0;
  _PIDCLBlock_IC_i_OUT1 = 0;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_zero_0UT1 = 0;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_delta_t_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN1 = _PIDCLBlock_IC_d_OUT1;
  PIDCLBlock DzCq6ToobE1hvCmslog 17 multIc OUT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN1 *
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_IN2);
  _PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg1_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_multIc_OUT1;
   PIDCLBlock DzCq6ToobE1hvCmsloq 17 neq1 OUT1 = (-
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_neg1_IN1);
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_IN2 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 17_sum1_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_neg1_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_0UT1 =
(_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_IN1 +
_PIDCLBlock_DzCg6ToobE1hvCmslog__17_sum1_IN2);
  PIDCLBlock DzCg6ToobE1hvCmslog 17 delay IN1 =
PIDCLBlock DzCq6ToobE1hvCmslog 7 OUT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 17 delay IC =
```

```
_PIDCLBlock_DzCg6ToobE1hvCms1og__17_sum1_0UT1;
   PIDCLBlock DzCg6ToobE1hvCmslog 13 delayState IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_sumState_OUT1;
  _PIDCLBlock_DzCg6ToobE1hvCmslog__13_delayState_IC = _PIDCLBlock_IC_i_0UT1;
  _PIDCLBlock_DzCg6ToobE1hvCms1og__13_delayIn_IN1 =
_PIDCLBlock_DzCg6ToobE1hvCmslog__7_0UT1;
  PIDCLBlock DzCg6ToobE1hvCmslog 13 delayIn IC =
_PIDCLBlock_DzCg6ToobE1hvCmslog__13_zero_0UT1;
  _PIDCLBlock_u(t) = _PIDCLBlock_DzCg6ToobE1hvCms1og__25_0UT1;
  cbd->time_last = cbd->time;
}
void getContinuousStates(CBD* cbd, double x[], size_t nx) {
void setContinuousStates(CBD* cbd, const double x[], size_t nx) {
// calculateEquations(cbd);
}
void getDerivatives(CBD* cbd, double dx[], size_t nx) {
// calculateEquations(cbd);
void getStateEvents(CBD* cbd, double z[], size_t nz) {
  // No state events found
void stateEvent(CBD* cbd) {
  // No state events found
  cbd->nominalsOfContinuousStatesChanged = False;
  cbd->terminateSimulation = False;
  cbd->nextEventTimeDefined = False;
}
Status doStep(CBD* cbd, double t, double tNext) {
  // No state events found
  Boolean timeEvent;
  double h = tNext - t;
  while(cbd->time + h < tNext + 0.01 * h) {
    timeEvent = cbd->nextEventTimeDefined && cbd->time >= cbd->nextEventTime;
    if(timeEvent) {
      stateEvent(cbd);
    if(cbd->terminateSimulation) {
      // Force termination
      return Discard;
    }
    cbd->time += h;
    calculateEquations(cbd);
  }
```

```
return OK;
}
```

description.xml describes the model and the general simulation setup. It shows each variable the model has and lists their property, such as the initial values, how their values change, and if they are input, output or a local variable.

model.c contains the actual implementation of the model, how the values of variables are calculated and such. The calculations are separated into initial equations and equations, just like in Modelica. Each variable are assigned their initial value in the initial equation function. For each time step, calculateEquations is called and each variable gets their new value.

As the FMU was originally a CBD, we can still see the connections between blocks in the equations. Let's look at the first calculation of the model.cfile:

```
PIDCLBlock_DzCg6ToobE1hvCmslog__4_IN1 = _PIDCLBlock_x(t);
```

From this we can assume that there is a connection between one of the blocks and the input of the model, called PIDCLBlock\_x(t). We can tell that the PIDCLBlock\_x(t) is an input of the model by looking at the causality property of PIDCLBlock.x(t) in the XML-file.

It is very easy to take multiple FMUs and use them in a single simulation. As each FMU has inputs and outputs, which we can easily get from the XML file, we can just feed the outputs of a model to the input of another model and simulate them separately with the same settings.

## 2.3 | Compile Controller C-Code and Co-simulate

Now let us try a co-simulation between the plant and CBD we just created.

After some tweaking with the test script, we can get some graphs to compare with the pure Modelica simulation. Do note that there were some errors in the previous Modelica model and has been fixed. The graphs may therefore be different from what you have previously seen. There is also the fact that  $K_i$  has been set to 1, instead of 0.

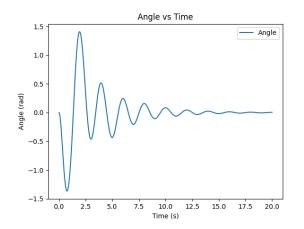


Figure 11: Pendulum angle theta vs time CBD assets/part\_2/Angle\_vs\_Time.png

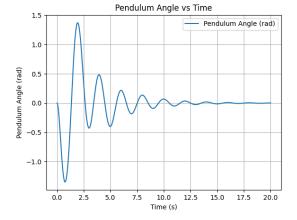


Figure 12: Pendulum angle theta vs time

Modelica

assets/part\_2/

Pendulum\_Angle\_vs\_Time\_Modelica.png

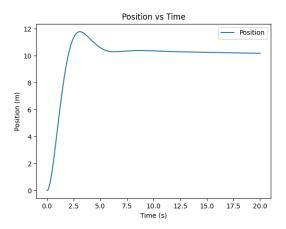


Figure 13: Position X vs time CBD assets/part\_2/Position\_vs\_Time.png

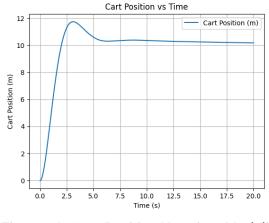


Figure 14: Cart Position X vs time Modelica assets/part\_2/
Cart\_Position\_vs\_Time\_Modelica.png

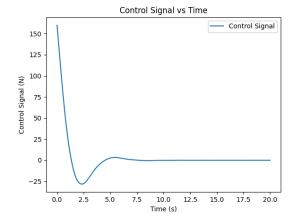


Figure 15: Control signal vs time CBD assets/part\_2/Control\_Signal\_vs\_Time.png

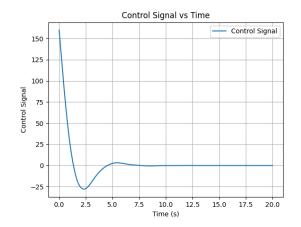


Figure 16: Control signal vs time Modelica assets/part\_2/
Control\_Signal\_vs\_Time\_Modelica.png

On the left side we see the CBD models graphs and on the right side we see the modelica graphs.

The similarity between the Modelica and CBD model values is likely due to both models accurately representing the same control loop system dynamics and using consistent mathematical formulations and parameter values.

So the environment are the same, the only difference is "how it is made". How the equations are computed and how the models will behave are the same.