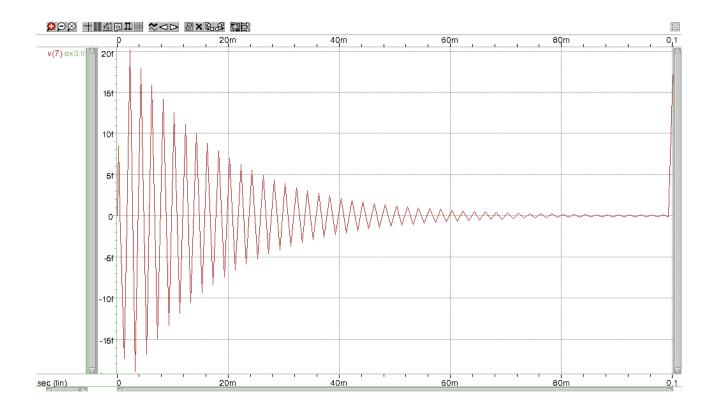
April 8, 2016

### **Prelab MOSFET 2**

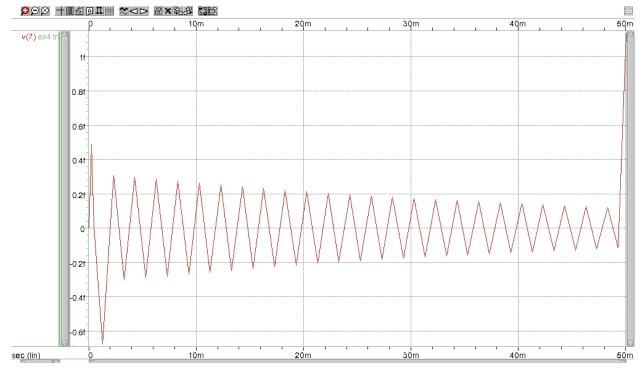
### Exercise 3

```
📝 ex3.sp 🗶
Exercise 3
.opt post
*MOSFET model description
.MODEL NMOS2N7000 NMOS(LEVEL=3
+Rs=0.205 NSUB=1.0e15 DELTA=0.1
+KAPPA=0.0506 TPG=1 CGD0=3.1716e-9
+RD=0.239 VT0=1 VMAX=1.0e7
+ETA=0.0223089 NFS=6.6e10 T0X=1.0e-5
+LD=1.698e-9 U0=862.425 XJ=6.4666e-7
+THETA=1.0e-5 CGS0=9.09e-9)
*Voltage Source
Vs 1 0 SIN(0V 50mV 10kHz)
V1 3 0 DC 5V
V2 6 0 DC 5V
*MOSFET
M1 5 2 4 4 NMOS2N7000 L=2.5e-6 W=0.8e-2
*Circuit Description
Rb1 3 2 1k
Rd 6 5 1.5k
Rb2 2 0 3k
Rss 4 0 500
R1 7 0 1k
C1 1 2 10e-9
C2 5 7 10e-9
.TRAN 1ms 100ms 0ms 1ms
.PLOT V(R1)
.end
```

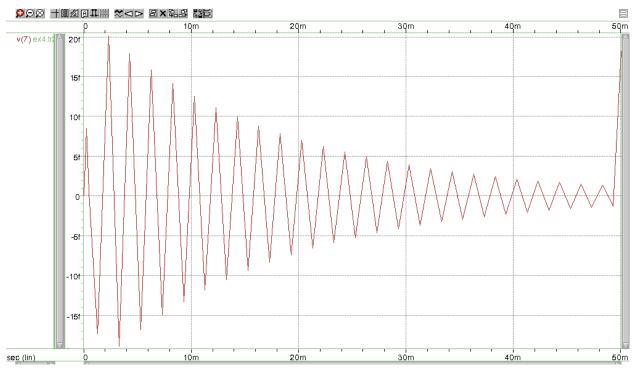


### Exercise 4

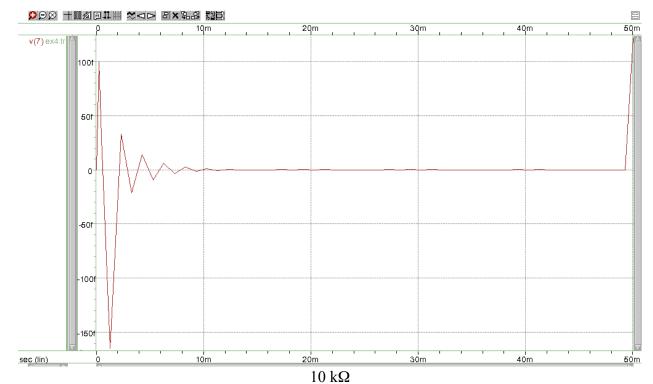
```
📝 ex4.sp 🗶
Exercise 4
.opt post
*MOSFET model description
.MODEL NMOS2N7000 NMOS(LEVEL=3
+Rs=0.205 NSUB=1.0e15 DELTA=0.1
+KAPPA=0.0506 TPG=1 CGD0=3.1716e-9
+RD=0.239 VTO=1 VMAX=1.0e7
+ETA=0.0223089 NFS=6.6e10 T0X=1.0e-5
+LD=1.698e-9 U0=862.425 XJ=6.4666e-7
+THETA=1.0e-5 CGS0=9.09e-9)
*Voltage Source
Vs 1 0 SIN(0V 50mV 10kHz)
V1 3 0 DC 5V
V2 6 0 DC 5V
M1 5 2 4 4 NMOS2N7000 L=2.5e-6 W=0.8e-2
*Circuit Description
Rb1 3 2 1k
Rd 6 5 1.5k
Rb2 2 0 3k
Rss 4 0 500
R1 7 0 r1val
.param r1val = 100k
C1 1 2 10e-9
C2 5 7 10e-9s
.TRAN 1ms 50ms 0ms 1ms
.PLOT V(R1)
.alter
.param r1val = 10k
.alter
.param r1val = 1k
.alter
.param r1val = 100
.end
```



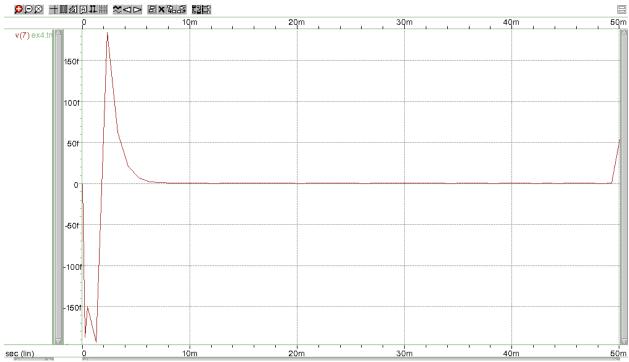




 $1 \text{ k}\Omega$ 







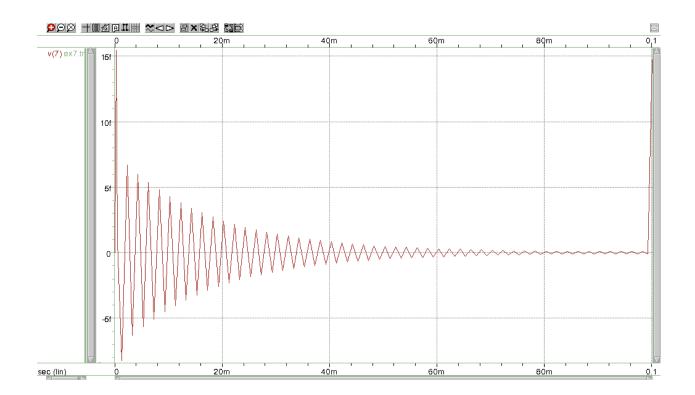
 $100~\mathrm{k}\Omega$ 

# 👱 ex7.sp 🗶 Exercise 7 .opt post \*MOSFET model description .MODEL NMOS2N7000 NMOS(LEVEL=3 +Rs=0.205 NSUB=1.0e15 DELTA=0.1 +KAPPA=0.0506 TPG=1 CGD0=3.1716e-9 +RD=0.239 VT0=1 VMAX=1.0e7 +ETA=0.0223089 NFS=6.6e10 T0X=1.0e-5 +LD=1.698e-9 U0=862.425 XJ=6.4666e-7 +THETA=1.0e-5 CGS0=9.09e-9) \*Voltage Source Vs 1 0 SIN(0V 50mV 10kHz) V1 3 0 DC 5V V2 6 0 DC 5V \*MOSFET M1 5 2 4 4 NMOS2N7000 L=2.5e-6 W=0.8e-2 \*Circuit Description Rb1 3 2 1k Rd 6 5 1.5k Rb2 2 0 3k Rss 4 0 500 R1 7 0 1k C1 1 2 10e-9 C2 4 7 10e-9

TRAN 1ms 100ms 0ms 1ms

.PLOT V(R1)

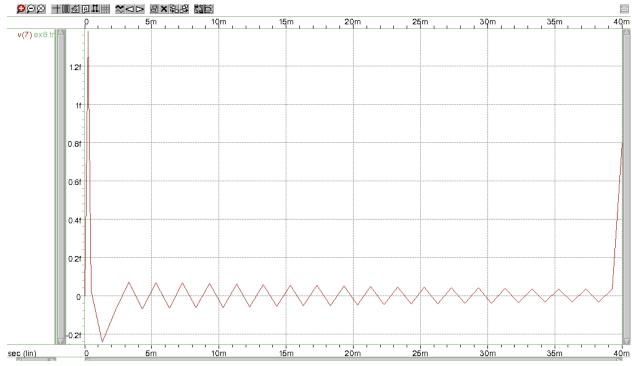
.end



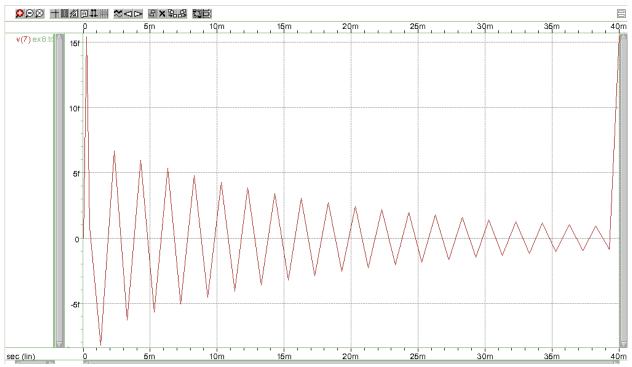
### Exercise 8

## 👱 ex8.sp 🗶

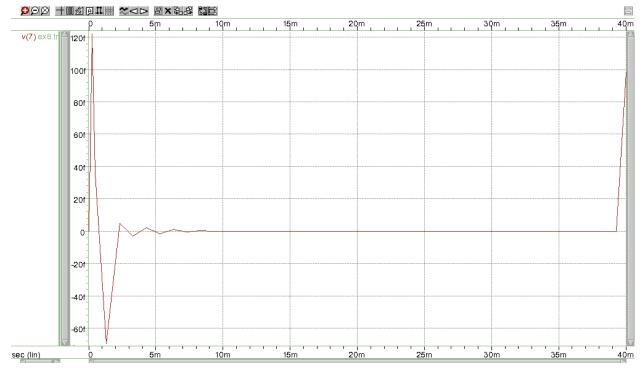
```
Exercise 8
.opt post
*MOSFET model description
.MODEL NMOS2N7000 NMOS(LEVEL=3
+Rs=0.205 NSUB=1.0e15 DELTA=0.1
+KAPPA=0.0506 TPG=1 CGD0=3.1716e-9
+RD=0.239 VTO=1 VMAX=1.0e7
+ETA=0.0223089 NFS=6.6e10 T0X=1.0e-5
+LD=1.698e-9 U0=862.425 XJ=6.4666e-7
+THETA=1.0e-5 CGS0=9.09e-9)
*Voltage Source
Vs 1 0 SIN(0V 50mV 10kHz)
V1 3 0 DC 5V
V2 6 0 DC 5V
M1 5 2 4 4 NMOS2N7000 L=2.5e-6 W=0.8e-2
Rb1 3 2 1k
Rd 6 5 1.5k
Rb2 2 0 3k
Rss 4 0 500
R1 7 0 r1val
.param r1val = 100k
C1 1 2 10e-9
C2 4 7 10e-9
.TRAN 1ms 40ms 0ms 1ms
.PLOT V(R1)
.alter
.param r1val = 10k
.alter
.param r1val = 1k
.alter
.param r1val = 100
.end
```



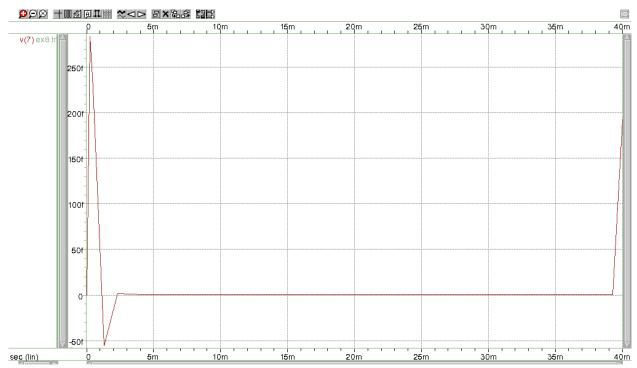




 $1 \text{ k}\Omega$ 



 $10 \text{ k}\Omega$ 



 $100~\mathrm{k}\Omega$