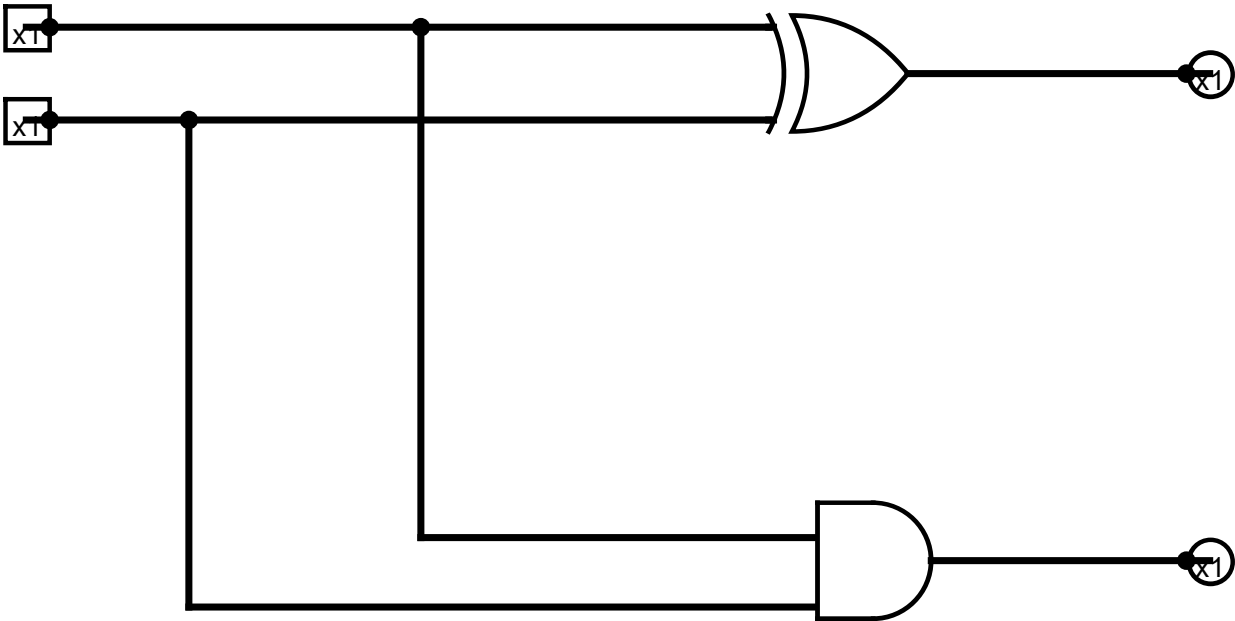
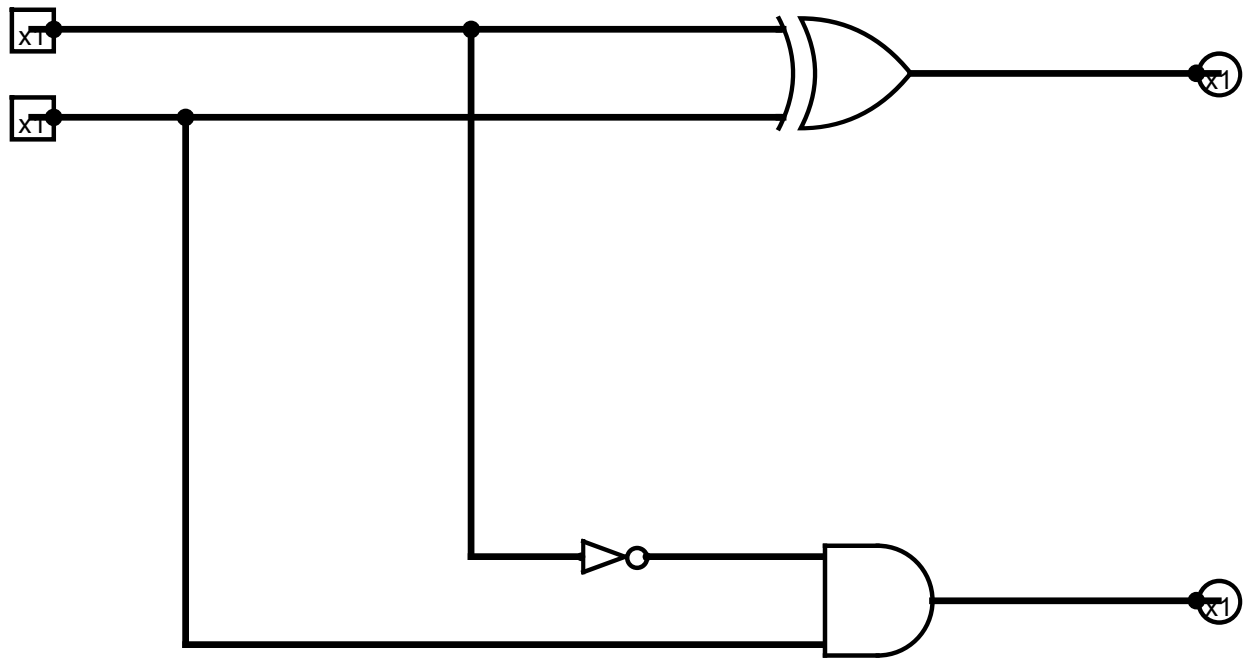


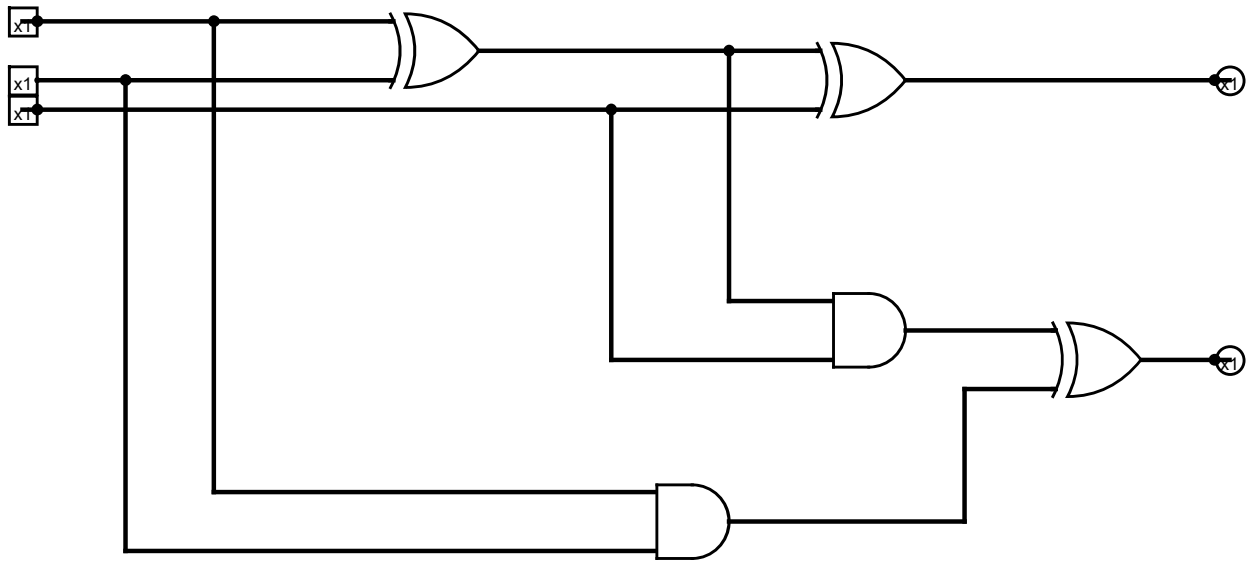
1bit half adder (1 of 12)



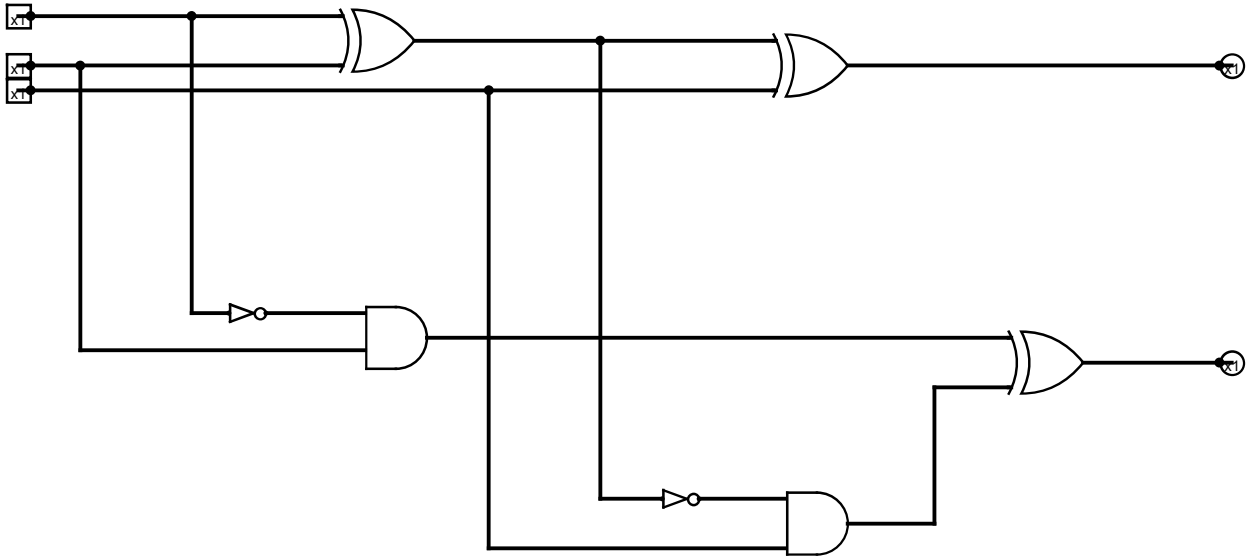
1bit half subtractor (2 of 12)



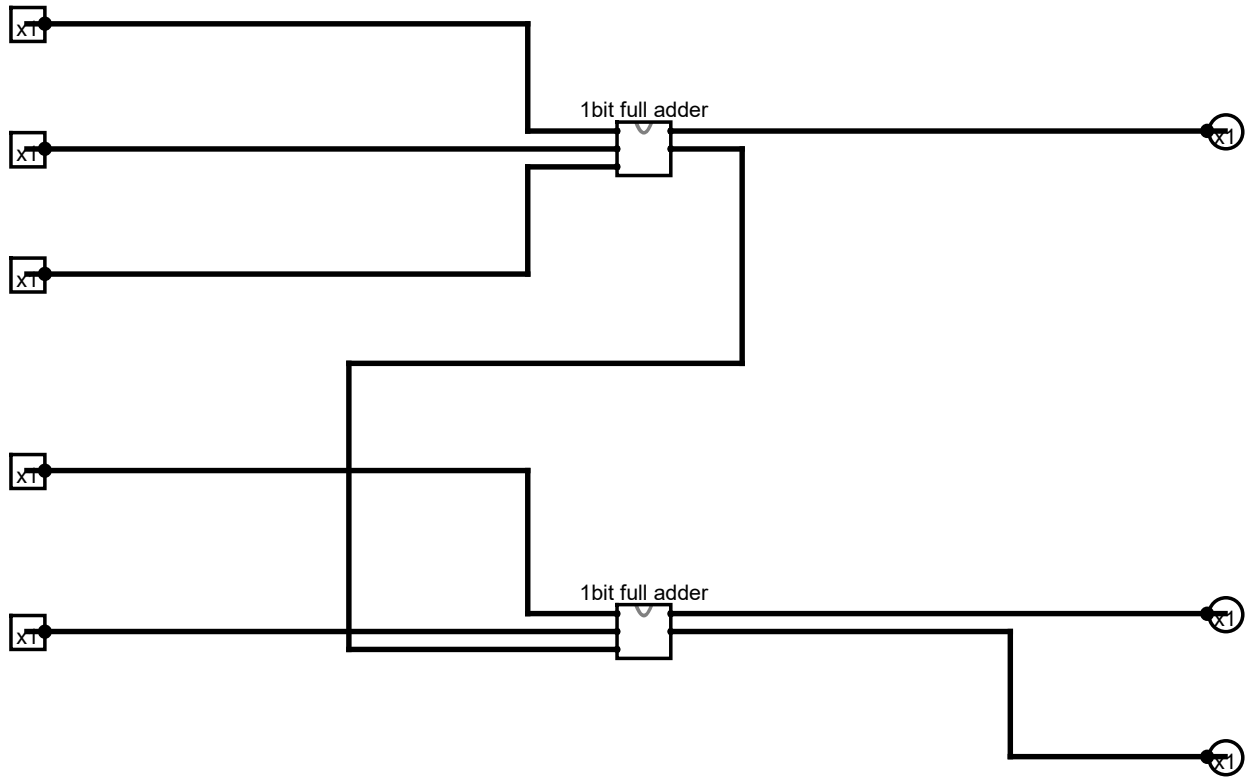
1bit full addder (3 of 12)



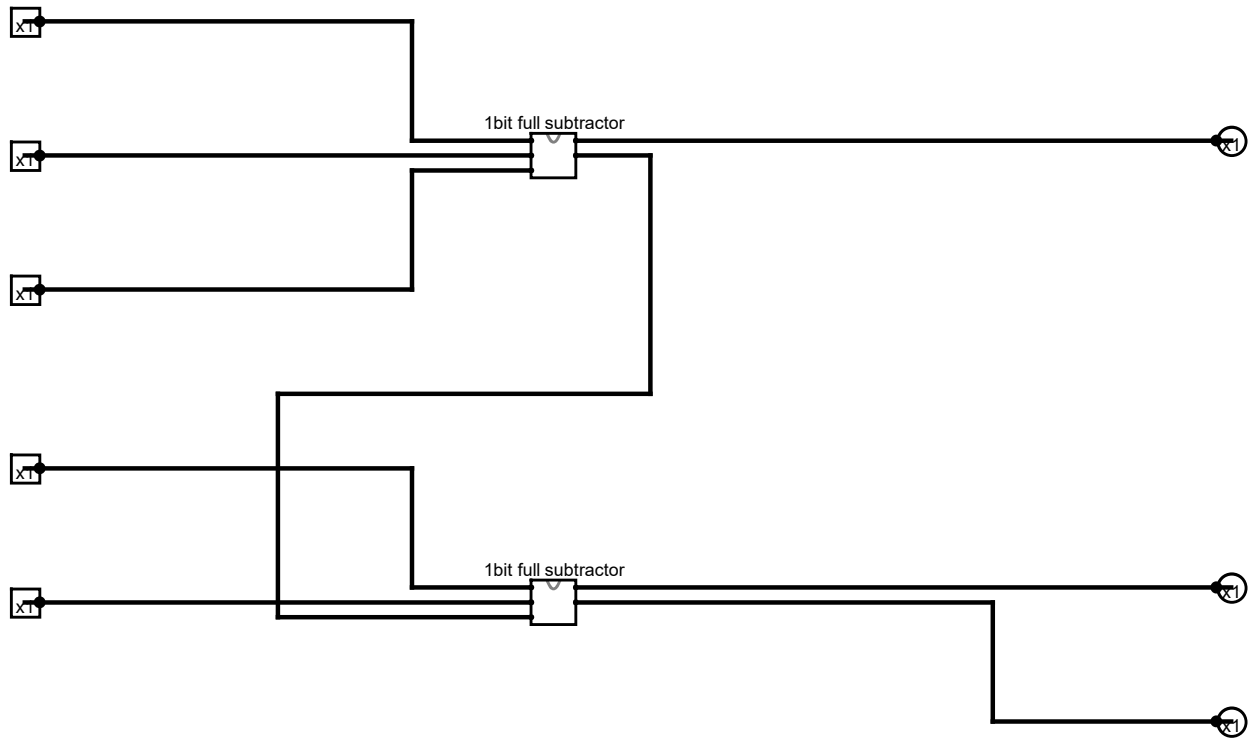
1bit full subtractot (4 of 12)



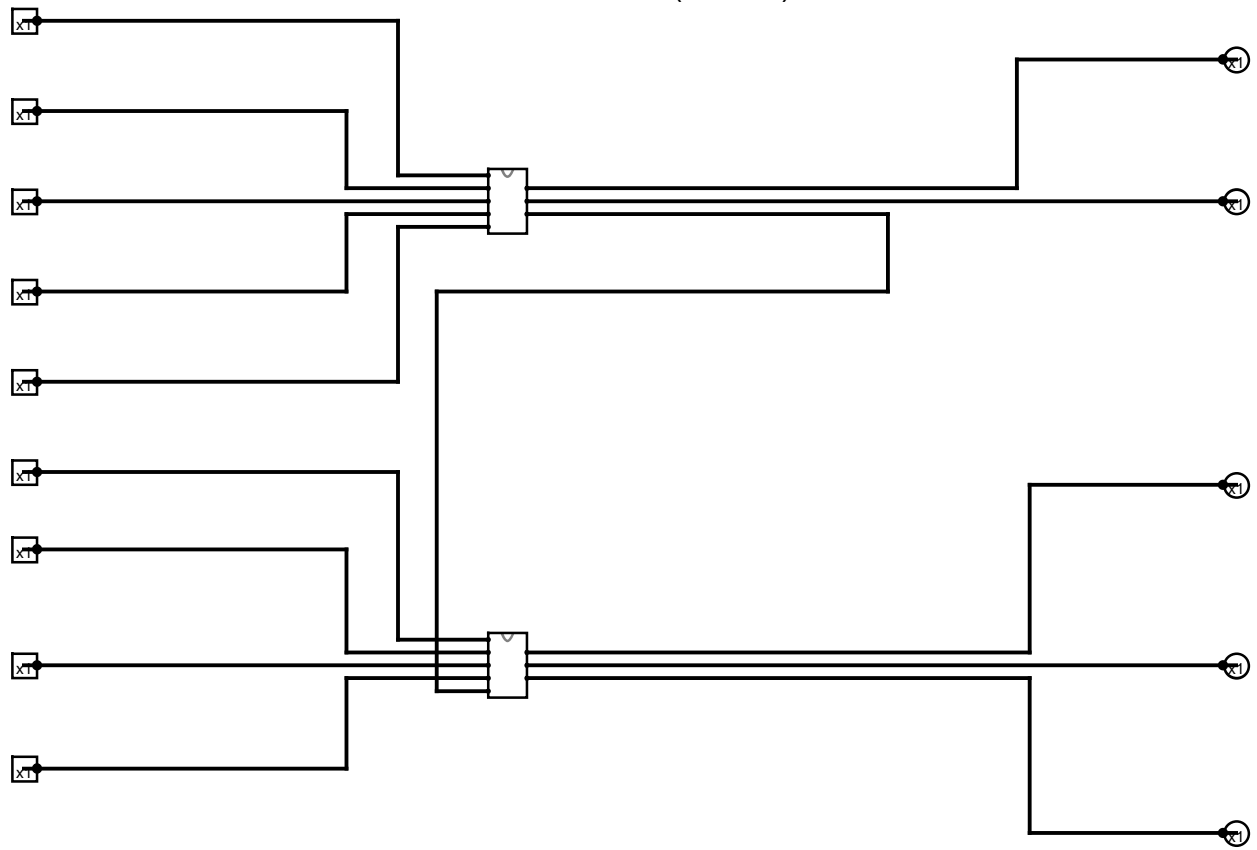
2bit full adder (5 of 12)



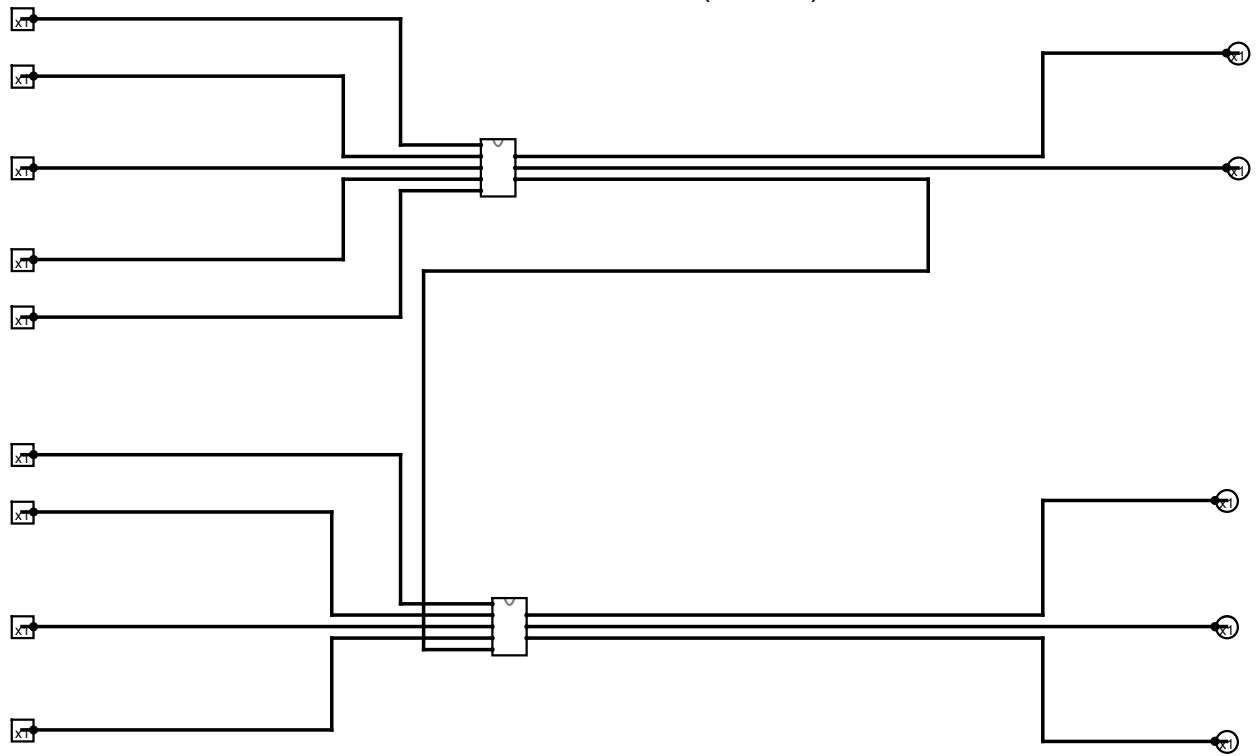
2bit full subtractor (6 of 12)



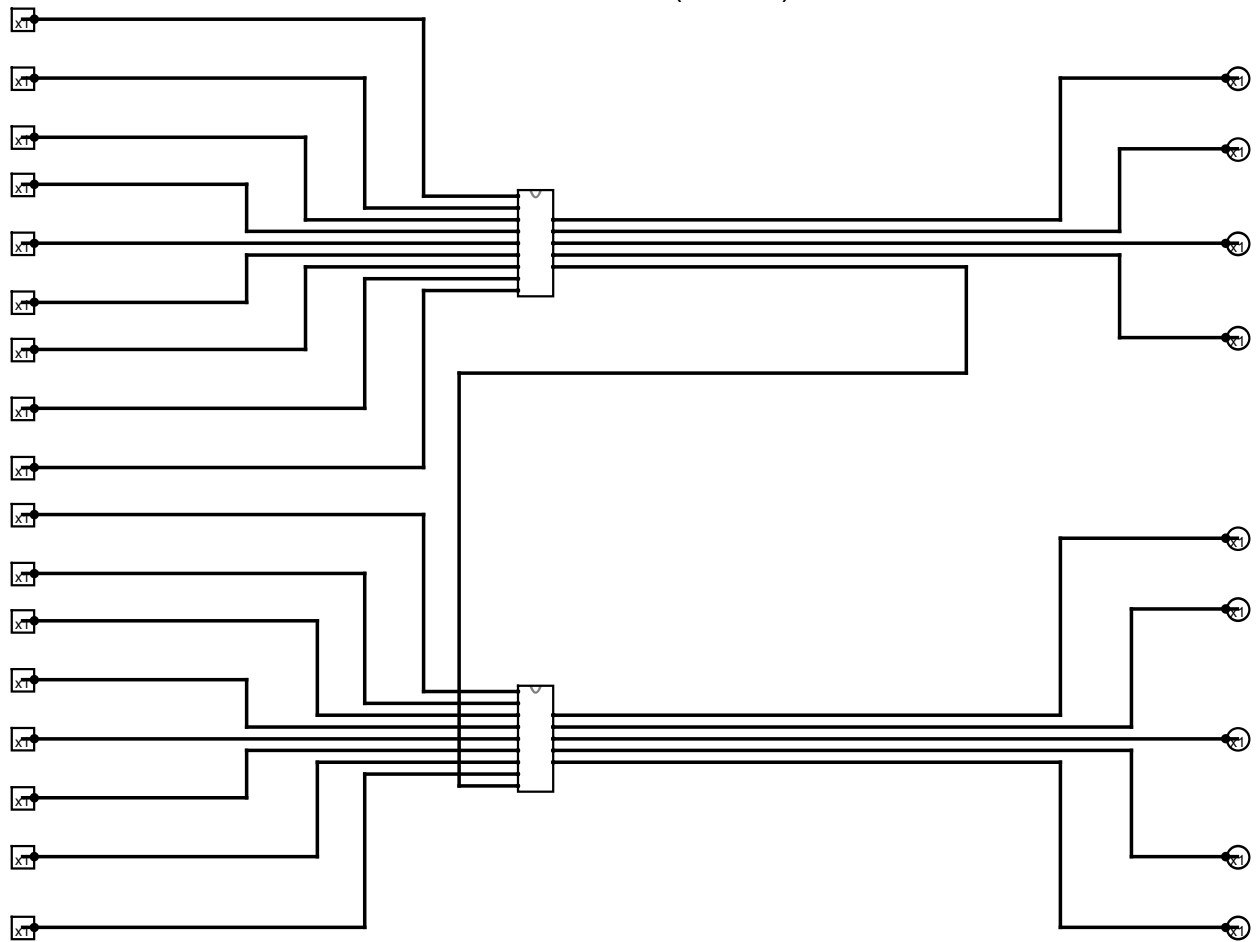
4bit full adder (7 of 12)



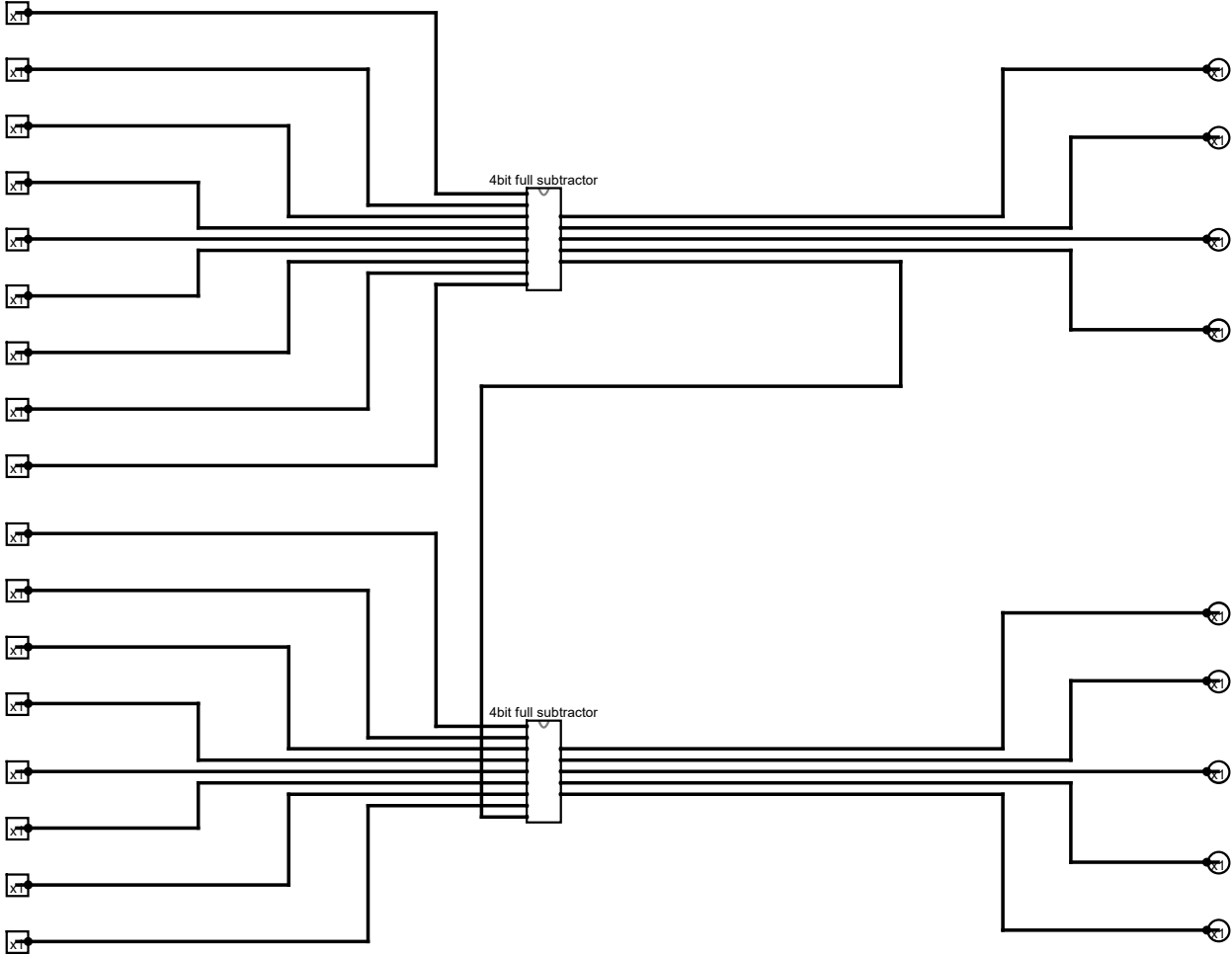
4bit full subtractor (8 of 12)



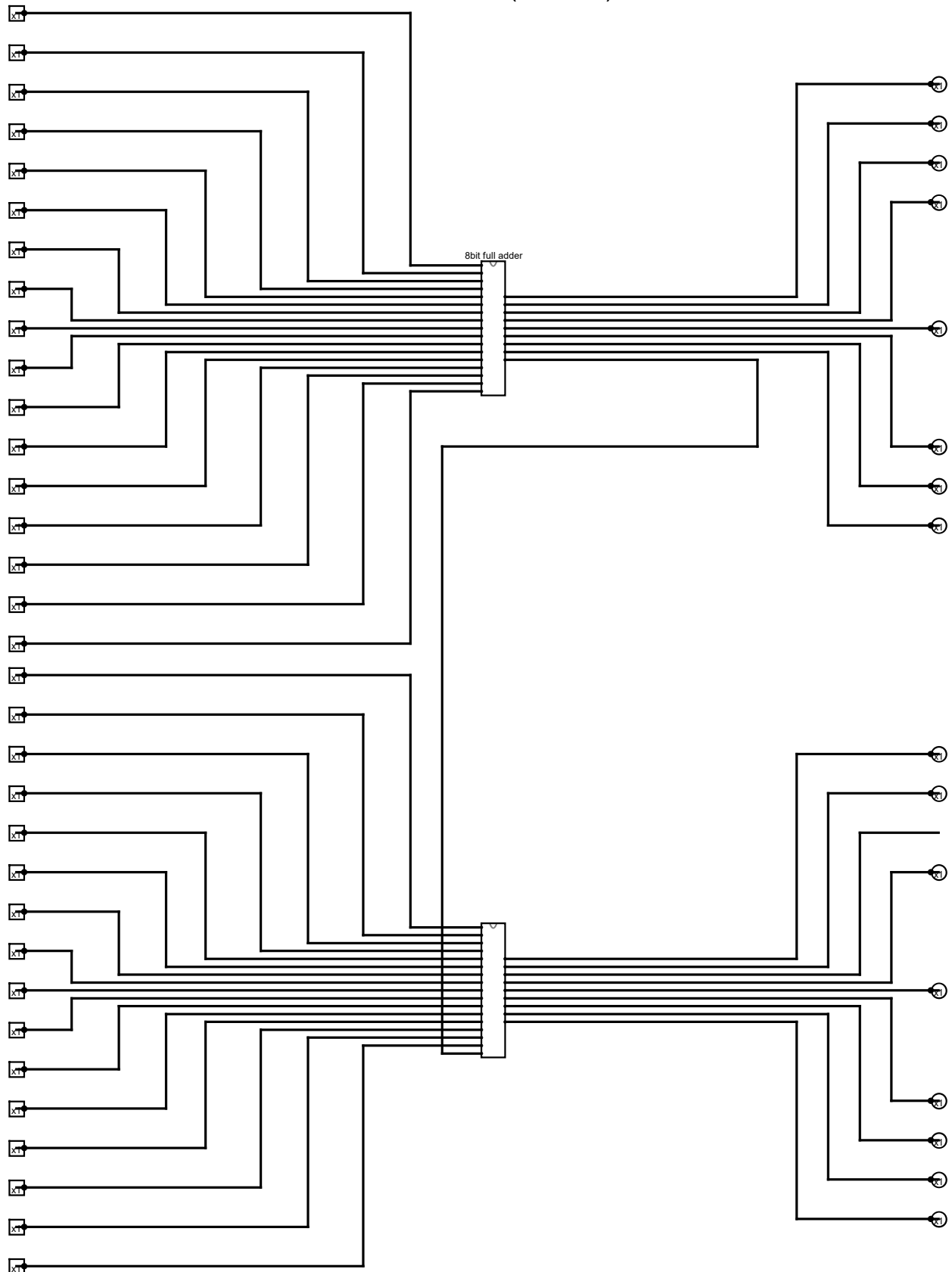
8bit full adder (9 of 12)



8bit full subtractor (10 of 12)



16bit full adder (11 of 12)



16bit full subtractor (12 of 12)

