

Lab 6 Report

If I was permitted to use Yacc's built in operator precedence module, the operators portion of my specification would obviously be affected. In my submission, I have separated all the operators by precedence starting with the highest (multiplication/division/modulo), going all the way down to the lowest precedence bitwise or operation. The changed specification would allow me to reduce these rules to one rule which contained all the operators after defining the precedence (and associativity) of the operators at the top of the file.

This would look like:

%left MUL DIV MOD

%left ADD SUB

%left LSH RSH

... and so on.

Later in the specification, I would then define these 'operators' in the same rule, and they would have their respective precedences.