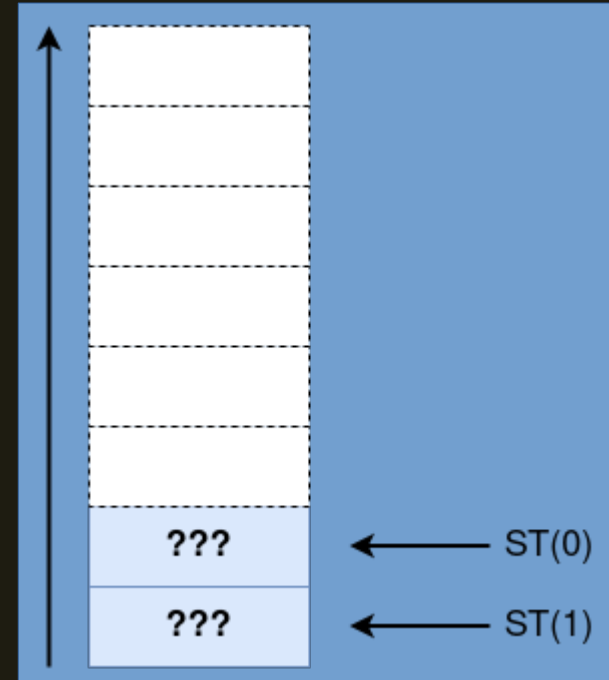


x87-FPU-Stack

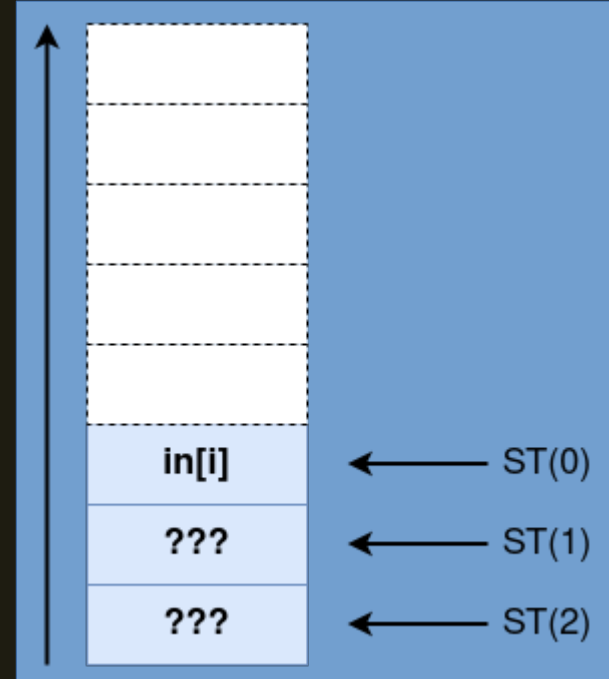
→
`fld dword ptr [rbx+rcx]
fld dword ptr [rax+rcx]
fmulp st(1), st(0)
fstp dword ptr [rax+rcx]
jnz my_label1`



x87-FPU-Stack



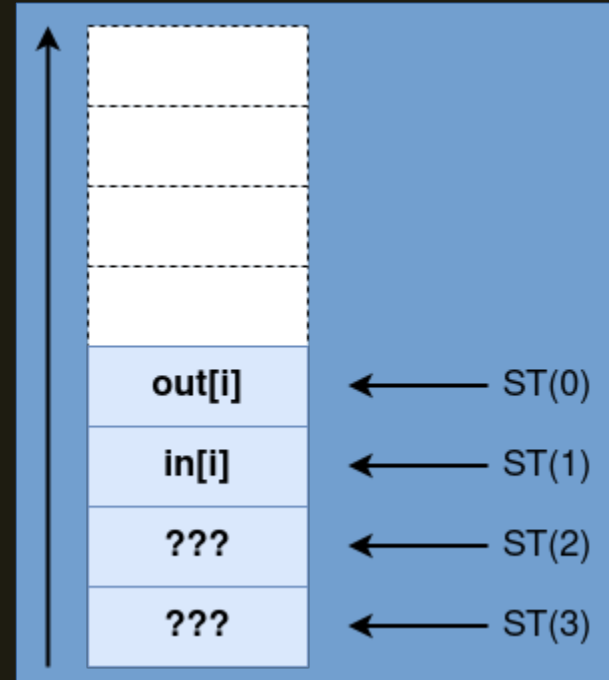
```
fld dword ptr [rbx+rcx]
fld dword ptr [rax+rcx]
fmulp st(1), st(0)
fstp dword ptr [rax+rcx]
jnz my_label1
```



x87-FPU-Stack



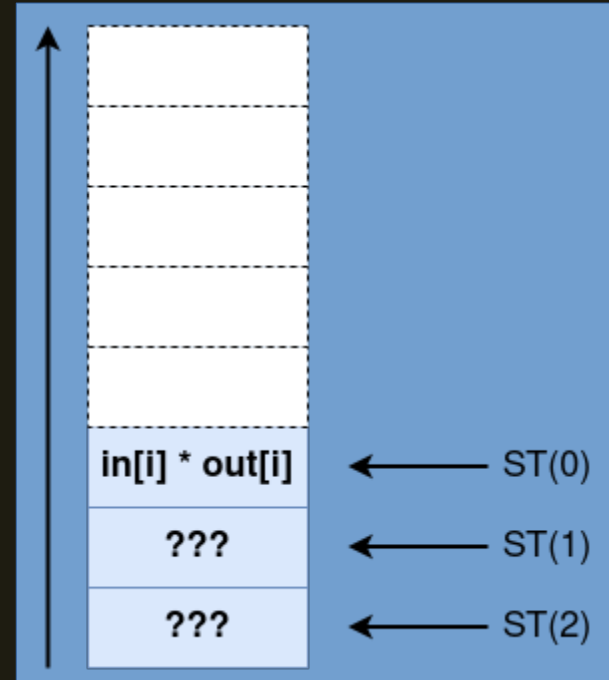
```
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x87-FPU-Stack



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x87-FPU-Stack



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