

Jayant Mukundsing Patil

Embedded Software Engineer



✉ jayantp000252@gmail.com

☎ +4915753647929

📍 Weingarten, Germany

🌐 [linkedin.com/in/jayant-patil252](https://www.linkedin.com/in/jayant-patil252)

EDUCATION

M.ENG. ELECTRICAL ENGINEERING AND EMBEDDED SYSTEMS

Hochschule Ravensburg-Weingarten

09/2023 - Present

Weingarten, Germany.

BACHELORS OF ENGINEERING IN ELECTRONICS AND TELECOMMUNICATION

Savitribai Phule Pune University

08/2015 - 07/2018

Nashik, India.

WORK EXPERIENCE

Work Student

Zoller + Fröhlich GmbH

04/2024 - Present

Wangen in Allgäu, Germany.

Achievements/Tasks

- Responsible for development of Laser Control Software using C++.
- Testing of Implemented Features.
- Debugging the issues assigned by senior colleague.

SENIOR SOFTWARE ENGINEER

BOSCH GLOBAL SOFTWARE TECHNOLOGIES PRIVATE LIMITED

03/2022 - 08/2023

Bangalore, India.

Tasks

- Function Ownership responsibility of Automated Parking brake for one of the leading German OEM.
- Evaluated the requirements shared by the customer and deriving the requirement for Bosch systems.
- Analyzed of the functional safety aspects of the customer requirements.
- Responsible for Software development, Integration.
- Responsible for debugging defects, providing impact and risk analysis.
- Validated the software by LabCar (HIL) test and Evaluation of traces from vehicle.

SENIOR ENGINEER

TATA ELXSI

09/2019 - 02/2022

Pune, India.

Tasks

- Responsible for firmware development on the NVIDIA TX2 tegra processor with Linux OS using C/C++.
- Worked on multi-threading application and designed functionality of peripherals.
- Designed Power-on Self-Test for device, DRV8830 Motor driver and functionality of other peripherals.
- Analyzed the issues and bugs reported by clients and the testing team and worked on bug fixes.
- Designed the firmware requirement specifications, design document, flowcharts and UML diagrams for application design.

SKILLS

C

C++

Data structure

Embedded C

Linux

Embedded Operating System

RTOS

SystemC

ARM cortex-M

VHDL

IOT

CAN

I2C

Agile Methodologies

ISO26262

ASPICE

PERSONAL PROJECTS

Vehicle Emission Monitoring System. (2019)

- Developed a system to monitor and track vehicle emissions in real-time using sensor data, an STM32 microcontroller, and the CAN protocol.
- **Individual Role:** Led the development of the STM32 interface with other electronic components and contributed to the coding in a team of three.

Deep Learning Based Indian Currency Coin Recognition. (2018)

- Designed a system for automated coin recognition using deep learning techniques on a Raspberry Pi controller.
- **Individual Role:** Contributed to the development of the system in a team of three, specifically responsible for flashing the controller with Raspbian OS and developing the Python code to process captured coin images.

ACHIEVEMENTS

IEEE Paper Publication

A. U. Tajane, J. M. Patil, A. S. Shahane, P. A. Dhulekar, S. T. Gandhe and G. M. Phade, "Deep Learning Based Indian Currency Coin Recognition," 2018 International Conference On Advances in Communication and Computing Technology (ICACCT), Sangamner, India, 2018, pp. 130-134, doi: 10.1109/ICACCT.2018.8529467.

EXTRA-CURRICULAR ACTIVITIES

Contributed to the National level event "AAYAAM 2017" as a coordinator at the SITRC in Nashik.

Volunteered for the State Level Project Competition "E Tantra-2017", SITRC, Nashik.

LANGUAGES

English

Full Professional Proficiency

Marathi and Hindi

Native or Bilingual Proficiency

Deutsch

Elementary Proficiency