

## (Established under Karnataka Act No.16 of 2013)

## 100 Feet Ring Road, BSK III Stage, Bangalore-560085

## **TRANSCRIPT**

Name	JAYANTH S	Duration of the Program	4 Years
SRN	01FB16EEC111	Medium of Instruction	English
Transcript Number	UT/2016/ECE/0111	Date Of Issue	October 09, 2020
Year Of Admission	2016	Year of Completion	2020
Program Of Study	B.Tech Electronics and Comm	unication Engineering	



Program Of Study		B.Tech Electronics and Communication En					
	SI. Course Code		Course Tile	Credits	Grade	Month & Year o Completion	f
Γ	1 UE16CS101	Introduction Python	to Computing using	4	В	Dec 2016	
1	2 UE16CS102	Introduction	to Computing Using	1	S	Dec 2016	
	3 UE16EE101	Python Lat Basic Elect	rical Engineering	4	Α	Dec 2016	
	4 UE16HS102	Environmen	ntal Studies	-	AP	Dec 2016	
	5 UE16MA101	Enginering	Mathematics- I	4	Α	Dec 2016	
1	6 UE16ME101	Mechanical	Engineering Sciences	4	Α	Dec 2016	
1	7 UE16ME102	Computer A	ided Engineering Graphics	2	Α	Dec 2016	
1	B UE16PH101	Enginering	Physics	4	s	Dec 2016	I
1	9 UE16PH102	Physics Lab	poratory	1	s	Dec 2016	
1	0 UE16CS151	Problem So	Iving With C	4	Α	May 2017	
1	1 UE16CS152	Problem So	lving With C Laboratory	1	Α	May 2017	
1	2 UE16CV101	Engineering	Mechanics	4	В	May 2017	١
1	3 UE16CY101	Engineering	Chemistry	4	Α	May 2017	١
1	4 UE16CY102	Chemistry L	aboratory	1	Α	May 2017	l
1	5 UE16EC101	Basic Electr	onics Engineering	4	В	May 2017	l
1	6 UE16HS101		of India and Professional	-	AP	May 2017	l
1	7 UE16JE101	Ethics Unified Engi	neering	2	Α	May 2017	
18	B UE16MA151	Engineering	Mathematics - II	4	В	May 2017	
19	9 UE16EC201	Network Ana	alysis and Synthesis	4	А	Dec 2017	
20	UE16EC202	Electronic D	evices and Circuits	4	s	Dec 2017	
2	1 UE16EC203	Logic Design	ı	4	s	Dec 2017	
22	2 UE16EC204	Signals & Sy	rstems	4	S	Dec 2017	
23	3 UE16EC205		evices and Circuits	1	s	Dec 2017	
24	UE16EC206	Laboratory Logic Design	Laboratory	1	s	Dec 2017	
25	UE16EC207	Signals & Sy	stems Laboratory	1	s	Dec 2017	
26	UE16MA201	Engineering	Mathematics -III	4	Α	Dec 2017	
27	UE16EC251	Linear Integr	ated Circuits	4	s	May 2018	
28	UE16EC252	Microcontroll	ers	4	s	May 2018	
29	UE16EC253	Digital Signa	Processing	4	Α	May 2018	
30	UE16EC254		etic Field & Transmission	4	Α	May 2018	
31	UE16EC255	Lines Linear Integra	ated Circuits Laboratory	1	s	May 2018	
32	UE16EC256	Microcontroll	ers Laboratory	1	s	May 2018	
33	UE16EC257	Digital Signal	Processing Laboratory	1	s	May 2018	
34	UE16MA251	Linear Algebi	a and its Applications	4	s	May 2018	
35	UE16EC301	Communicati	on Engineering	4	s	Dec 2018	
36	UE16EC302	Microwave E	ngineering	4	А	Dec 2018	

SI. No	Course Code	Course Tile	Credits	Grade	Month & Year of Completion
42	UE16CS333	Natural Language Processing		Α	May 2019
43	UE16EC347	Machine Learning	4	В	May 2019
44	UE16EC351	Computer Networks	4	S	May 2019
45	UE16EC352	Digital Communication	4	s	May 2019
46	UE16EC353	Digital System Design	4	Α	May 2019
47	UE16EC354	Digital Communication Laboratory	1	S	May 2019
48	UE16EC355	Digital System Design Laboratory	1	S	May 2019
49	UE16EC357AA	Select Topics in Data Science	2	Α	May 2019
50	UE16EC401	Fundamentals of Antennas	4	s	Dec 2019
51	UE16EC402	Embedded Systems Design	4	s	Dec 2019
52	UE16EC403	Analog Integrated Circuits	4	Α	Dec 2019
53	UE16EC426	Speech Processing	4	s	Dec 2019
54	UE16EC431	Research Project	4	S	Dec 2019
55	UE16EC451H	Multimedia Communication	2	s	July 2020
56	UE16EC490	Project Work	14	s	July 2020

Credits	176	CGPA	9.41

Grade: S - 10 (Outstanding), A - 9 (Excellent), B - 8 (Very Good), C - 7 (Good), D - 6 (Fair), E - 5 (Satisfactory), AP - Audit Pass Class: >= 9.50 - First Class With Honors, >= 7.75 & 9.50 - First Class With Distinction, >= 6.75 & 7.75 - First Class, >= 5.75 & 6.75 - Second Class, >= 5/0 & 5.75 - Pass Class

UE16EC303

UE16EC304

UE16EC305

UE16EC316

UE16EC326

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VLSI Design

Microwave Engineering Laboratory

VLSI Design Laboratory

Digital Image Processing

Artificial Neural Networks

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Dec 2018

Dec 2018

Dec 2018

Dec 2018

Dec 2018

CONTROLLER PE EXAMINATIONS

PES University 100 Feet Ring Road, BSK III Stage Bangalore – 560 085



(Established under Karnataka Act No. 16 of 2013) 100 Feet Ring Road, BSK III Stage, Bengaluru - 560 085

## **CERTIFICATE FOR MINOR COURSE**



It is certified that JAYANTH S bearing SRN 01FB16EEC111 has fulfilled the requirement of **Minors in Computer Science and Engineering** by successfully passing the courses listed below:

SI. No	Course Code	Course Tile	Credits	Grade Awarded	Month & Year of Completion
1	UE14CS160	Data Structures	4	Α	Dec 2017
2	UE14CS162	Data Structures Laboratory	1	S	Dec 2017
3	UE16CS180	Data Base Management Systems	4	Α	May 2018
4	UE16CS182	Data Base Management Systems Lab	1	S	May 2018
5	UE16CS240	Operating Systems	4	Α	May 2019
6	UE16CS242	Operating Systems Laboratory	1	A	May 2019
7	UE16CS280	Design And Analysis Of Algorithms	4	A	Dec 2018
8	UE16CS282	Design And Analysis Of Algorithms Laboratory	1	А	Dec 2018

This certificate is issued under the seal and signature of the undersigned.

Controller of Examinations
Controller of Examinations

100 Feet Ring Road, BSK III Stage Bangalere -- 560 085 Date: November 8, 2020

Place: Bengaluru

PES

Vice-Chancellor
Vice-Chancellor
PES University
100 Feet Ring Road, BSK III Stage
Bengaluru-560085.