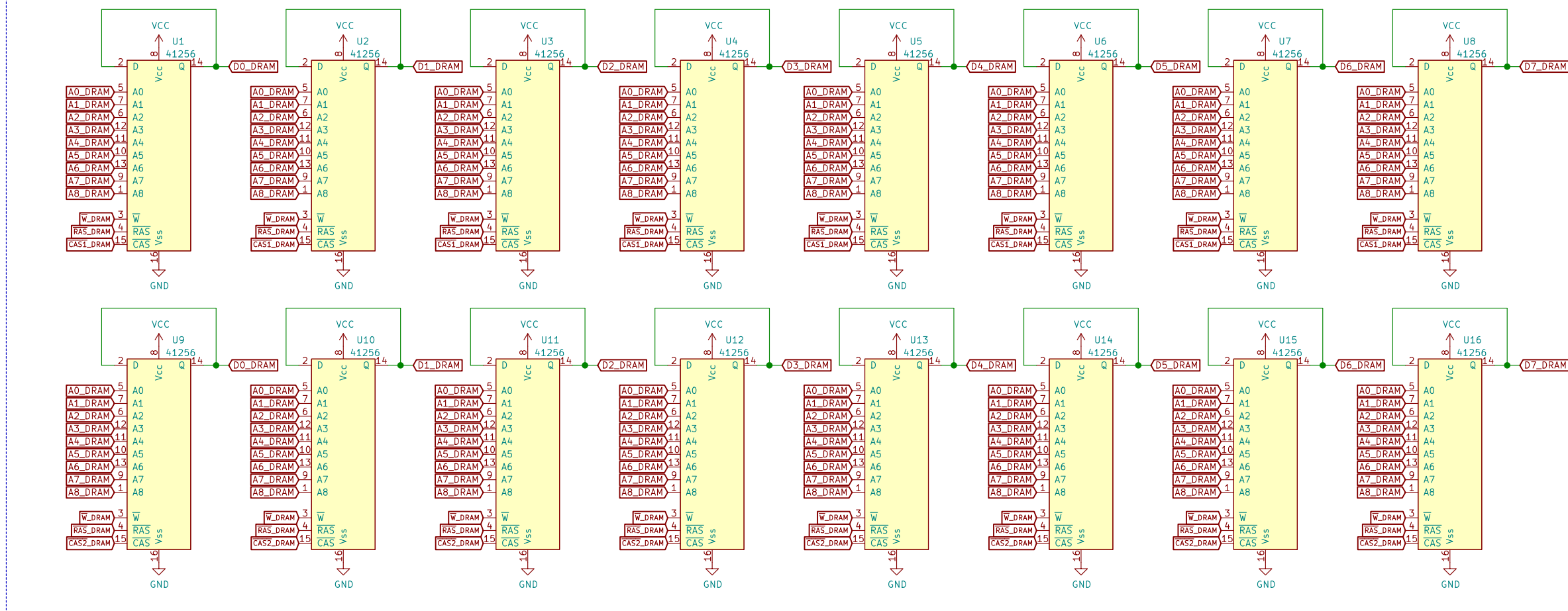
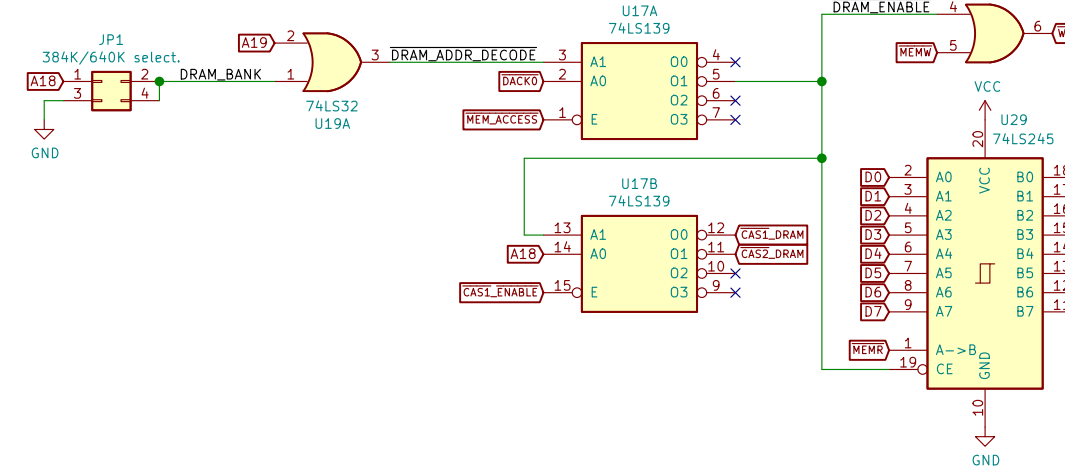


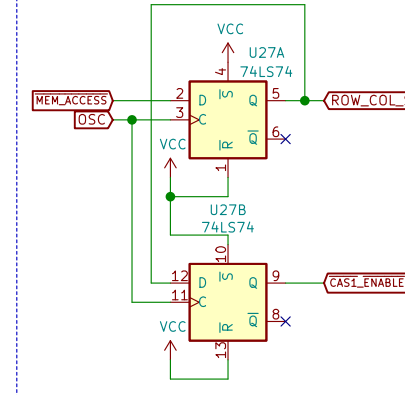
## DRAM



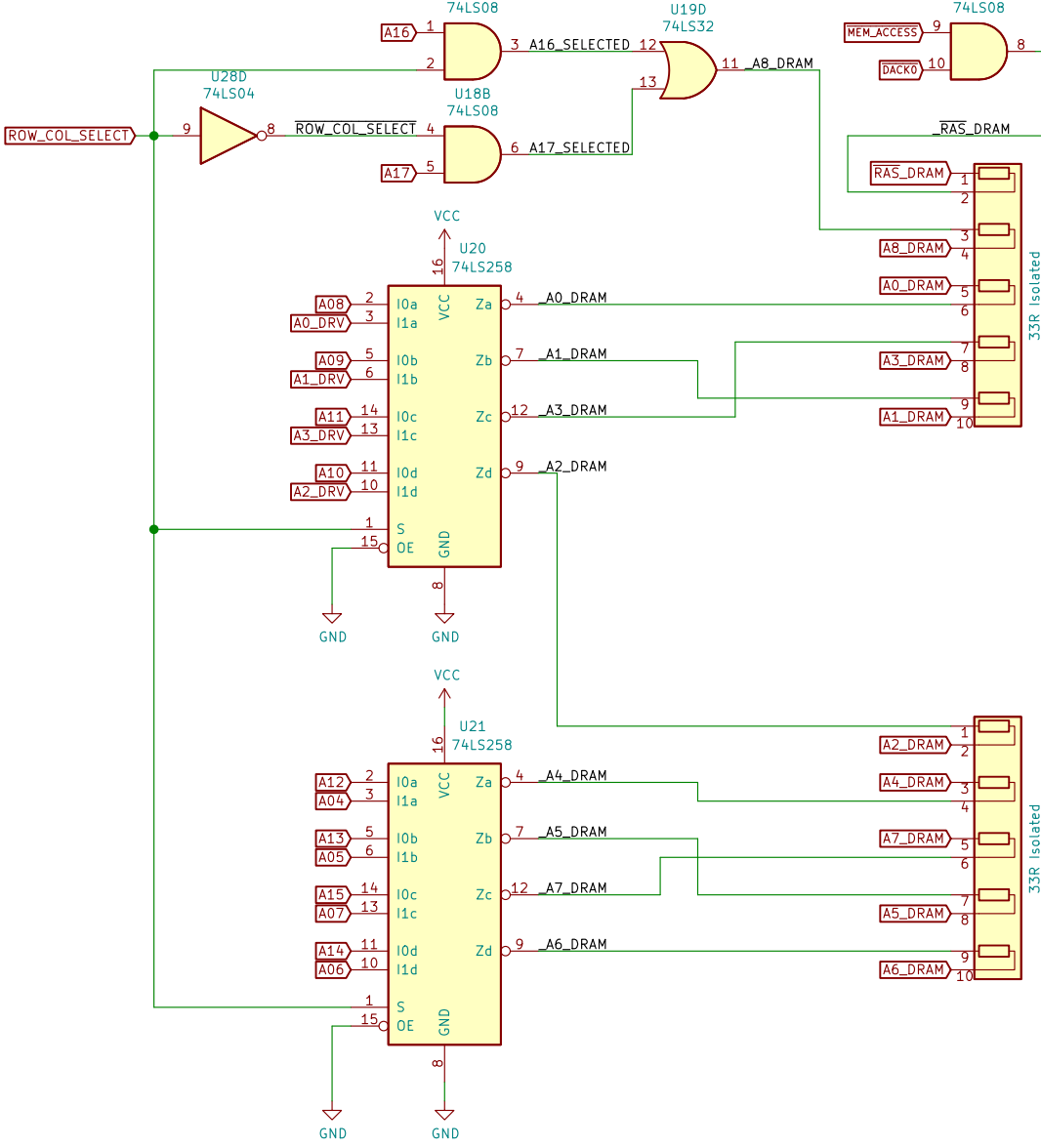
## DRAM CONTROL



## DRAM TIMING

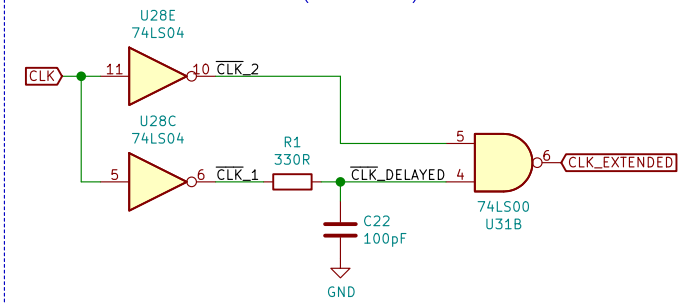


## DRAM MULTIPLEXING

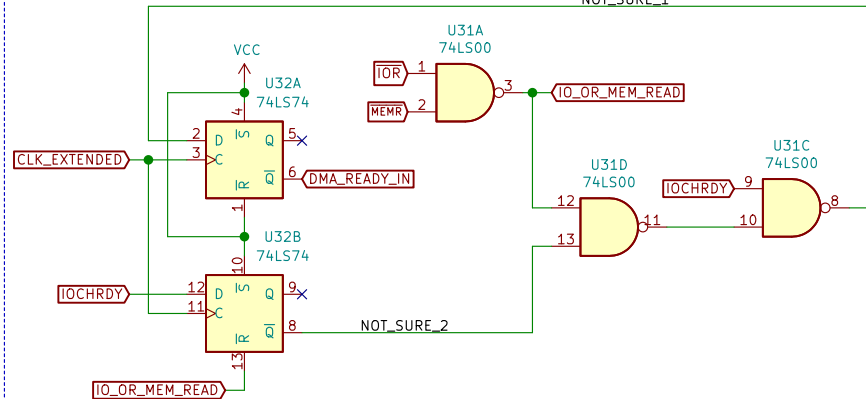


## DMA

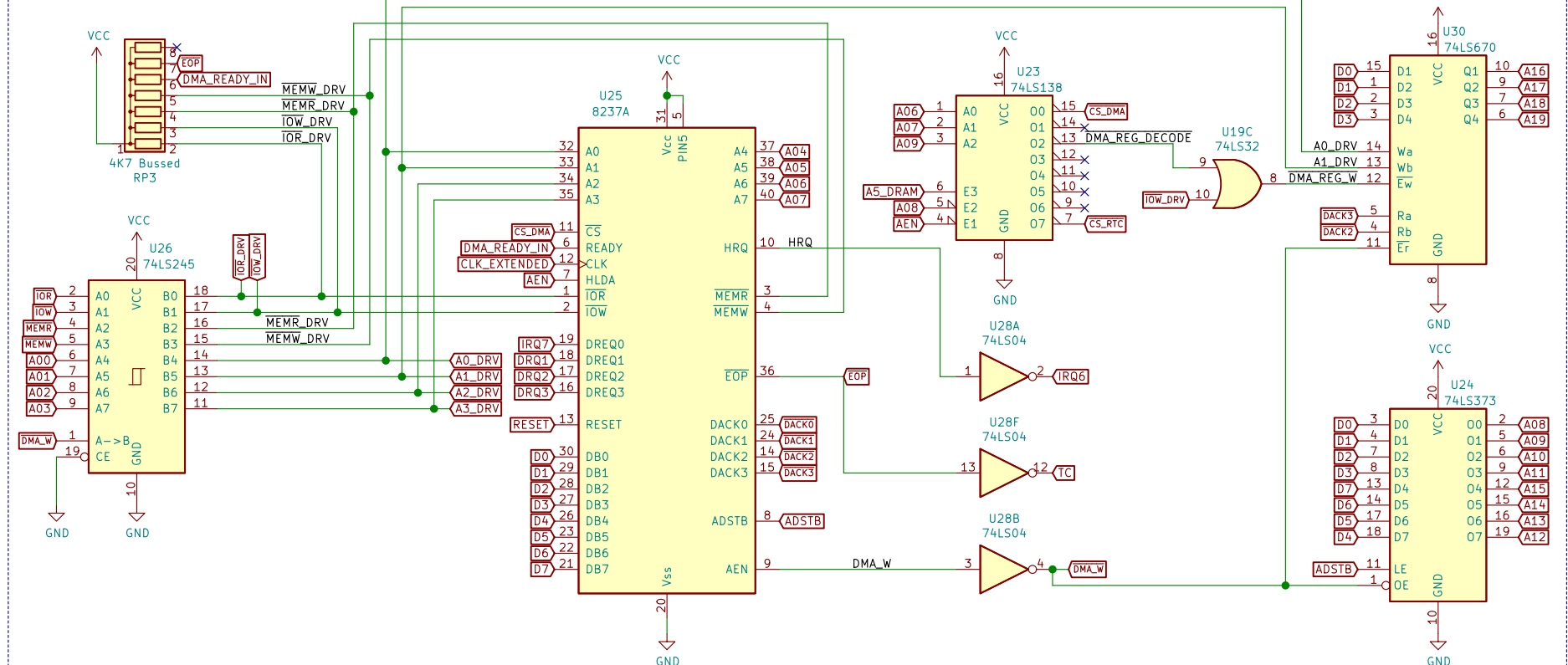
### CLOCK HIGH EXTENSION (FOR DMA)



### DMA TIMING



## DMA



## ISA BUS

