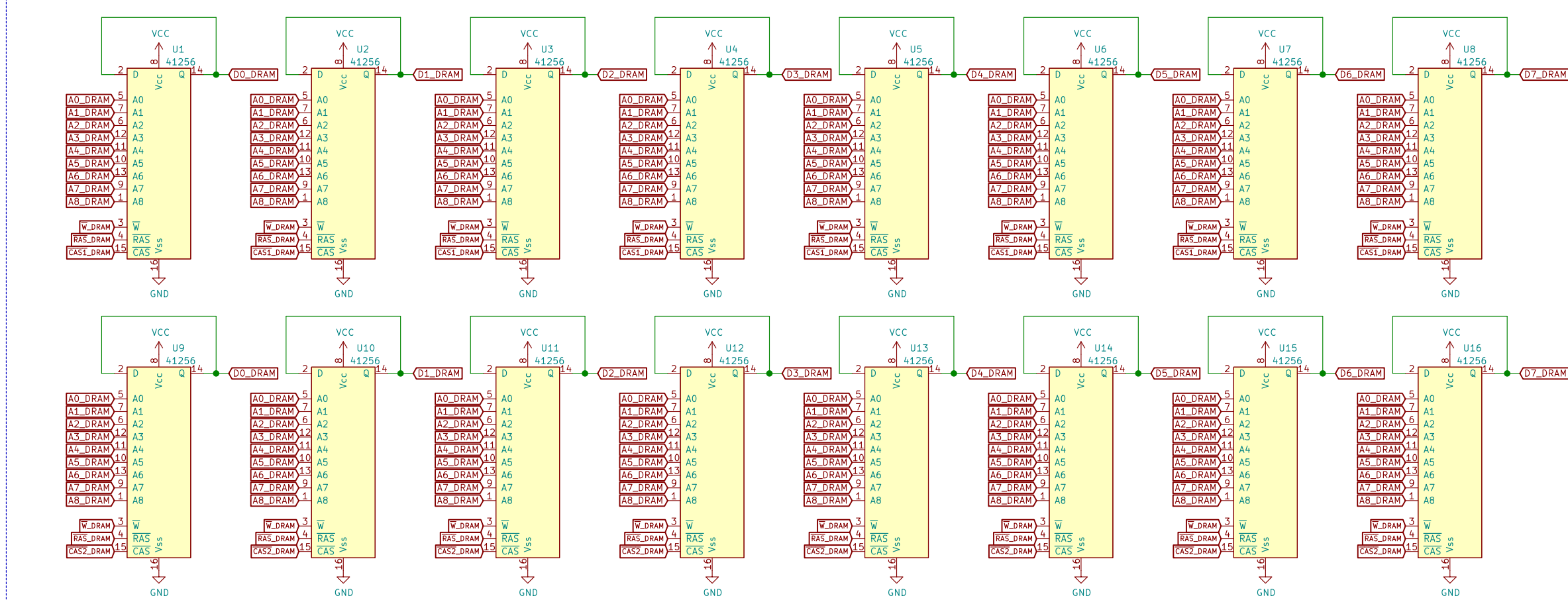
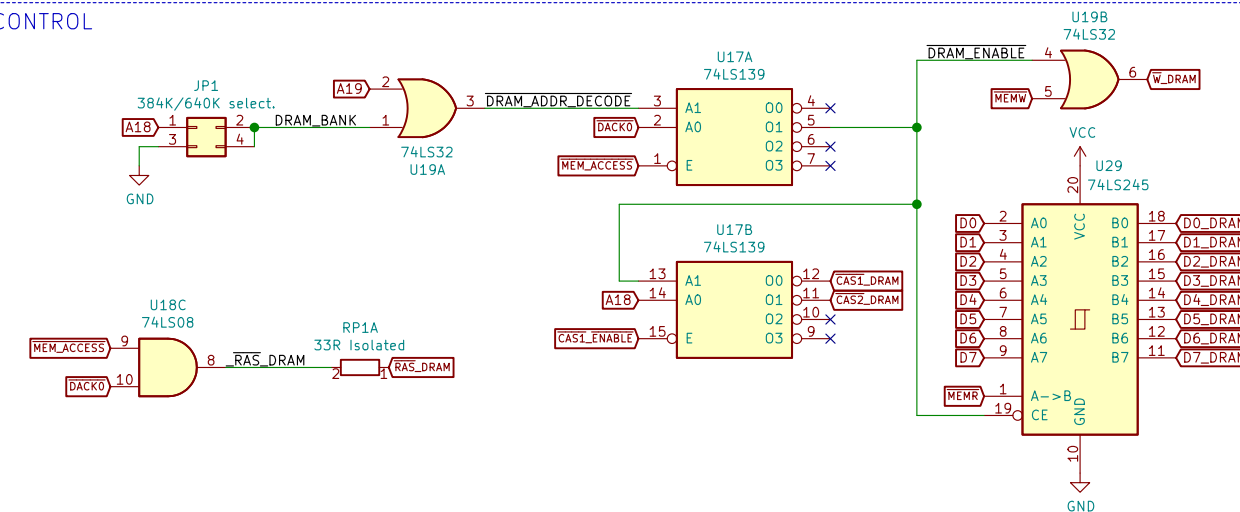


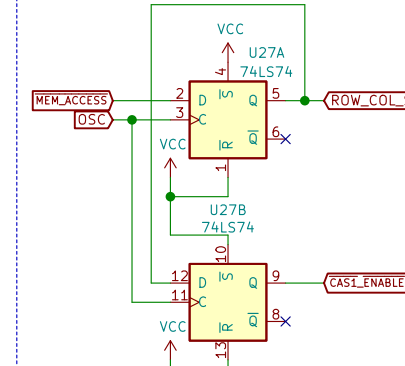
## DRAM



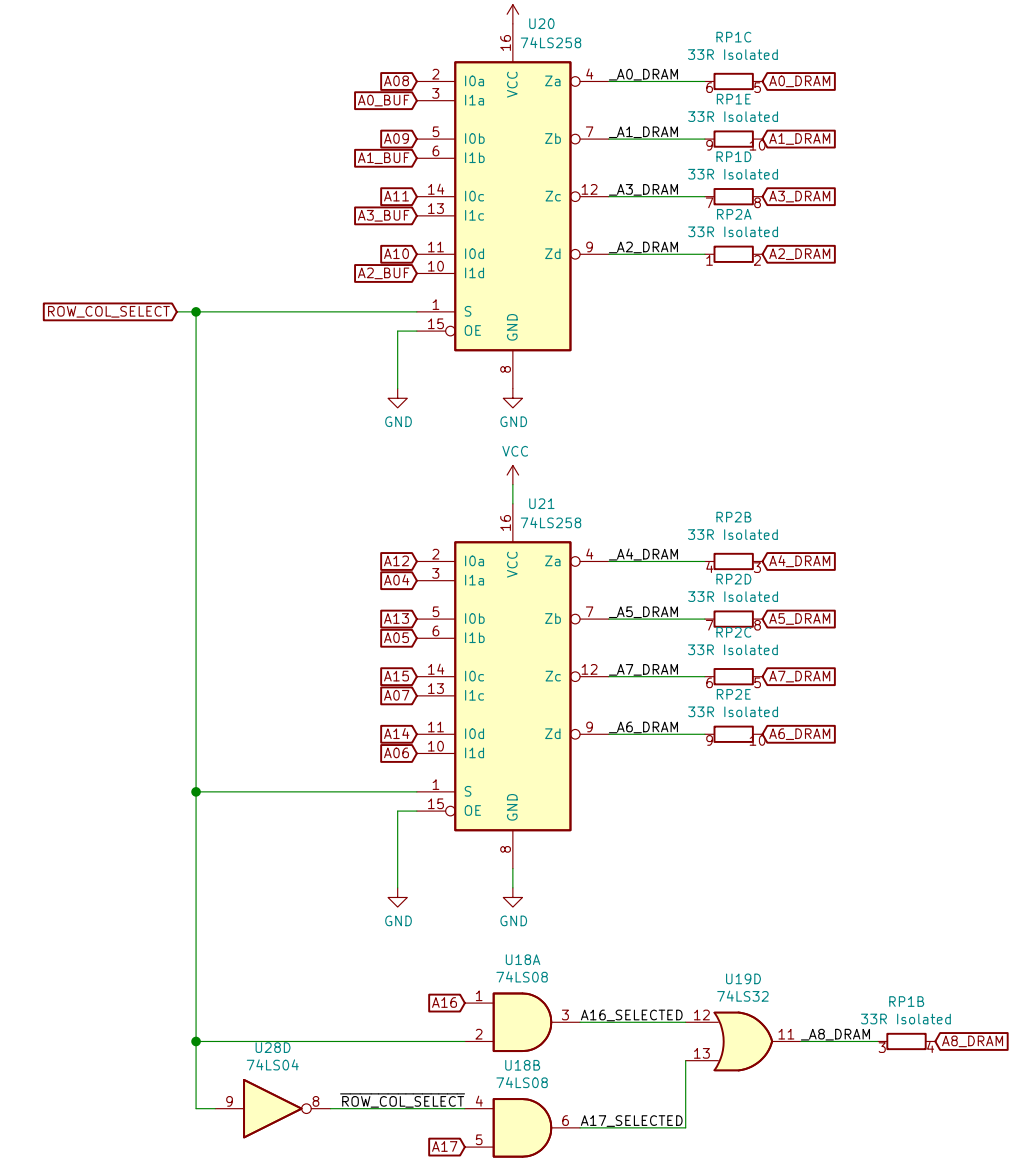
## DRAM CONTROL



## DRAM TIMING

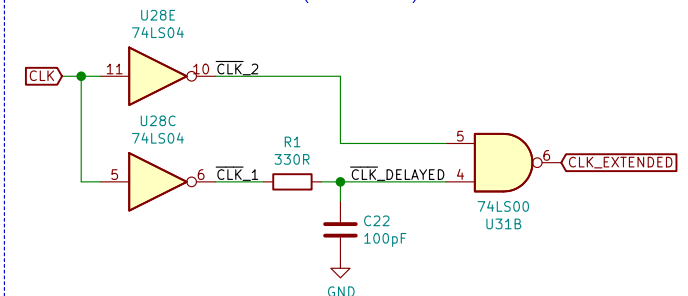


## DRAM MULTIPLEXING

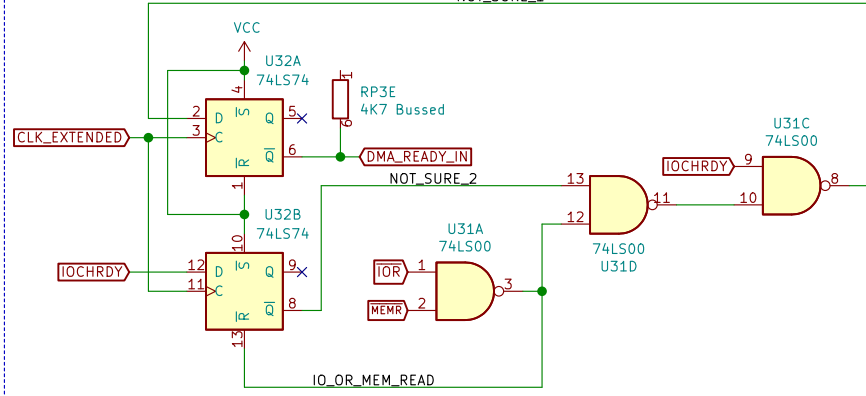


## DMA

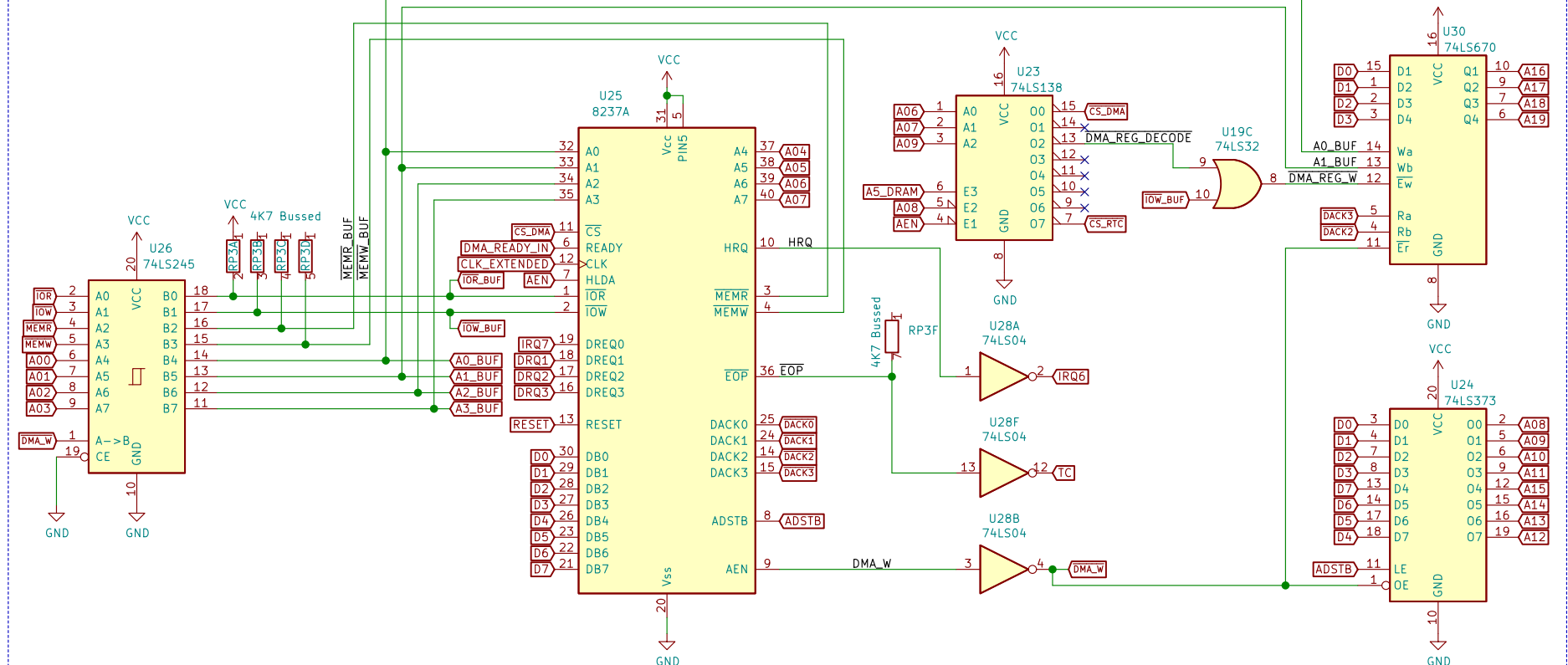
### CLOCK HIGH EXTENSION (FOR DMA)



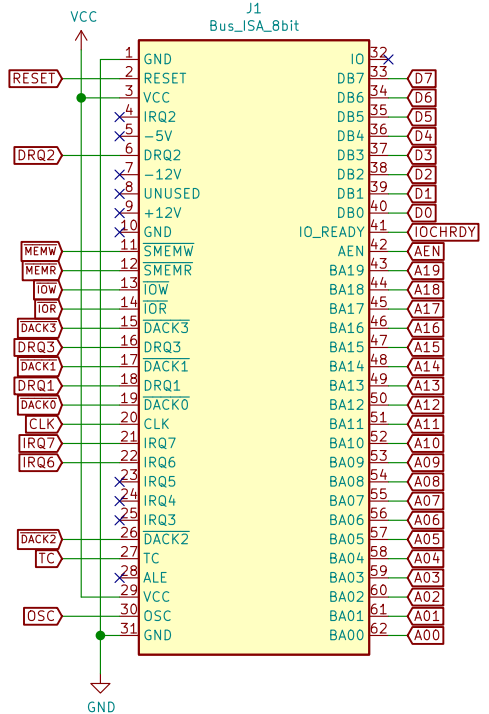
### DMA TIMING



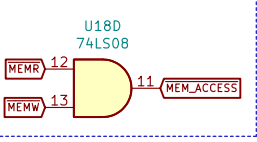
## DMA



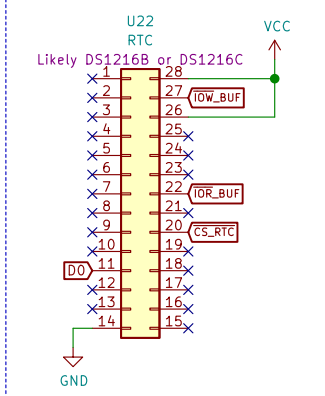
## ISA BUS



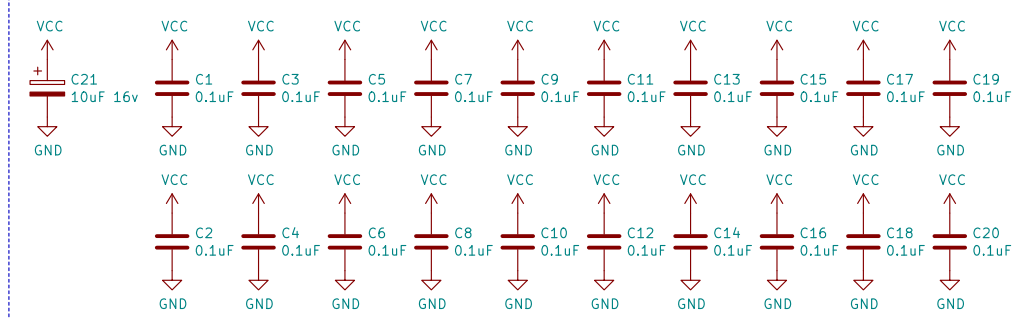
## COMMON



## REAL TIME CLOCK



## POWER FILTERING



## LOGIC POWER

