# Cpr E 281 HW07

**ENGINEERING IOWA STATE UNIVERSITY** 

#### Arithmetic Circuits and Combinational-Circuit Building Blocks Assigned Date: Eighth Week Due Date: First class of 9th week

P1. (6 points) Negate the following binary numbers in 4-bit 2's complement representation:

(Remark: Negate means you find the negative of the number.)

(a) 0001;

(b) 1100;

(c) 0111

To negate any number in 2's complement, flip all the bits and then add 1.

(a) 1110+1 = 1111

(b) 0011+1 = 0100

(c) 1000+1 = 1001

P2.

Construct the truth table

$x_{n-1}$	$y_{n-1}$	$c_{n-1}$	$c_n$	$s_{n-1}$ (sign bit)	Overflow
0	0	0	0	0	0
0	0	1	0	1	1
0	1	0	0	1	0
0	1	1	1	0	0
1	0	0	0	1	0
1	0	1	1	0	0
1	1	0	1	0	1
1	1	1	1	1	0

Note that overflow cannot occur when two numbers with opposite signs are added. From the truth table overflow expression is

$$Overflow = \overline{c}_n c_{n-1} + c_n \overline{c}_{n-1} = c_n \oplus c_{n-1}$$

P3. (6 points) Give the 4-bit 2's complement representation for the following decimal numbers:

(a) -6;

(b) -1;

(c) 6

(a)

 $-6 = 1010_2$ 

(b)  $-1 = 1111_2$ 

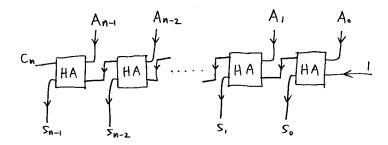
 $6 = 0110_2$ (c)

## Cpr E 281 HW07 SOLUTION

ELECTRICAL AND COMPUTER
ENGINEERING
IOWA STATE UNIVERSITY

#### Arithmetic Circuits and Combinational-Circuit Building Blocks Assigned Date: Eighth Week Due Date: First class of 9th week

P4.



P5.

A) 
$$27_{10} = 1101.1_2 = 1.1011 \times 2^3$$
.

In IEEE 754 single-precision floating-point format:

 $0\ 10000010\ 1011000000000000000000000$ 

Note that in excess-127 representation, the exponent 3 is represented as 10000010. For the mantissa, the leading one is not stored.

P6. For part A it is straight forward where

$$s_{i} = (x_{i} \oplus y_{i})\overline{c_{i}} + \overline{(x_{i} \oplus y_{i})}c_{i} = x_{i} \oplus y_{i} \oplus c_{i}$$
$$c_{i} = x_{i}y_{i} + x_{i}c_{i} + y_{i}c_{i}$$

For part B

$$s_{i} = \overline{x_{i}} y_{i} \overline{c_{i}} + x_{i} \overline{y_{i}} \overline{c_{i}} + \overline{x_{i}} \overline{y_{i}} c_{i} + x_{i} y_{i} c_{i} = (\overline{x_{i}} y_{i} + x_{i} \overline{y_{i}}) \overline{c_{i}} + (\overline{x_{i}} \overline{y_{i}} + x_{i} y_{i}) c_{i}$$

$$= (x_{i} \oplus y_{i}) \overline{c_{i}} + \overline{(x_{i} \oplus y_{i})} c_{i} = x_{i} \oplus y_{i} \oplus c_{i}$$

$$c_{i} = (x_{i} \oplus y_{i}) c_{i} + x_{i} y_{i} = (\overline{x_{i}} y_{i} + x_{i} \overline{y_{i}}) c_{i} + x_{i} y_{i} = y_{i} (x_{i} + c_{i}) + x_{i} (y_{i} + c_{i}) = x_{i} y_{i} + x_{i} c_{i} + y_{i} c_{i}$$

Then A and B expressions are the same. You can also do this exercise with the help of truth tables.

P6.

a. A 2<sup>n</sup>x1 multiplexer would take 2<sup>n</sup>-1 2x1 multiplexers.

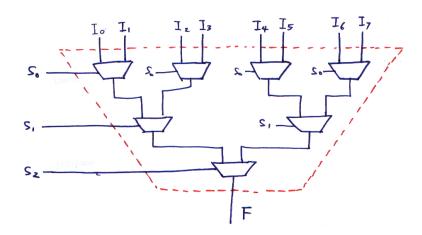
# Cpr E 281 HW07 SOLUTION

ELECTRICAL AND COMPUTER
ENGINEERING
IOWA STATE UNIVERSITY

### Arithmetic Circuits and Combinational-Circuit Building Blocks Assigned Date: Eighth Week

Due Date: First class of 9th week

b.



P8. (a)

				(a) (b)	(c)
A3	A2	<b>A</b> 1	A0	F	
0	0	0	0	1	A0'
0	0	0	1	0	AU
0	0	1	0	0	A0
0	0	1	1	1	AU
0	1	0	0	0	0
0	1	0	1	0	U
0	1	1	0	1	1
0	1	1	1	1	1
1	0	0	0	0	A0
1	0	0	1	1	AU
1	0	1	0	0	0
1	0	1	1	0	U
1	1	0	0	1	A0'
1	1	0	1	0	AU
1	1	1	0	1	1
1	1	1	1	1	1

(B)

### Cpr E 281 HW07 SOLUTION

ELECTRICAL AND COMPUTER
ENGINEERING
IOWA STATE UNIVERSITY

### Arithmetic Circuits and Combinational-Circuit Building Blocks Assigned Date: Eighth Week Due Date: First class of 9th week

