ISU Net ID \_\_\_\_\_@iastate.edu

Lab Section (circle one): A (T 6-9), B (W 3-6), C (R 4-7), D (F 12-3), E (F 3-6), F (T 12-3), G (W 6-9), J (W 12-3), K (T 3-6)

## **CprE 281 Digital Logic**

Examination #1

3/2/2015 9:00-9:50AM

**Directions:** There are 7 questions in this exam. Each question is worth points indicated along with the problem. You should roughly spend 1 minute for every two point. So plan accordingly. If a problem appears to be hard, move on. Please read the questions carefully. Do not write more than what is required.

## Calculator should NOT be used.

Problem 1 2 3 4 5 6 7	Score
Total	(out of 100 points) } JOHN (TOTAL + ENTER INTO Bb)

1. (Total 12 points)

(c) 
$$(1 \text{ points}) 1010101 \text{ (in binary)} = 12.5 \text{ (in octal)}$$

(f) (3 points) 456 (in decimal) 
$$= 111001000$$
 (in binary)

(g) (1 points) 52 (in decimal) 
$$=$$
 \_\_\_\_\_ (in hexadecimal)

2. (Total 12 points) Consider the truth table for function F below.

XYZ	F	Index
0 0 0	0	0
0 0 1	1	1
0 1 0	0	2
0 1 1	1	2 3 4
1 0 0	1	
1 0 1	0	5
1 1 0	1	6
1 1 1	1	7

(a) (3 points) Write the **canonical sum-of-products** expression. Please do **not** use shorthand notation.

$$F = X'Y'Z + X'YZ + XYZ' + XYZ' + XYZ' + XYZ'$$

(b) (3 points) Write the **canonical product-of-sums** expression. Please do **not** use shorthand notation.

$$F = (x + Y + Z) \cdot (X + Y' + Z) \cdot (X' + Y + Z')$$

(c) (3 points) Write the canonical sum-of-products expression in shorthand notation.

$$F(x, 7, z) = \sum m(1, 3, 4, 6, 7)$$

(d) (3 points) Write the canonical product-of-sums expression in shorthand notation.

$$F(x,7,Z) = TTM(0,2,5)$$

- 3. (Total 17 points) Four people participate in a lucky draw. Each one of them draws a ball from a box containing 3 red balls and 3 blue balls. The balls will not be put back to the box once they are drawn. They win as a team if and only if two or more people get a blue ball. Let the color of the four balls drawn be represented by logic variables A, B, C, and D. A variable is set to 1 if the ball is red, and 0 if the ball is blue. Let F be a function describing whether or not they win. F=1 if they win, or 0 if they lose.
  - (a) (8 points) Complete the truth table for F. (Use "d" to indicate "Don't care conditions".)

A	В	С	D		F	
0	0	0	0		d	
0	0	0	1		1	
0	0	1	0		1	
0	0	1	1		1	
0	1	0	0		1	
0	1	0	1		1	
0	1	1	0		1	
0	1	1	1		0	
1	0	0	0		1	
1	0	0	1		1	
1	0	1	0		1	
1	0	1	1		0	
1	1	0	0		1	
1	1	0	1		0	
1	1	1	0		0	
1	1	1	1		d	
				_		

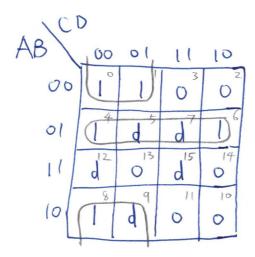
(b) (5 points) Draw a Karnaugh map for F.

AB CI	00	01	11	10
0(	o d	1	1	1
01	1	l	0	
U	1	0	10	0
[0		1	0	1

(c) (4 points) Write down the simplest POS logic expression for F that you can get.

$$F = (A' + B' + C')(A' + B' + D')(A' + C' + D')(B' + C' + D')$$

- 4. (Total 18 points) Use K-map to get the **simplest SOP** expression of function F.
  - (a) (9 points)  $F(A,B,C,D) = \sum m(0,1,4,6,8) + D(5,7,9,12,15)$

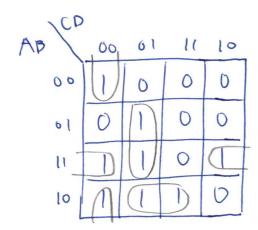


$$F = A'B + B'C'$$

(b) (9 points) 
$$F(A,B,C,D) = B'.C'.D' + A.C' + A'.B.C'.D + A.B'.C.D. + A.B.C.D'$$

$$= A C' \cdot B'D' + A C' \cdot B'D + A C' \cdot BD' + AC' \cdot BD$$

$$= A' \cdot B'C'D' + A \cdot B'C'D'$$



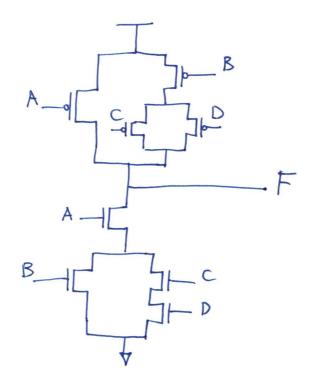
$$F = C'D'B' + D'AB + C'DB + DAB'$$

(a) (2 points) Convert F' into a form that combines A, B, C and D with AND and OR functions only.

$$F' = [A' + B'(C' + D')]'$$

$$= A \cdot (B + C \cdot D) \leftarrow PDN$$

(b) (8 points) Draw a CMOS complex gate to implement the function F using 4 PMOS and 4 NMOS transistors. Please label the signals clearly.



- 6. (Total 12 points) Consider the logic expression F = (A + 0).(A + B).(A' + B').
  - (a) (2 points) Please write the dual expression of F.

$$F^{D} = A \cdot I + A \cdot B + A' \cdot B'$$

(b) (4 points) Complete the truth table for F.

A B	F
0 0	0
0 1	0
1 0	
1 1	0

(c) (6 points) Use Boolean algebra to simplify F as much as you can. Please show all steps and write down the rule number for each step.

1a: 
$$0.0 = 0$$

1b: 
$$1+1=1$$

2b: 
$$0+0=0$$

3a: 
$$0.1 = 1.0 = 0$$

3b: 
$$1+0=0+1=1$$

4a: If 
$$x=0$$
, then  $x' = 1$ 

4b: If 
$$x=1$$
, then  $x'=0$ 

5a: 
$$x.0 = 0$$

5b: 
$$x+1 = 1$$

6a: 
$$x.1 = x$$

6b: 
$$x+0 = x$$

7a: 
$$x.x = x$$

7b: 
$$x+x = x$$

8a: 
$$x.x' = 0$$

8b: 
$$x+x' = 1$$

9: 
$$(x')' = x$$

10a: 
$$x.y = y.x$$

10b: 
$$x+y = y+x$$

11a: 
$$x.(y.z) = (x.y).z$$

11b: 
$$x+(y+z) = (x+y)+z$$

12a: 
$$x.(y+z) = x.y + x.z$$

12b: 
$$x+y.z = (x+y).(x+z)$$

13a: 
$$x+x.y = x$$

13b: 
$$x.(x+y) = x$$

14a: 
$$x.y+x.y' = x$$

14b: 
$$(x+y).(x+y') = x$$

15a: 
$$(x.y)' = x' + y'$$

15b: 
$$(x+y)' = x'.y'$$

16a: 
$$x+x'.y = x+y$$

16b: 
$$x.(x'+y) = x.y$$

$$F = (A+0) \cdot (A+B) \cdot (A'+B')$$
=  $A \cdot (A+B) \cdot (A'+B')$ 
=  $A \cdot (A'+B')$ 
=  $A \cdot (A'+B')$ 
=  $A \cdot B'$ 

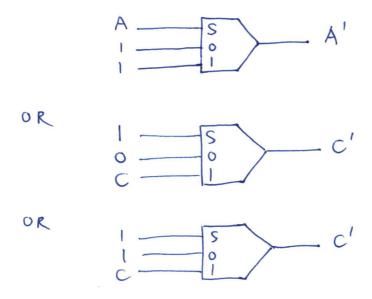
7. (Total 19 points) A company MacroHard introduces a new type of gate called BILL (Brilliant Industrial Low-cost Logic) gate with three inputs A, B and C and one output F. Its functionality is specified by the following truth table. Its circuit symbol is also shown below. Since the inputs of the BILL gate is not symmetric, please label the inputs of the gate by "S", "0" and "1" as in the circuit symbol shown.

A	В	С	F	
0	0	0	0	A — S
0	0	1	0	$B \longrightarrow 0$
0	1	0	1	C 1
0	1	1	1	
1	0	0	1	
1	0	1	0	
1	1	0	1	
1	1	1	0	

The company claims that the BILL gate can be used to replace several other types of gates. We will verify their claim in this question.

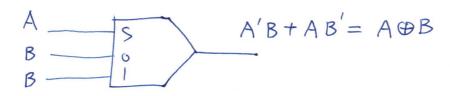
(a) (4 points) Please write a simplified SOP expression for F from the truth table. You do not need to show how to derive the expression.

(b) (4 points) Draw a circuit to show how to implement a NOT gate using one BILL gate.



(Question 7 cont'd)

(c) (5 points) Draw a circuit to show how to implement a two-input XOR gate using one BILL gate.



(d) (6 points) Draw a circuit to show how to implement a two-input NAND gate using one BILL gate.

$$A = S$$

$$C = A' + A \cdot C'$$

$$= A' + C'$$

$$= A' + C'$$

$$= (A \cdot C)'$$

$$= A \cdot C$$