

JASON TANG

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EDUCATION

University of California, Berkeley <i>Bachelor of Science in Electrical Engineering & Computer Sciences</i>	GPA: 3.97 <i>Expected May 2027</i>
Relevant Coursework: Tapeout, Digital Design and Integrated Circuits w/ ASIC Lab, Computer Architecture, Operating Systems, Computer Security, Internet Architecture and Protocols, Data Structures	

EXPERIENCE

UC Berkeley Electrical Engineering & Computer Sciences <i>EECS 16B Lab TA</i>	Aug 2025 – Present <i>Berkeley, CA</i>
<ul style="list-style-type: none">Support 200+ students with circuit analysis, WaveForms, and LTSpice through office hours and an online forumHost weekly lab sections for 40+ students, guiding hands-on circuit construction and use of instrumentation toolsHelp develop weekly prelab and lab assignments, updating and adjusting course content for precision	
UC Berkeley Electrical Engineering & Computer Sciences <i>EECS 151 Tutor</i>	Jan 2026 – Present <i>Berkeley, CA</i>
<ul style="list-style-type: none">Support ASIC labs by debugging RTL designs in Verilog/SystemVerilog and validating design correctnessGuided students in analyzing DVE waveforms to verify correctness and diagnose timing-related issues	
UC Berkeley Operations and Behavioral Analytics Lab <i>Undergraduate Research Assistant</i>	Jan 2025 – May 2025 <i>Berkeley, CA</i>
<ul style="list-style-type: none">Conducted research in human-AI interaction to investigate non-compliance with artificial intelligenceDiscussed findings with students and professor in close discussions, resulting in an exploration of new directions for potential research and existing gaps of knowledge	
UC Berkeley Engineers and Mentors <i>Primary School Mentor</i>	Aug 2024 – Dec 2024 <i>Berkeley, CA</i>
<ul style="list-style-type: none">Taught Title 1 elementary students foundational STEM concepts such as human bone anatomy and physics forces (drag, thrust, lift, gravity) through creative demonstrations and hands-on activitiesDesigned interactive lesson plans and experiments to engage students and spark early interests in science	

PROJECTS

TSMC 16nm Digital and AI SoC Tapeout <i>Chisel, Chipyard, Hammer</i>	Jan 2026 – Present
<ul style="list-style-type: none">Contributed to a TSMC 16nm Compact tapeout w/ Apple mentorship, owning a block and driving through signoffWorked on the digital RTL team developing and refining matrix engine RTL for AI acceleration	
Five Stage Pipelined RISC-V CPU with Caches <i>Verilog</i>	Aug 2025 – Dec 2025
<ul style="list-style-type: none">Awarded 1st Place in the Apple NSI Design Contest for achieving the best performance and area metricsImplemented a 5-stage pipeline with hazard detection and data forwarding to increase frequency and reduce stallsDesigned cache logic with one-cycle read hits, two-cycle write hits, and optimized write-back behavior	
Pintos Operating System <i>C, x86</i>	Aug 2025 – Dec 2025
<ul style="list-style-type: none">Built and extended core components of an OS to support process control, multithreading, and UNIX FFSImplemented multithreading support and synchronization primitives (locks, semaphores, condition variables)Utilized GDB extensively to trace low-level kernel execution, inspect memory, and uncover subtle concurrency and synchronization bugs, demonstrating strong debugging and systems-level problem-solving skills	
Secure File Sharing System <i>Go</i>	Jun 2025 – Aug 2025
<ul style="list-style-type: none">Utilized PBKDFs, symmetric, and public-key cryptography to design a secure file sharing system with user loginAnalyzed RFC security standards to confirm proper protocol usage and compliance with established practicesImplemented with Go and golang/crypto library and wrote 2000+ lines of code to test said implementation for confidentiality, integrity, and authenticity of information, earning top 5 scoring design in a class of 140	

TECHNICAL SKILLS

Languages: Verilog, SystemVerilog, C, x86, RISC-V, Go, Rust, Swift, Java, Python, SQL, JavaScript, HTML/CSS
Developer Tools & Frameworks: DVE, GDB, Docker, Makefile, Valgrind, WaveForms, LTSpice, SwiftUI, UIKit