



Jayvirsinh Chavada

Roll No.: 21BIT077

B.tech

Information & Communication Technology
Pandit Deendayal Energy University

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EDUCATION

- Pandit Deendayal Energy University, Gujarat** 2025
Bachelor of Technology in Information and Communication Technology CGPA: 9.08*
- Tejas Vidyalyaya, Vadodara, Gujarat** 2021
Gujarat Secondary and Higher Secondary Education Board (GSEB) Percentage(Class XII) : 81.38%
Percentage(Class X) : 81.6%

PERSONAL PROJECTS

- Basic 16-bit CPU Design using Logisim**
 - Tools & technologies used: Logisim Software
 - This project provides the fundamental knowledge required to decipher the hardware digital processing unit. It explains how a computer's register transfer micro-operation works. It also describes the operation of the ALU, register, and multiplexer.
- Replicating IEEE paper on printed Quasi-Yagi antenna (PQYA)**
to observe the improvements in the bandwidth (BW) by making needful modifications to a PQYA.
 - Tools & technologies used: Ansys HFSS Software
 - output : To replicate an already researched IEEE paper on PQYA, and observe the improvement in bandwidth of the antenna using Ansys HFSS software, and try to obtain the same output as the research paper.

TECHNICAL SKILLS AND INTERESTS

Programming Languages: C, C++, Embedded C, python , Web development(HTML, CSS, javascript)
, SQL

Developer Tools: Ansys HFSS(Radio Frequency Engineering) , Logisim(Computer organisation),
TINA-TI, MATLAB, React.js, Proteus Design Suite, Git, Github, VS code

Soft Skills: Problem Solving, Time Management, Teamwork, Accountability, Teaching,
Communication

Areas of Interest: Embedded Systems, Web Development, Radio Frequency engineering,
Cloud Computing

POSITIONS OF RESPONSIBILITY

- CORE committee** , sports club PDEU 2024
- School House Captain**, Tejas Vidyalyaya 2021