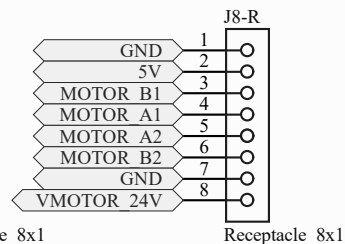
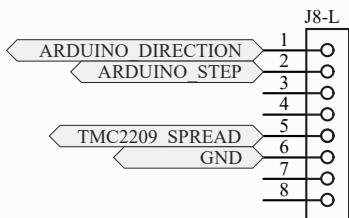
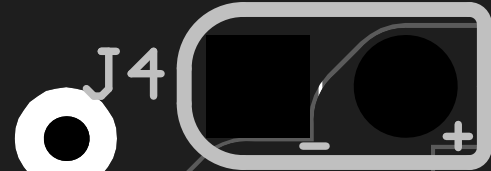


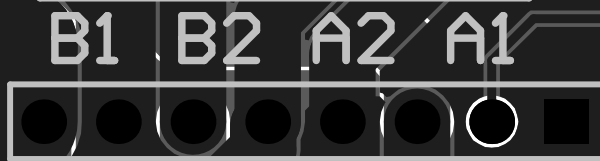
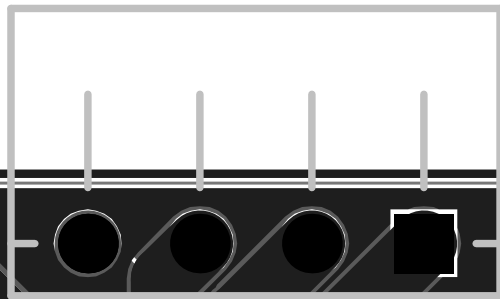
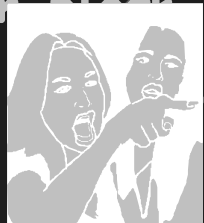
EB+/- are intentionally omitted



| Title | | |
|-------|-------------------|-----------|
| Size | Number | Revision |
| A | | |
| Date: | 1/25/2020 | Sheet of |
| File: | Connectors.SchDoc | Drawn By: |



3 interfaces
per block



J8-R

J8-L



J1



SPREAD 5V



J9

J7: A1 | 5V
J6: A0 | 5V
J5: RX | TX

U2



J7



J6



J5



D1 D2

TX RX

C10

X1

C11

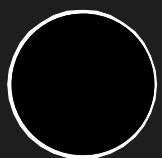
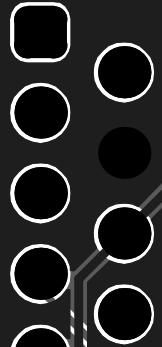
C12



D4



R8



J3



My
group