Universidad de Costa Rica

Facultad de Ingeniería

Escuela de Ingenierá Eléctrica

Laboratorio de Circuitos Digitales

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Fast Fourier Transform Calculator

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1. Objectives

1.1. General Objective

Implementation of Fast Fourier Transform Calculator in an FPGA.

1.2. Specific Objectives

- Research about methods to calculate the fft by hardware.

- Improve the hands on skills by computing several logic calculations at bit level.

2. Introduction

3.   Body of Document