

Mechatronics for Rehabilitation Engineering: Lab Notebook

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Update on July 16, 2025

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Experiment 1

Orientation

This is an introductory lab to get you oriented to the course's lab component, the equipment in the instrumentation lab, and learning to prototype simple electrical circuits with passive components in a breadboard.

The logistics for the lab component of the course will be provided by the instructor at the start of the session. This will be followed by the course TAs and the instructor demonstrating the use of the equipment in the lab.

You will be required to build the following circuits and answer the questions associated with each circuit. You will be required to prepare a report on the circuits and the answers to the questions within a week of the lab session.

1.1 Circuit 01: Series RC

Build the following series RC circuit (Figure 1.1) on a breadboard and answer the following question:

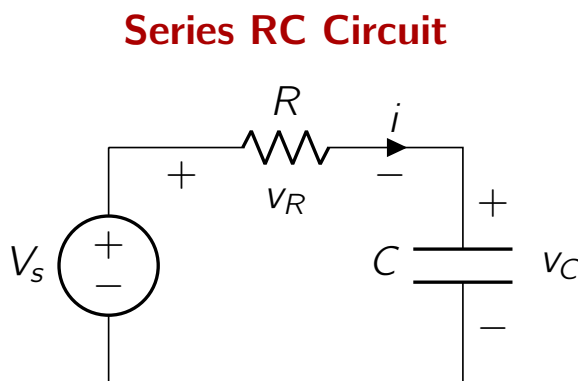


Fig. 1.1: A series RC circuit.

1. Propose a procedure for estimating the time constant for this circuit. You are free to choose any input signal V_s , but you must justify your choice. Based on

this procedure, make your measurement, tabulate them, and estimate the time constant τ of the circuit. How does this value compare to the theoretical value of $\tau = RC$?

2. Can you use this circuit or one with the appropriate modification to measure the input resistance of the oscilloscope? If so, how would you do it? Explain your procedure and estimate the input resistance of the oscilloscope.

1.2 Circuit 02: Parallel RC

Build the following parallel RC circuit (Figure 1.2) on a breadboard and answer the following question:

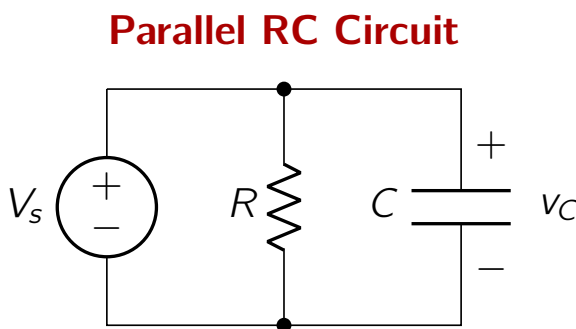


Fig. 1.2: A parallel RC circuit.

1. Derive the expression for the time constant τ for this circuit. You are free to choose any input signal V_s , but you must justify your choice. Based on this procedure, make your measurement, tabulate them, and estimate the time constant τ of the circuit. How does this value compare to the theoretical value of $\tau = RC$?

1.3 Circuit 03: RLC

Let's now look at a more complex circuit, the parallel RLC circuit shown in Figure 1.3. Build this circuit on a breadboard and answer the following question, for $V_s = 5V$ (DC). Choose $R_s = 100\Omega$, which is used to limit the current drawn V_s .

1. First, choose the value of the resistor to be $R = 1M\Omega$. Close the switch for some time, while measuring the voltage across R . What is the voltage across R when the switch is closed for a long time?
2. What happens when the switch is opened? Can you explain this behavior?

3. Repeat the same experiment with $R = 1k\Omega$. Is there any difference in the behavior of the circuit? If so, explain why.
4. What is the frequency of oscillation of the circuit? How does it compare to the theoretical value of $f = \frac{1}{2\pi\sqrt{LC}}$?

Parallel RLC Circuit

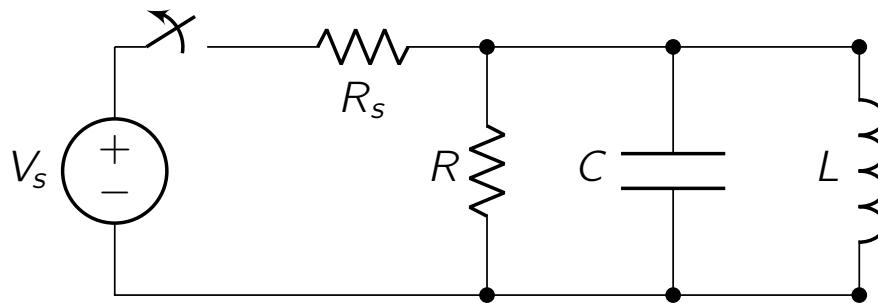


Fig. 1.3: A parallel RLC circuit.

1.4 Circuit 04: RC Circuit Sinusoidal Response

In Figure 1.1, use a sinusoidal input for V_s with amplitude 5V.

- When you apply an input signal V_s of any frequency, what is the frequency of the voltage across the capacitor? Is it the same or different?
- For an input of fixed amplitude, what happens to the amplitude of the voltage across the capacitor as the frequency of the input signal is varied? Tabulate your measurements of the output voltage amplitude across the capacitor for different input frequencies (0Hz to 1MHz). Plot the amplitude versus frequency.
- What is the phase difference between the input voltage and the voltage across the capacitor? How does this change with frequency?

Compare the results with the theoretical predictions for the circuit by doing the steady state sinusoidal analysis by using the impedance of the resistor and the capacitor.

1.5 Circuit 05: RL Circuit Sinusoidal Response

In Figure 1.2, use a sinusoidal input for V_s with amplitude 5V.

- When you apply an input signal V_s of any frequency, what is the frequency of the voltage across the inductor? Is it the same or different?

- For an input of fixed amplitude, what happens to the amplitude of the voltage across the inductor as the frequency of the input signal is varied? Tabulate your measurements of the output voltage amplitude across the inductor for different input frequencies (0Hz to 1MHz). Plot the amplitude versus frequency.
- What is the phase difference between the input voltage and the voltage across the inductor? How does this change with frequency?

Compare the results with the theoretical predictions for the circuit by doing the steady state sinusoidal analysis by using the impedance of the resistor and the inductor.

1.6 Circuit 06: Series RLC Circuit Sinusoidal Response

In Figure 1.4, use a sinusoidal input for V_s with amplitude 5V.

- For an input of fixed amplitude, what happens to the amplitude of the voltage across the inductor as the frequency of the input signal is varied? Tabulate your measurements of the output voltage amplitude across the inductor for different input frequencies (0Hz to 1MHz). Plot the amplitude versus frequency.
- What is the phase difference between the input voltage and the voltage across the inductor? How does this change with frequency?

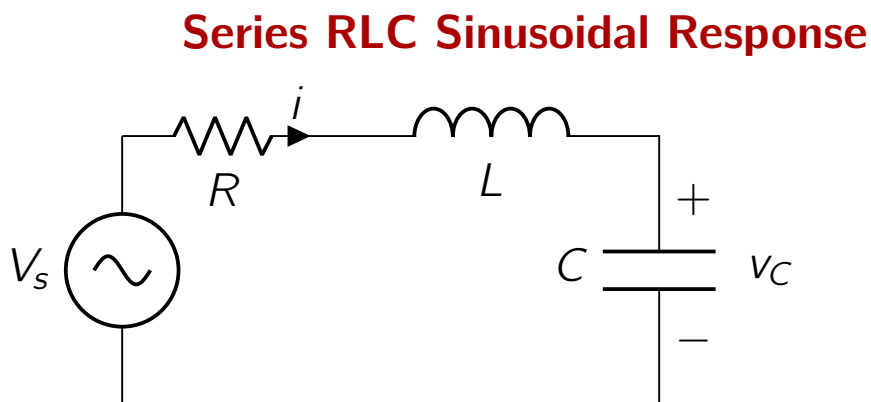


Fig. 1.4: A series RLC circuit.

Compare the results with the theoretical predictions for the circuit by doing the steady state sinusoidal analysis by using the impedance of the resistor and the inductor.

Experiment 2

Characterizing Electronic Components