

SIMPLIFICATION OF BOOLEAN EXPRESSIONS

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Basic Rules for Boolean Algebra

Basic Rules of Boolean Algebra

1.	$\mathbf{A} + 0 = \mathbf{A}$	7. $\mathbf{A} \cdot \mathbf{A} = \mathbf{A}$
2.	$\mathbf{A} + 1 = 1$	$8. \mathbf{A} \cdot \overline{\mathbf{A}} = 0$
3.	$\mathbf{A} \cdot 0 = 0$	9.
4.	$A \cdot 1 = A$	10. A + AB = A
5.	A + A = A	11. $A + \overline{A}B = A + B$
6.	$A + \overline{A} = 1$	12. $(A + B)(A + C) = A + BC$

DeMorgan's Theorem

$$\overline{(AB)} = (\overline{A} + \overline{B}) \qquad \overline{(A + B)} = (\overline{A} \overline{B})$$

Explained: in Detail

Boolean Expression	Description	Equivalent Switching Circuit	Boolean Algebra Law or Rule
A + 1 = 1	A in parallel with closed = "CLOSED"	A	Annulment
A + 0 = A	A in parallel with open = "A"	A	Identity
A . 1 = A	A in series with closed = "A"	_A	Identity
A . 0 = 0	A in series with open = "OPEN"		Annulment
A + A = A	A in parallel with A = "A"	A	Idempotent
A . A = A	A in series with A = "A"	A A	Idempotent

Explained: in Detail

$NOT\overline{A} = A$	NOT NOT A (double negative) = "A"		Double Negation
A + A = 1	A in parallel with NOT A = "CLOSED"	A	Complement
A . A = 0	A in series with NOT A = "OPEN"	A Ā	Complement
A+B = B+A	A in parallel with B = B in parallel with A	A	Commutative
A.B = B.A	A in series with B = B in series with A	A B	Commutative
$\overline{A+B} = \overline{A}.\overline{B}$	invert and replace OR with AND		de Morgan's Theorem
$\overline{A.B} = \overline{A} + \overline{B}$	invert and replace AND with OR		de Morgan's Theorem

Comprehensive Version Ahead!

i.	Law of Identity	$\frac{A}{A} = \frac{A}{A}$
2.	Commutative Law	$A \cdot B = B \cdot A$ A + B = B + A
3.	Associative Law	$A \cdot (B \cdot C) = A \cdot B \cdot C$ A + (B + C) = A + B + C
4.	Idempotent Law	$A \cdot A = A$ $A + A = A$
5.	Double Negative Law	= A
6.	Complementary Law	$A \cdot \overline{A} = 0$ $A + \overline{A} = 1$
7.	Law of Intersection	$A \cdot 1 = A$ $A \cdot 0 = 0$
8.	Law of Union	A+1 = 1 $A+0 = A$
9,	DeMorgan's Theorem	$\frac{\overline{AB} = \overline{A} + \overline{B}}{A + B} = \overline{A} \overline{B}$
10.	Distributive Law	$A \cdot (B + C) = (A \cdot B) + (A \cdot C)$ $A + (BC) = (A + B) \cdot (A + C)$
11.	Law of Absorption	$A \cdot (A + B) = A$ $A + (AB) = A$
12.	Law of Common Identities	$A \cdot (\overline{A} + B) = AB$ $A + (\overline{A}B) = A + B$

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Simplifications

- Using the above laws, simplify the following expression:

Simplifications

$$Q = (A + B).(A + C)$$

$$A.A + A.C + A.B + B.C$$

Distributive law

$$A + A.C + A.B + B.C$$

- Idempotent AND law (A.A = A)

$$A(1 + C) + A.B + B.C$$

Distributive law

$$A.1 + A.B + B.C$$

- Identity OR law (1 + C = 1)

$$A(1 + B) + B.C$$

Distributive law

$$A.1 + B.C$$

- Identity OR law (1 + B = 1)

$$Q = A + (B.C)$$

- Identity AND law (A.1 = A)

Simplifications

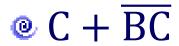
Simplify the following:

$$\Phi C + \overline{BC}$$

$$\Phi \overline{AB}(\overline{A} + B)(\overline{B} + B)$$

$$\Phi (A + C)(AD + A\overline{D}) + AC + C$$

$$\Phi \overline{A}(A + B) + (B + AA)(A + \overline{B})$$



$$\Phi \overline{AB}(\overline{A} + B)(\overline{B} + B)$$

$$\Phi(A + C)(AD + A\overline{D}) + AC + C$$

$$\Phi \overline{A}(A + B) + (B + AA)(A + \overline{B})$$

Simplify: $C + \overline{BC}$:

Expression	Rule(s) Used
$C + \overline{BC}$	Original Expression
$C + (\overline{B} + \overline{C})$	DeMorgan's Law.
$(C + \overline{C}) + \overline{B}$	Commutative, Associative Laws.
$T + \overline{\mathbf{B}}$	Complement Law.
T	Identity Law.

Simplify:
$$\overline{AB}(\overline{A} + B)(\overline{B} + B)$$
:

Expression	Rule(s) Used
$\overline{AB}(\overline{A} + B)(\overline{B} + B)$	Original Expression
$\overline{AB}(\overline{A} + B)$	Complement law, Identity law.
$(\overline{A} + \overline{B})(\overline{A} + B)$	DeMorgan's Law
$\overline{A} + \overline{B}B$	Distributive law. This step uses
	the fact that or distributes over
	and. It can look a bit strange
	since addition does not distribute
	over multiplication.
$\overline{\mathbf{A}}$	Complement, Identity.

Simplify: $(A + C)(AD + A\overline{D}) + AC + C$:

Expression

$$(A + C)(AD + A\overline{D}) + AC + C$$

$$(A + C)A(D + \overline{D}) + AC + C$$

$$(A + C)A + AC + C$$

$$A((A+C)+C)+C$$

$$A(A + C) + C$$

$$AA + AC + C$$

$$A + (A + T)C$$

$$A + C$$

Rule(s) Used

Original Expression

Distributive.

Complement, Identity.

Commutative,

Distributive.

Associative,

Idempotent.

Distributive.

Idempotent, Identity,

Distributive.

Identity, twice.

Simplify: $\overline{A}(A + B) + (B + AA)(A + \overline{B})$:

<u>Expression</u>

$$\overline{A}(A+B) + (B+AA)(A+\overline{B})$$

$$\overline{A}A + \overline{A}B + (B + A)A + (B + A)\overline{B}$$

$$\overline{A}B + (B + A)A + (B + A)\overline{B}$$

$$\overline{A}B + BA + AA + B\overline{B} + A\overline{B}$$

$$\overline{A}B + BA + A + A\overline{B}$$

$$\overline{A}B + AB + AT + A\overline{B}$$

$$\overline{A}B + A(B + T + \overline{B})$$

$$\overline{A}B + A$$

$$A + \overline{A}B$$

$$(A + \overline{A})(A + B)$$

$$A + B$$

Rule(s) Used

Original Expression

Idempotent (AA to A), then Distributive, used twice.

Complement, then Identity. (Strictly speaking, we also used the Commutative Law for each of these applications.)

Distributive, two places.

Idempotent (for the A's), then Complement and Identity to remove $B\overline{B}$.

Commutative, Identity; setting up for the next step.

Distributive.

Identity, twice (depending how you count it).

Commutative.

Distributive.

Complement, Identity.

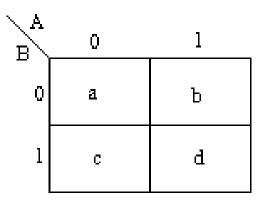
- The Karnaugh map provides a simple and straight-forward method of minimising boolean expressions
- With the Karnaugh map Boolean expressions having up to four and even six variables can be simplified

- ② A Karnaugh map provides a pictorial method of grouping together expressions with common factors and therefore eliminating unwanted variables
- The Karnaugh map can also be described as a special arrangement of a truth table

The correspondence between the Karnaugh map and the truth table for the general case of a two variable problem

A	В	F
0	0	а
0	1	Ъ
1	0	С
1	1	d

Touth Table

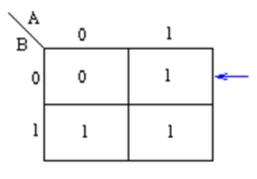


F.

© Example

A	В	F
0	0	0
0	1	1
1	0	1 ≪
1	1	1

Truth Table.



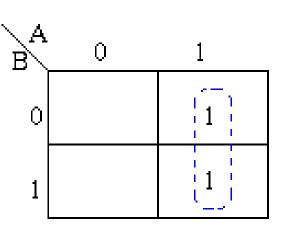
F.

Using Karnaugh maps for simplifications

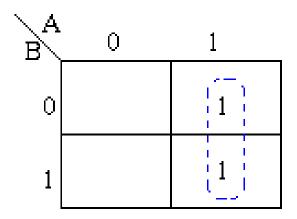
© Example:

$$>$$
 Z = A \overline{B} + AB

- Using algebraic simplification,
 - > Z = A(\overline{B} + B)
 - > Z = A
- © Consider the following map:
- Referring to the map above, the two adjacent 1's are grouped together



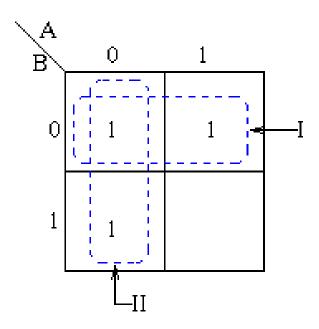
Using Karnaugh maps for simplifications



- Through inspection it can be seen that variable B has its true and false form within the group
- This eliminates variable B leaving only variable A which only has its true form
- The minimised answer therefore is Z = A

Example 2

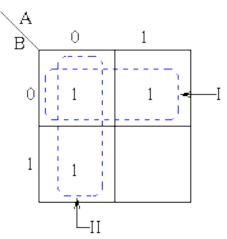
© Consider the expression $Z = \overline{A} \cdot \overline{B} + A\overline{B} + \overline{A}B$



- Pairs of 1's are grouped as shown above, and the simplified answer is obtained by using the following steps:
- Notice that a 1 can belong to more than one group

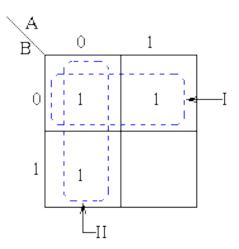
Example 2

$$Z = \overline{A}.\overline{B} + A\overline{B} + \overline{A}B$$



- The first group labelled I, consists of two 1s which correspond to A = 0, B = 0 and A = 1, B = 0
- Put in another way, all squares that correspond to the area of the map where B = 0 contains 1s, independent of the value of A
- So when B = 0 the output is 1
- @ The expression of the **output will contain** the term $\overline{\mathbf{B}}$

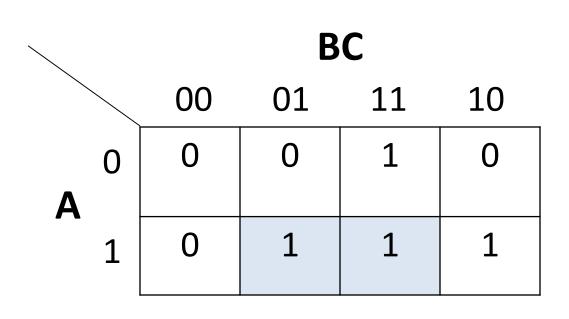
Example 2



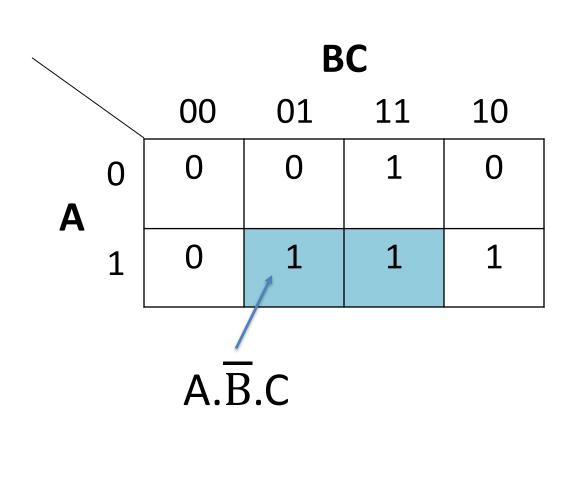
- For group labelled II corresponds to the area of the map where A = 0
- @ The group can therefore be defined as \overline{A}
 - > This implies that when A = 0 the output is 1
- The output is therefore 1 whenever B = 0 and A = 0
- Hence the simplified answer is $Z = \overline{A} + \overline{B}$

Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

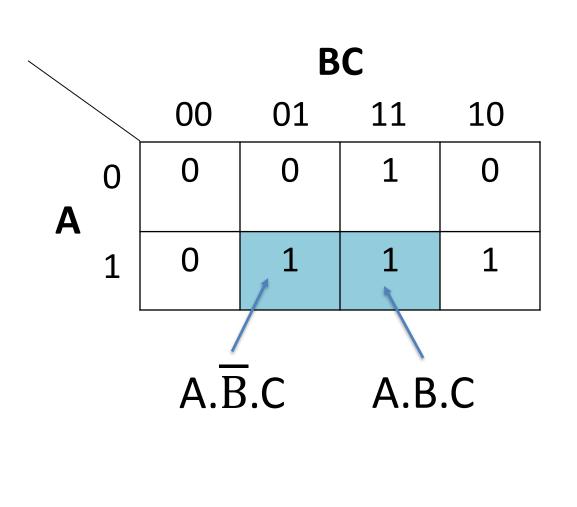
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



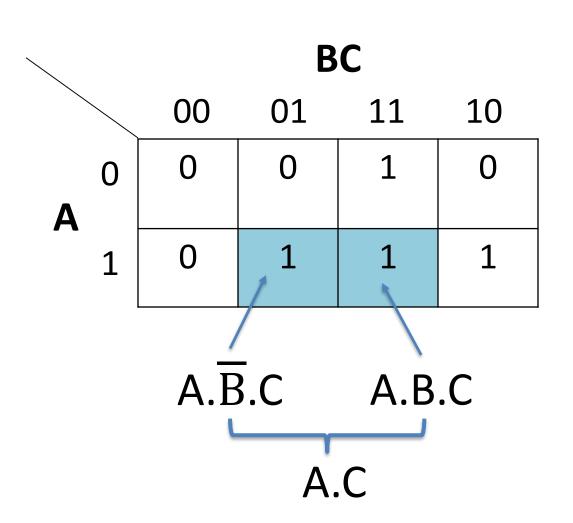
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



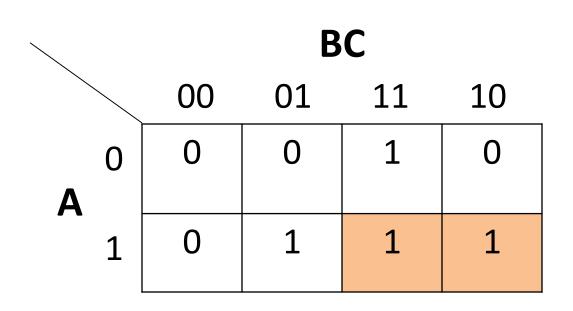
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



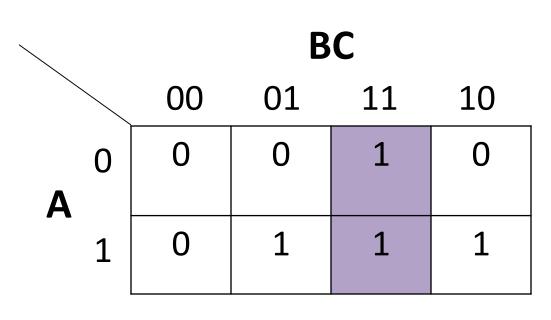
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



Accordingly....

A.B

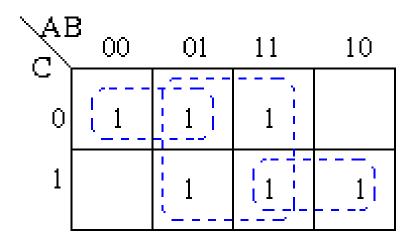
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1



B.C

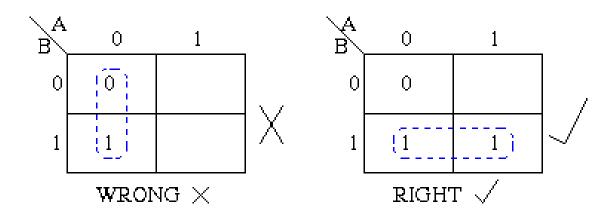
Examples with more than 2 variables

$$\bullet$$
 Z = \overline{A} . \overline{B} . \overline{C} + \overline{A} .B + \overline{A} B + \overline{A} C

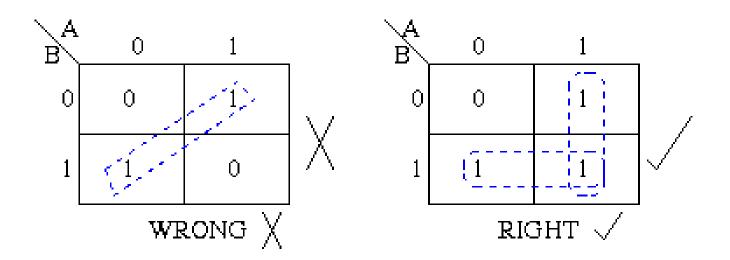


Karnaugh Maps - Rules of Simplification

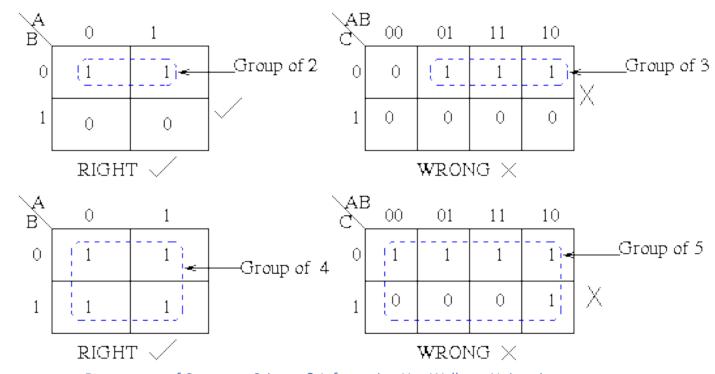
- The Karnaugh map uses the following rules for the simplification of expressions by grouping together adjacent cells containing ones
 - > Groups may not include any cell containing a zero



@ Groups may be horizontal or vertical, but not diagonal

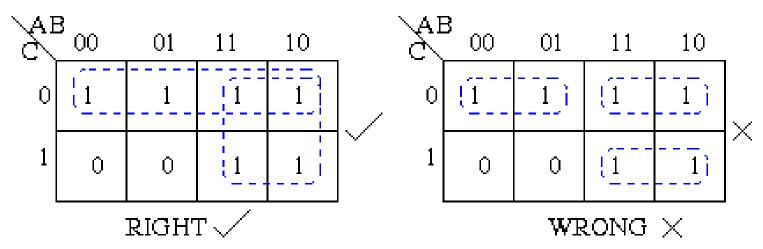


- @ Groups must contain 1, 2, 4, 8, or in general 2ⁿ cells
 - \rightarrow That is if n = 1, a group will contain two 1's since $2^1 = 2$
 - \rightarrow If n = 2, a group will contain four 1's since $2^2 = 4$



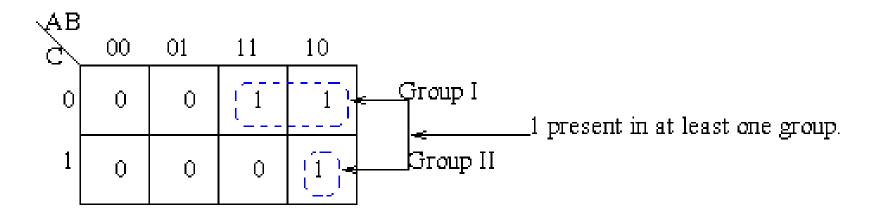
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© Each group should be as large as possible

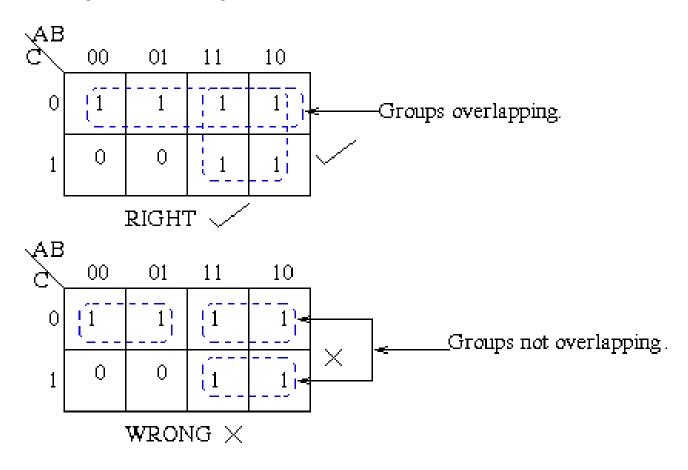


(Note that no Boolean laws broken, but not sufficiently minimal)

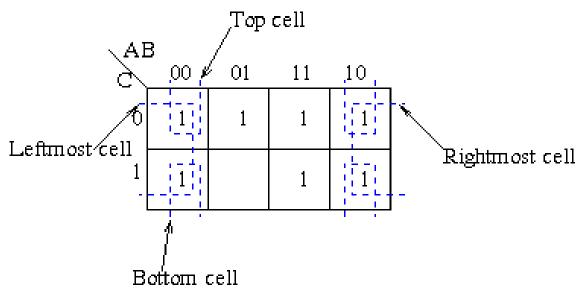
© Each cell containing a one must be in at least one group



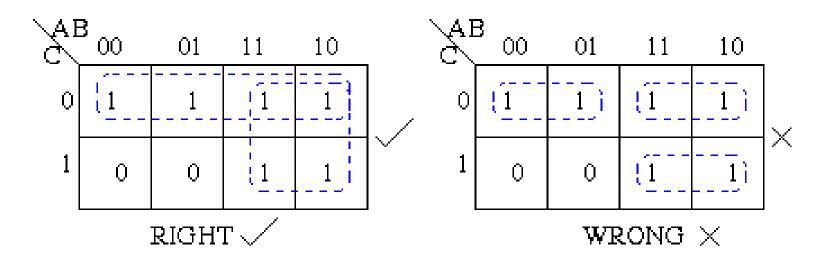
@ Groups may overlap



- @ Groups may wrap around the table
- The leftmost cell in a row may be grouped with the rightmost cell and the top cell in a column may be grouped with the bottom cell



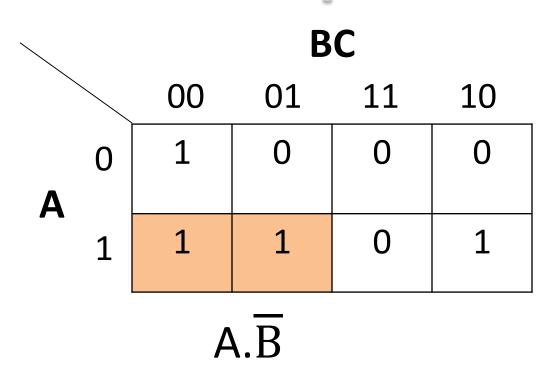
There should be as few groups as possible, as long as this does not contradict any of the previous rules



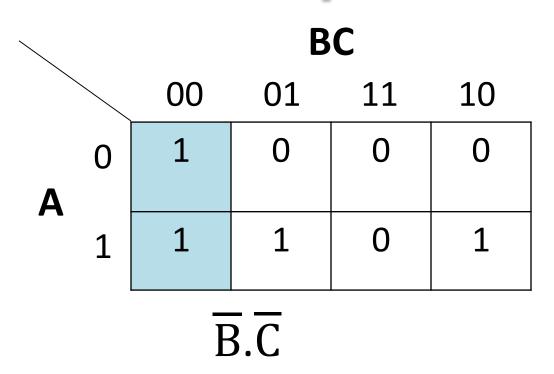
Summary

- No zeros allowed
- No diagonals
- Only power of 2 number of cells in each group
- @ Groups should be as large as possible
- Every one (1) must be in at least one group
- Overlapping allowed
- Wrap around allowed
- © Fewest number of groups possible

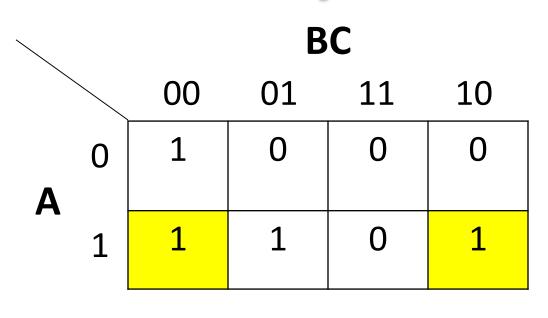
Examples



Examples



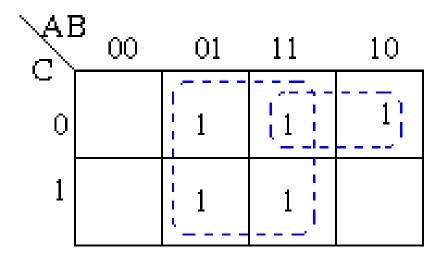
Examples



$$\bullet$$
 Z = \overline{A} . B + B. \overline{C} + B. C + A. \overline{B} . \overline{C}

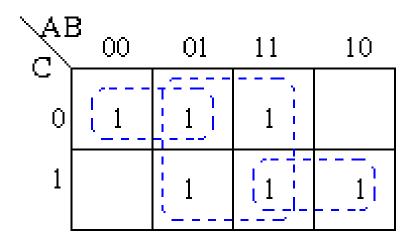
AI C	3 00	01	11	10
0		1		1
1		1	1	

$$\bullet$$
 Z = \overline{A} . B + B. \overline{C} + B. C + A. \overline{B} . \overline{C}

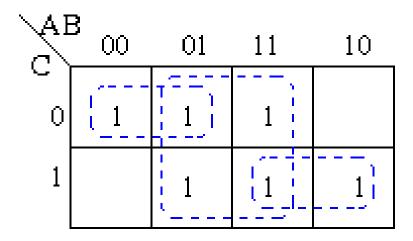


© The minimized result obtained is B + \overline{AC}

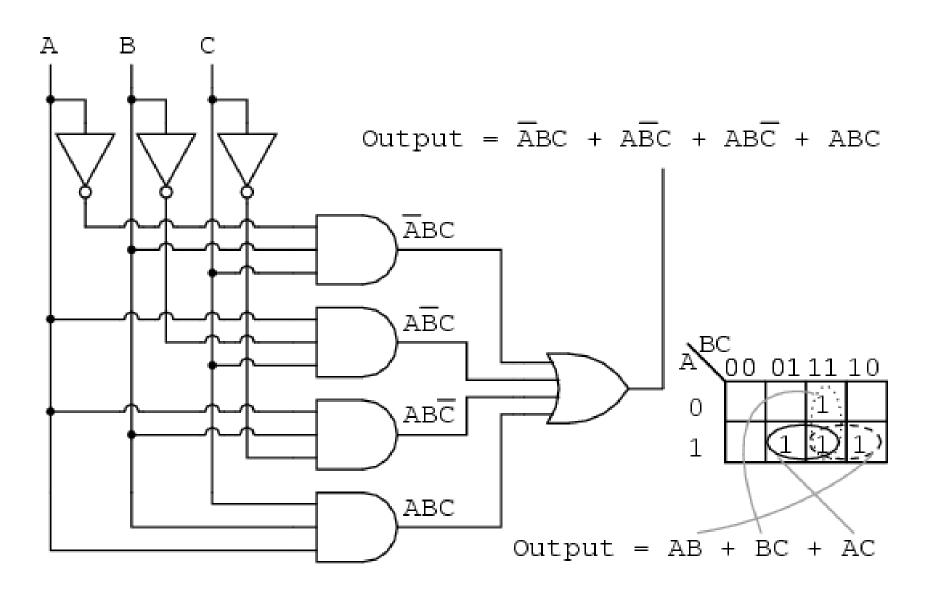
$$\bullet$$
 Z = \overline{A} . \overline{B} . \overline{C} + \overline{A} .B + \overline{A} B + \overline{A} C



$$\bullet$$
 Z = \overline{A} . \overline{B} . \overline{C} + \overline{A} .B + \overline{A} B + \overline{A} C



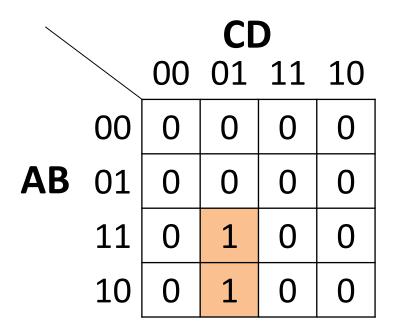
© The minimized result obtained is B + AC + \overline{A} . \overline{C}



CD AB	00	01	11	10
00	1	0	0	1
01	1	1	0	0
11	1	0	0	0
10	1	0	0	1

		CD			
		00	01	11	10
	00	0	0	0	0
AB	01	1	1	0	0
	11	0	0	0	0
	10	0	0	0	0

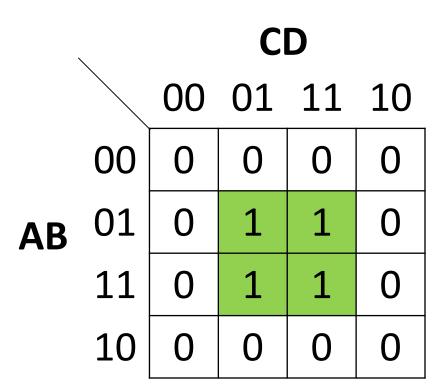
- The variable D changes from a 0 to a 1 as you move from the left cell to the right cell
- © Consequently, these two 1s are not dependent upon the value of D, and therefore:
- D will not appear in the product term that results
- The Result is $\mathbf{Z} = \overline{\mathbf{A}}$. \mathbf{B} . $\overline{\mathbf{C}}$



- @ Accordingly:
- The Result is $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{C}} \cdot \mathbf{D}$

		CD				
		00	01	11	10	
	00	0	0	0	0	
AB	01	0	0	0	0	
	11	0	1	1	0	
	10	0	1	1	0	

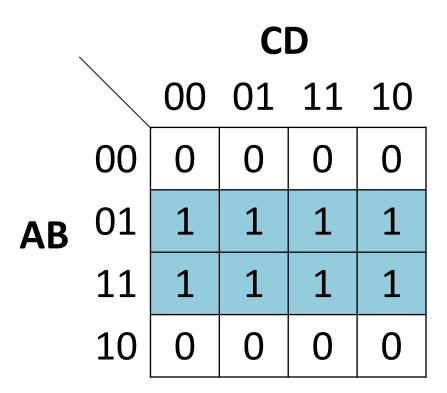
@ Answer: Z = A.D



@ Answer: Z = B.D

		CD				
		00	01	11	10	
	00	0	0	0	0	
AB	01	1	1	1	1	
	11	0	0	0	0	
	10	0	0	0	0	

@ Answer: $Z = \overline{A}.B$



@ Answer: Z = B

		CD			
		00	01	11	10
	00	1	0	0	1
AB	01	0	0	0	0
	11	0	0	0	0
	10	1	0	0	1

- @ In the upper left: $\mathbf{Z} = \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D}$.
- @ In the upper right: $\mathbf{Z} = \overline{A} \cdot \overline{B} \cdot \mathbf{C} \cdot \overline{D}$.
- @ In the lower left: $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{B}} \cdot \overline{\mathbf{C}} \cdot \overline{\mathbf{D}}$.
- @ In the lower right: $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{B}} \cdot \mathbf{C} \cdot \overline{\mathbf{D}}$.

		CD			
		00	01	11	10
	00	1	0	0	1
AB	01	0	0	0	0
	11	0	0	0	0
	10	1	0	0	1

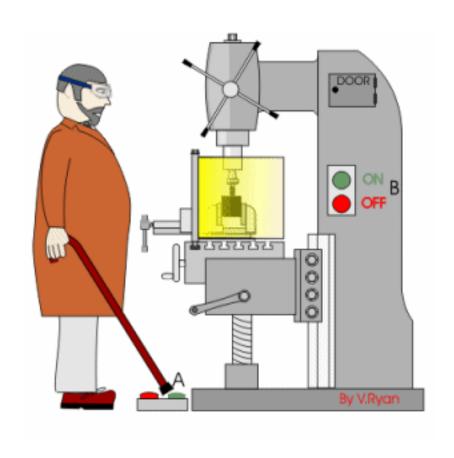
- In the upper left: $\mathbf{Z} = \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D}$.
- ② In the upper right: $\mathbf{Z} = \overline{\mathbf{A}} \cdot \overline{\mathbf{B}} \cdot \mathbf{C} \cdot \overline{\mathbf{D}}$.
- In the lower left: $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{B}} \cdot \overline{\mathbf{C}} \cdot \overline{\mathbf{D}}$.
- In the lower right: $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{B}} \cdot \mathbf{C} \cdot \overline{\mathbf{D}}$.
- @ By combining the first two terms above: $\mathbf{Z} = \mathbf{A} \cdot \mathbf{B} \cdot \mathbf{D}$.
- @ By combining the last two terms above: $\mathbf{Z} = \mathbf{A} \cdot \overline{\mathbf{B}} \cdot \overline{\mathbf{D}}$.
- Then, these two germs can be combined to give: Z = B. D.

Further Observations

- There may well be more than one solution of equal complexity
- \bullet A. \overline{C} . D. + \overline{A} . B. C. ?

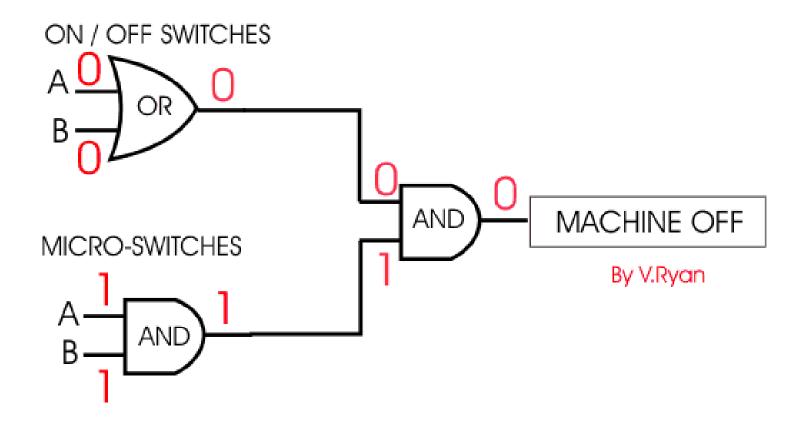
Further Observations

- There may well be more than one solution of equal complexity
- \bullet A. \overline{C} . D. + \overline{A} . B. C. ?
- And what about the 1 alone?

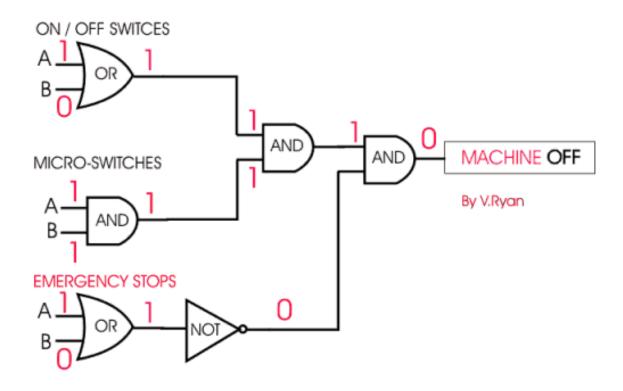


- A metal cutting milling machine has two switches, any one will allow the cutter to run.
- The first switch is on the side of the machine and the second is a foot operated switch.
- We have the machine has two micro-switches (used as safety devices) if any of these is released the cutter will stop.
- The first micro-switch is on a guard, if this is opened the machine will stop.
- The second micro-switch is on a door which allows access to the moving mechanism of the milling machine. If this is opened the machine will stop. The micro-switches are normally logic '1' (true, high, on) when pressed.
- Oraw the logic diagram for this machine

- A metal cutting milling machine has two switches, any one will allow the cutter to run.
- The first switch is on the side of the machine and the second is a foot operated switch.
- However, the machine has two microswitches (used as safety devices) if any of these is released the cutter will stop.
- The first micro-switch is on a guard, if this is opened the machine will stop.
- The second micro-switch is on a door which allows access to the moving mechanism of the milling machine. If this is opened the machine will stop. The micro-switches are normally logic '1' (true, high, on) when pressed.
- Draw the logic diagram for this machine



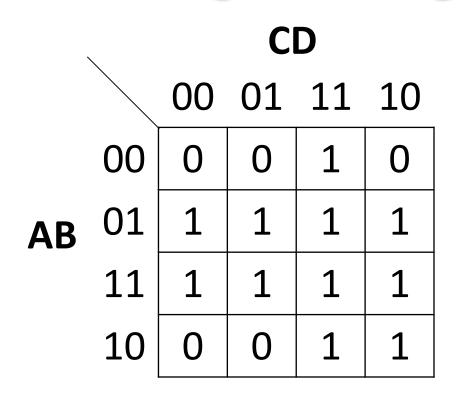
- In addition to the above (previous) arrangement,
 - > The room has two emergency stop buttons at either end of the workshop
 - If either of these are pressed all machinery in the room will stop
- © Draw the new logic circuit for this arrangement of buttons and switches

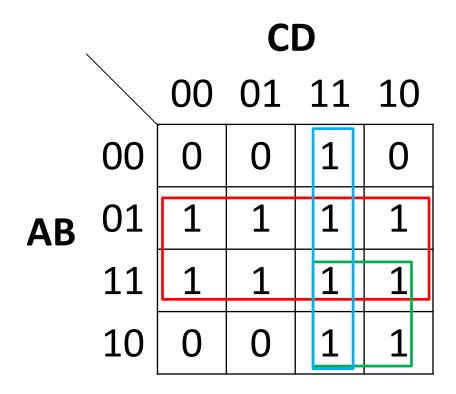


- An engine has 4 fail-safe sensors. The engine should keep running unless any of the following condition arise:
 - > If sensor 2 is activated
 - ➤ If sensor 1 and sensor 3 are activated together (meanwhile sensors 2 and 4 are inactive)
 - > If sensor 3 and sensor 4 are activated together
- Operive the truth table for this scenario
- © Construct a logic gate network to satisfy requirements with basic logic gates
- Re-design the developed logic gate network with NAND and NOR gates only

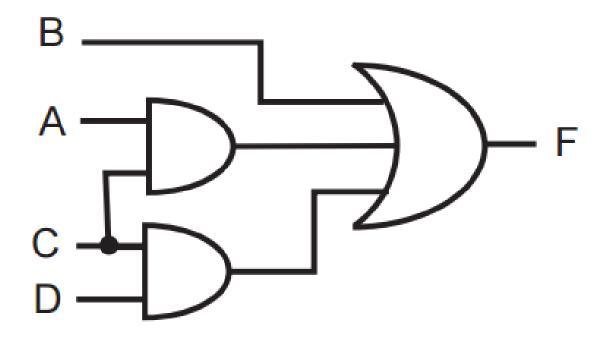
- © Conventions:
- A = Sensor1,B=Sensor2,C=Sensor3,D=Sensor4
- © Sensor activated = 1,
 F = Shutdown=1

A	В	C	D	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0 0 0	1	0	0	1 1 1 1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	
1	0	1	0	0 1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1





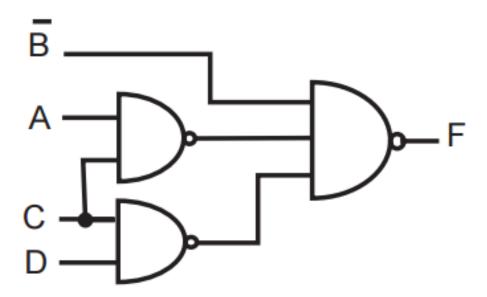
$$@$$
 F = B + CD + AC



using NAND gates only

$$F = B + CD + AC$$

$$= \overline{A + CD + AC} = \overline{B.CD.AC}$$



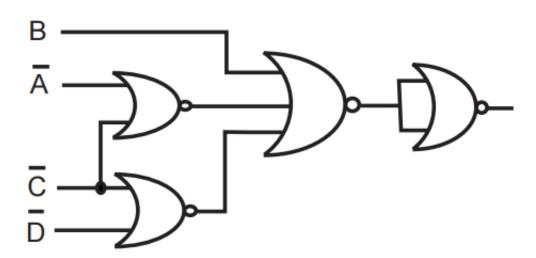
using NOR gates only

$$F = B + CD + AC$$

$$F = \overline{B} + \overline{CD} + \overline{AC}$$

$$F = \overline{B} + (\overline{C} + \overline{D}) + (\overline{A} + \overline{C})$$

$$F = \overline{F} = \overline{B} + (\overline{C} + \overline{D}) + (\overline{A} + \overline{C})$$



- An airbag of a vehicle is activated once the following conditions meets:
 - Engine must be running (Logic Level of sensor 'E' is High)
 - > The driver / passenger has buckled-up (fasten) the seat belt (Logic level of sensor 'B' is Low)
 - Speed sensor 'S' has exceeded the threshold value. (Logic level High)
 - > Vibration sensors both 'V1' and 'V2' together or 'V3' alone is triggered. (Logic level High)

- © Construct the Karnaugh Map for the scenario
- © Construct a logic gate network to satisfy requirements with basic logic gates
- Re-design the developed logic gate network with NAND and NOR gates only