L2 trigger

readout

from PMT

pipeline

PROCESSING UNIT

INPUTS: every 4 ns

OUTPUTS: 32 bytes (16 bits per channel) every 32 ns
OR
pack and send non−zero data only

ch00
ch01
ch02
ch03
ch04
ch05
ch06
ch07
ch08
ch09
ch10
ch11
ch12
ch13
ch14
ch15

to CTP board

8GBit backplane bus

NOTES:
– only channel 00 is shown in details, all other channels have the same structure

Flash ADC board data processing logic