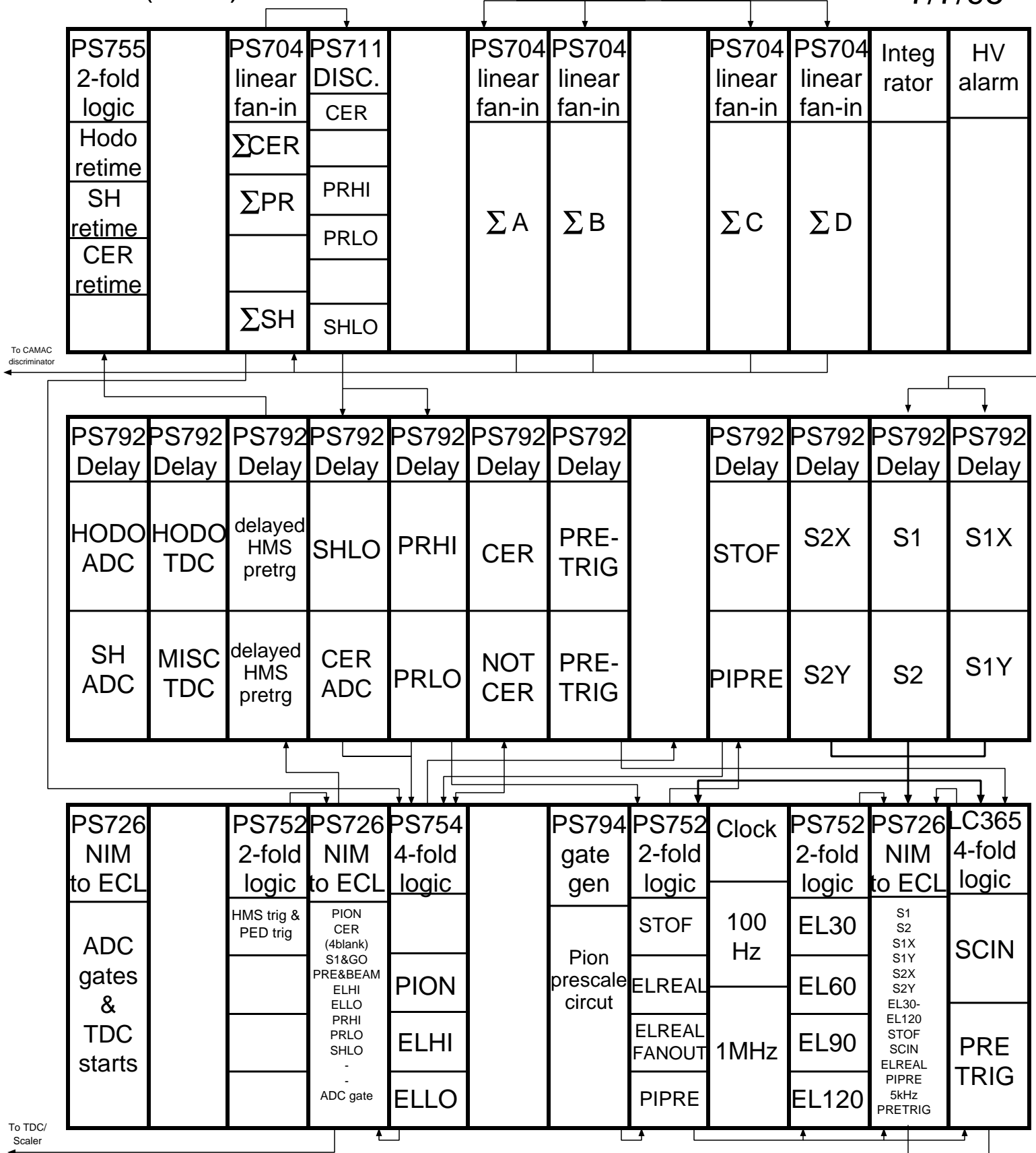


RACK 7 (HMS)

Shower counter signals.

7/7/95



$CER = C1 + C2 + C3 + C4$
 $STOF = S1 * S2$
 $SCIN = 3/4 \text{ scin planes}$
 $ELHI = SCIN * PRHI * SHLO$
 $ELLO = 2/3 \text{ of } (SCIN, STOF, PRLO)$
 Vetoed by CER
 $ELREAL = ELHI + ELLO$

$PION = SCIN$
 Vetoed by CER
 $PIPRE = PION * (gate)$
 $PRETRIG = ELREAL + PIPRE + RANDOM$

NOTE: Veto requires using the PS754 4-fold logic unit.

To TDC/Scaler