

## 1. Description

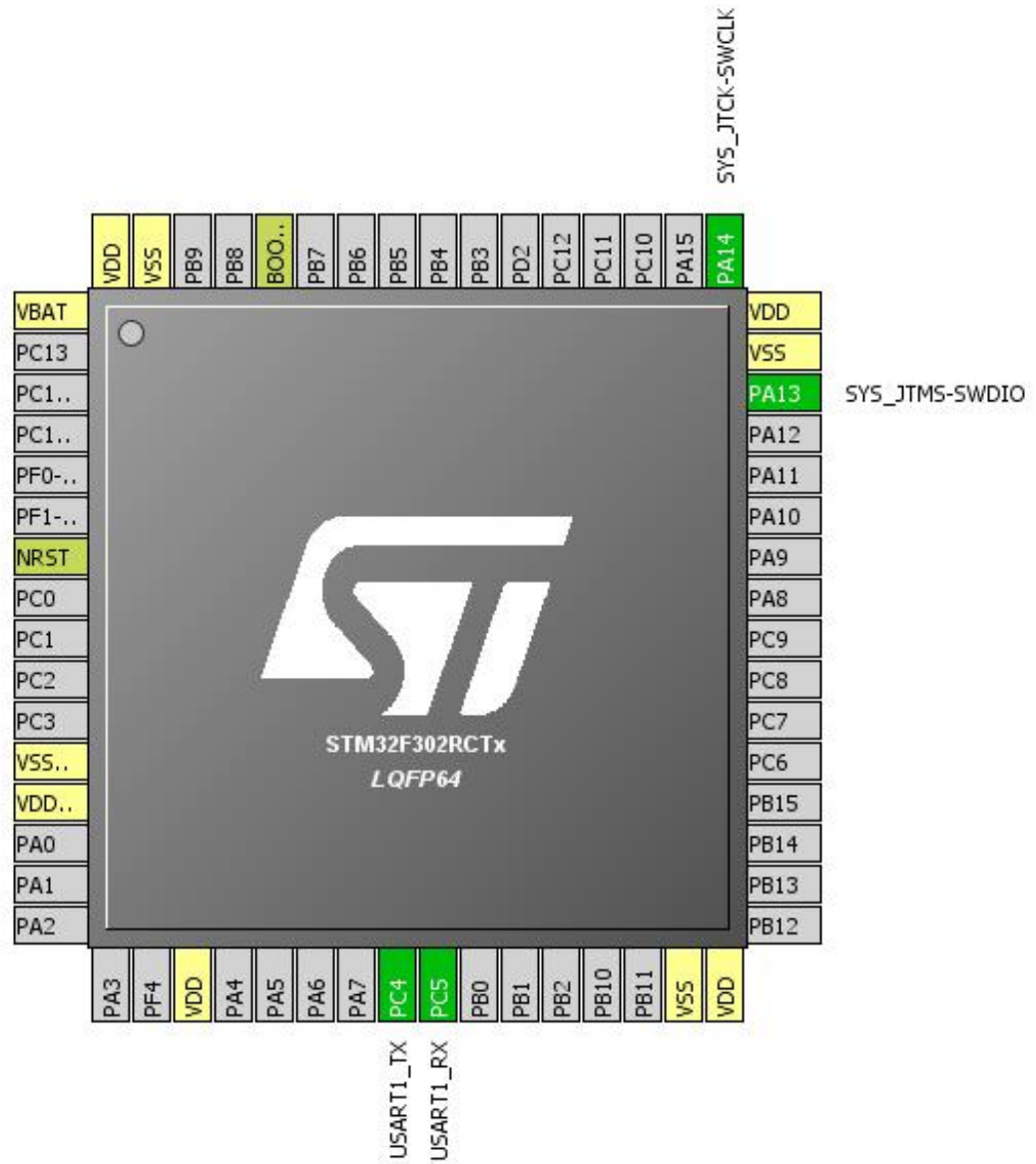
### 1.1. Project

Project Name	jel_base
Board Name	No information
Generated with:	STM32CubeMX 4.24.0
Date	02/22/2018

### 1.2. MCU

MCU Series	STM32F3
MCU Line	STM32F302
MCU name	STM32F302RCTx
MCU Package	LQFP64
MCU Pin number	64

## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
12	VSSA/VREF-	Power		
13	VDDA/VREF+	Power		
19	VDD	Power		
24	PC4	I/O	USART1_TX	
25	PC5	I/O	USART1_RX	
31	VSS	Power		
32	VDD	Power		
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		



## 5. IPs and Middleware Configuration

### 5.1. SYS

Debug: Serial Wire

Timebase Source: SysTick

### 5.2. TIM1

Clock Source : Internal Clock

#### 5.2.1. Parameter Settings:

##### Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	0
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 16 bits value)	0
auto-reload preload	Disable

##### Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection TRGO	Reset (UG bit from TIMx_EGR)
Trigger Event Selection TRGO2	Reset (UG bit from TIMx_EGR)

### 5.3. USART1

Mode: Asynchronous

#### 5.3.1. Parameter Settings:

##### Basic Parameters:

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

##### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

Single Sample	Disable
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**Advanced Features:**

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

\* **User modified value**

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
USART1	PC4	USART1_TX	Alternate Function Push Pull	No pull up pull down	<b>High *</b>	
	PC5	USART1_RX	Alternate Function Push Pull	No pull up pull down	<b>High *</b>	

### 6.2. DMA configuration

nothing configured in DMA service

### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break and TIM15 interrupts	unused		
TIM1 update and TIM16 interrupts	unused		
TIM1 trigger, commutation and TIM17 interrupts	unused		
TIM1 capture compare interrupt	unused		
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	unused		
Floating point unit interrupt	unused		

\* User modified value



## ***7. Power Consumption Calculator report***

### 7.1. Microcontroller Selection

Series	STM32F3
Line	STM32F302
MCU	STM32F302RCTx
Datasheet	025186_Rev7

### 7.2. Parameter Selection

Temperature	25
Vdd	3.6

## ***8. Software Pack Report***