Homework 2

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What is your last digit of your SID (0 is regarded as 10)? This value is defined as NUM\_1 in the whole question paper. (Since my last digit is 7, NUM\_1 is 7)

### Question 1. (15%)

Consider the following RISC-V instructions. Please note that we treat NUM\_1%2 and NUM\_1%2+1 as decimal values.

```
li a1, NUM_1%2
      li a2, NUM_1%2+1
2
      li a3, 6
3
      LOOP:
4
      slti t0, a3, 1
      bne t0, zero, DONE
6
      add a4, a1, a2
      addi a1, a2, 0
8
      addi a2, a4, 0
      addi a3, a3, -1
10
      jal x0, LOOP
      DONE:
12
      # end of the program
    1. How many times is the branch instruction executed? (7%)
```

2. What are the final values of a1 and a2. (8%)

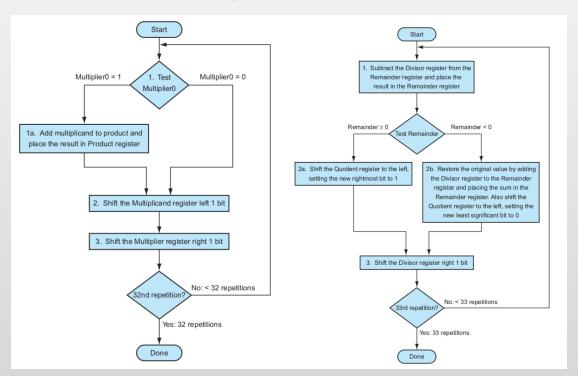
# Answer:

- 1. 6 times (with one more check which is line 5 and 6 as shown)
- 2. a1 = 21, a2 = 34

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#### Question 2. (20%)

Read through the multiplication / devision algorithm:



Left: multiplication algorithm, Right: division algorithm

Write down the step by step procedure to calculate  $5\times2$  or  $0101\times0010$ . Use Multiplier0 to indicate the least significant bit of the multiplier. List the initial values and the values in 1st to 4th iterations of Multiplier, Multiplier0, Multiplicand and Product. In each iteration, list the values after 1, 2 and 3 steps in Figure 1 left separately. (Represent Multiplier as 4bits, Multiplier0 as 1bit, Multiplicand as 8bits, Product as 8bits.)

#### Answer:

Iteration	$\operatorname{Step}$	Multiplier	Multiplier0	Mcand	Product
0	Initial value	010 <u>1</u>	1	0000 0010	0000 0000
1	1⇒Prod=Prod+Mcand	0101	1	0000 0010	0000 0010
	Shift left Multiplicand	0101	1	0000 0100	0000 0010
	Shift right Multiplier	001 <u>0</u>	0	0000 0100	0000 0010
2	Shift left Multiplicand	0010	0	0000 1000	0000 0010
	Shift right Multiplier	000 <u>1</u>	1	0000 1000	0000 0010
3	$1\Rightarrow Prod=Prod+Mcand$	0001	1	0000 1000	0000 1010
	Shift left Multiplicand	0001	1	0001 0000	0000 1010
	Shift right Multiplier	000 <u>0</u>	0	0001 0000	0000 1010
4	Shift left Multiplicand	000 <u>0</u>	0	0010 0000	0000 1010
	Shift right Multiplier	0000	0	0010 0000	0000 1010

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#### Question 3. (20%)

IEEE 754 Floating-Point Standard

- 1. What decimal number does this single precision float  $C13C0000_{16}$  represent? (Show your work.) (10 %)
- 2. What is -1.510 in IEEE single precision binary floating point format? (Show your work.) (10%)

#### Answer:

1. (a) by breaking the hexadecimal number into binary, we get

$$\mathtt{C13C0000}_{16} = \mathtt{1100000100111110000000000000000000}_2$$

- (b) the first bit indicate the sign, in this case it is a negative number
- (c) the next 8 bits are used to express the exponent of 2 offset by -127, so the exponent in this case is  $10000010_2 127_{10} = 130_{10} 127_{10} = 3_{10}$
- (e)  $1011_2 = 11_{10} \& 0.11_2 = 0.5 + 0.25 = 0.75_{10}$
- 2. (a) Since it is negative, the first bit is 1
  - (b) Since the part before the decimal point is -1 only, there is no need to shift the bits exponent = 127
  - (c) then convert the number after the decimal point by multiplying 2 and extract the decimal number for 23 times (mantissa is only 23 bits),

```
0.51 \times 2 = \underline{1}.02 \Rightarrow 1
                                                 0.02 \times 2 = 0.04 \Rightarrow 0
                                                                                                   0.04 \times 2 = 0.08 \Rightarrow 0
0.08 \times 2 = 0.16 \Rightarrow 0
                                                 0.16 \times 2 = 0.32 \Rightarrow 0
                                                                                                   0.32 \times 2 = 0.64 \Rightarrow 0
0.64 \times 2 = 1.28 \Rightarrow 1
                                                 0.28 \times 2 = 0.56 \Rightarrow 0
                                                                                                   0.56 \times 2 = 1.12 \Rightarrow 1
0.12 \times 2 = 0.24 \Rightarrow 0
                                                 0.24 \times 2 = 0.48 \Rightarrow 0
                                                                                                   0.48 \times 2 = 0.96 \Rightarrow 0
0.96 \times 2 = 1.92 \Rightarrow 1
                                                 0.92 \times 2 = 1.84 \Rightarrow 1
                                                                                                   0.84 \times 2 = 1.68 \Rightarrow 1
0.68 \times 2 = 1.36 \Rightarrow 1
                                                                                                   0.72 \times 2 = 1.44 \Rightarrow 1
                                                 0.36 \times 2 = 0.72 \Rightarrow 0
0.44 \times 2 = 0.88 \Rightarrow 0
                                                 0.88 \times 2 = 1.76 \Rightarrow 1
                                                                                                   0.76 \times 2 = 1.52 \Rightarrow 1
0.52 \times 2 = 1.04 \Rightarrow 1
                                                 0.04 \times 2 = \underline{0}.08 \Rightarrow 0
                                                                                                   0.08 \times 2 = \underline{0}.16 \Rightarrow 0
so we have -1.1000 0010 1000 1111 0101 110
```

(d) so the single precision floating point number is

 $1011, 1111, 1100, 0001, 0100, 0111, 1010, 1110_2 = BFC147AE_{16}$ 

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# Question 4. (10%)

Consider the following instruction: Instruction: xor rd, rs1, rs2

Interpretation: Reg[rd] = Reg[rs1] XOR Reg[rs2]

- 1. What are the values of control signals generated by the control in figure 2 for this instruction?
- 2. Which resource (block) produces no output for this instruction?

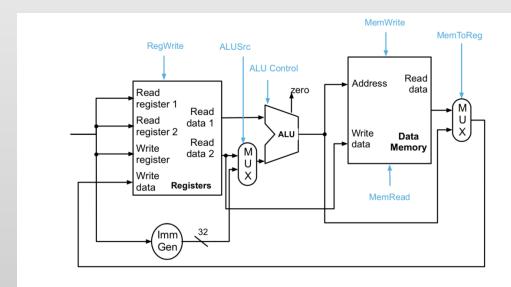


Figure 2: The datapath for the memory instructions and the R-type instructions.

# Answer:

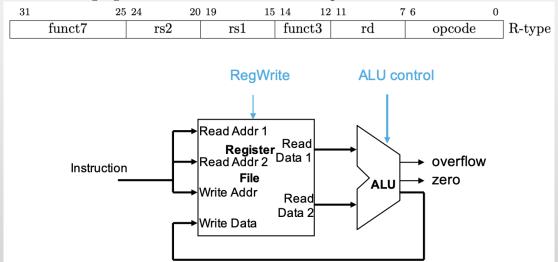
- 1. RegWrite = true, ALUSrc = 0, ALU Control = "xor", MemWrite = false, MemRead = false, MemToReg = 0
- 2. Data Memory

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# Question 5. (15%)

The following figures show the format and datapath of an R format instruction.



- 1. Assume we have an instruction whose machine code is 0x00c5d533, please write down the instruction in assembly language. (10%)
- 2. Which ports in the datapath do we use for addressing rs1, rs2, and rd? (5%)

#### Answer:

1. By breaking down the machine code, it is 000000001100010111010101100112. By bit0-7, we can know that it is a R-type instruction. By looking at the "funct3" (bit12-14 =  $101_2 = 0x5$ ), we know that it is a shift right function. By looking at the "funct7" (bit25-31 =  $000\ 000_2 = 0$ ), so we can know that it is a shift right logical function. Therefore the code instruction in assembly is:

2. The port "Read Addr 1" and "Read Addr 2" are used for addressing rs1 and rs2 respectively, while the "Write Addr" port is used for addressing rd.

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### Question 6. (20%)

In this exercise, we examine how pipelining affects the clock cycle time of the processor. Problems in this exercise assume that individual stages of the datapath have the following latencies:

IF	ID	$\mathbf{E}\mathbf{X}$	MEM	WB
$300 \mathrm{ps}$	500 ps	200ps	350 ps	250 ps

- 1. What is the clock cycle time in a pipelined and non-pipelined (single-cycle) processor?
- 2. What is the total latency of an lw instruction in a pipelined and non-pipelined (single-cycle) processor?
- 3. If we can split one stage of the pipelined datapath into two new stages, each with half the latency of the original stage, which stage would you split and what is the new clock cycle time of the processor?

#### Answer:

1. Clock cycle time:

• pipelined: max(300, 500, 200, 350, 250) = 500ps

• non-pipelined: 300 + 500 + 200 + 350 + 250 = 1600ps

2. Total latency:

• pipelined:  $500 \times 5 = 1500 ps$ 

 $\bullet$  non-pipelined: Single clock cycle time = 1600ps

3. I would split the stage "ID" as it has the longest latency which "EX" and "WB" only occupies less than or equal to half of its latency.

New clock cycle time =  $500 \div 2 = 250$ ps