

香港中文大學

The Chinese University of Hong Kong

CENG3430 Rapid Prototyping of Digital Systems

Lab00: Vivado Installation



Vivado ML Edition



- Vivado ML Edition 2023.2 will be used for the development of lab exercises and final project.
 - It is publicly available at:
 https://www.xilinx.com/support/download/index.html/content/
 /xilinx/en/downloadNav/vivado-design-tools/2023-2.html

Do it First!!!

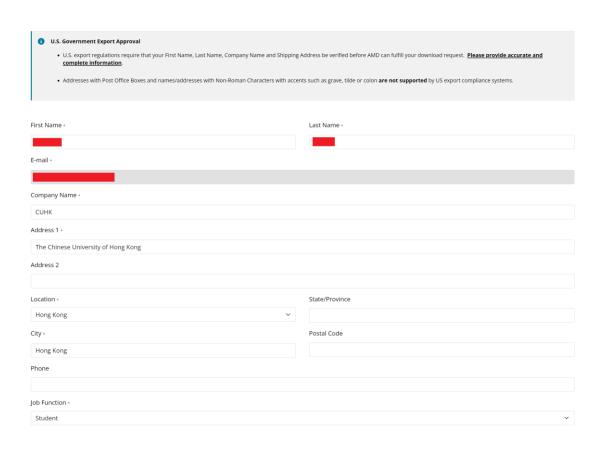
- It has an official FREE license.
 - You should register an online account and obtain a free license.
 - Register at: https://www.xilinx.com/registration/create-account.html
- It supports Microsoft Windows and Linux.
 - If you are using Mac, you may install Windows using BootCamp, or Parallels Desktop, or VMware Fusion Player.
 - Make sure there is **90GB free space** reserved for Vivado.
 - · This tutorial and all the lab sheets will be based on Windows.

Registration



- Use your email to register
 - Example:

Download Center - Name and Address Verification



Registration



If you encounter this error

Product Licensing - Name and Address Verification

Please correct the errors and send your information again.

We cannot fulfill your request as your account has failed export compliance verification.

U.S. Government Export Approval

- U.S. export regulations require that your First Name, Last Name, Company Name and Shipping Address be verified before Xilinx can fulfill your download request.
 Please provide accurate and complete information.
- Addresses with Post Office Boxes and names/addresses with Non-Roman Characters with accents such as grave, tilde or colon are not supported by US export compliance systems.

Maybe you are using an old account.

Please register a new account and try the verification again.

(Any email is OK, e.g., gmail, link.cuhk.edu.hk, ...)

Download



Select the 2023.2 version

Version

2023.2

2023.1

2022.2

2022.1

Vivado Archive

ISE Archive

CAE Vendor Libraries Archive

Vivado ML Edition - 2023.2 Full Product Installation

Important Information

Vivado™ ML 2023.2 is now available for download:

Meeting Fmax targets

- Increase performance of designs in Versal Premium and Versal HBM devices with automatic place & route of SLR crossings
- Faster device image generation with multi-threaded support

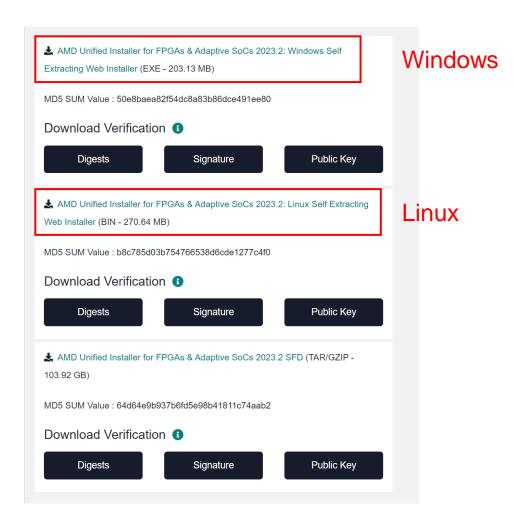
Ease of use enhancements in IPI, DFX, Debug and Simulation

- New GUI window added for address path visualization from source and sink for Versal devices in IPI
- Manually Assigned Address Locking Capability in BDs (IPI)

Download



Select the installer based on your OS



Download



Click "Download" and open the installation file

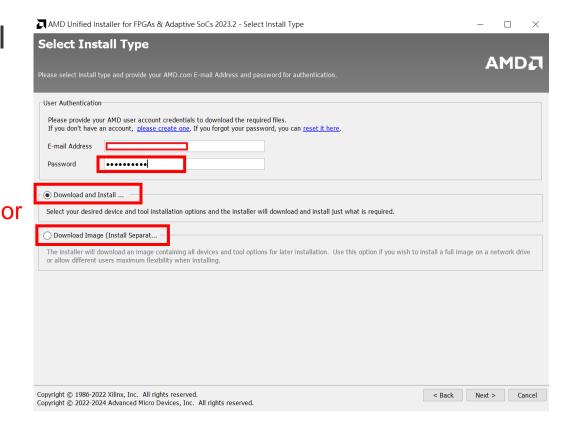
Phone
Job Function ∗
Student
Filename:
FPGAs_AdaptiveSoCs_Unified_2023.2_1013_2256_Win64.exe
If you are downloading the Vivado / Vitis unified installer, you will receive a follow-up confirmation email with a notice regarding our Developer Program.
You can read about how we handle your personal data, your personal data rights, and how you can contact us in our privacy policy.
Download



- Sign in with User ID (email address) and Password
- Choose either
 - Download and Install
 - Downland Image

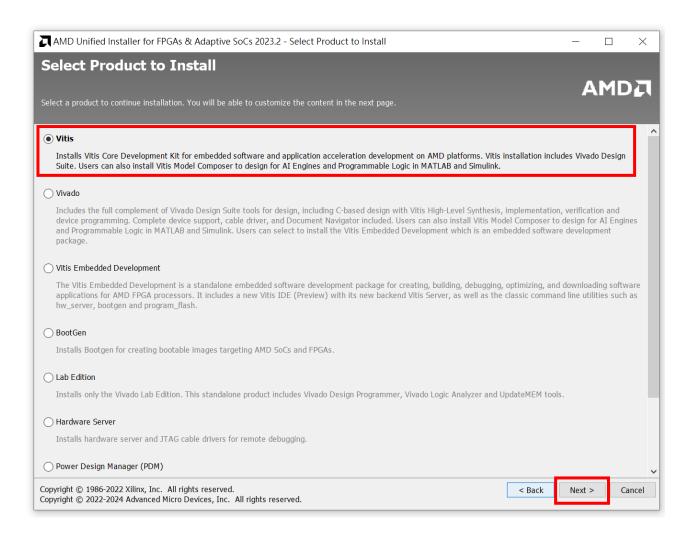
Download and install (If failed in the middle, you need to start over)

Download the image first, and then you install afterward



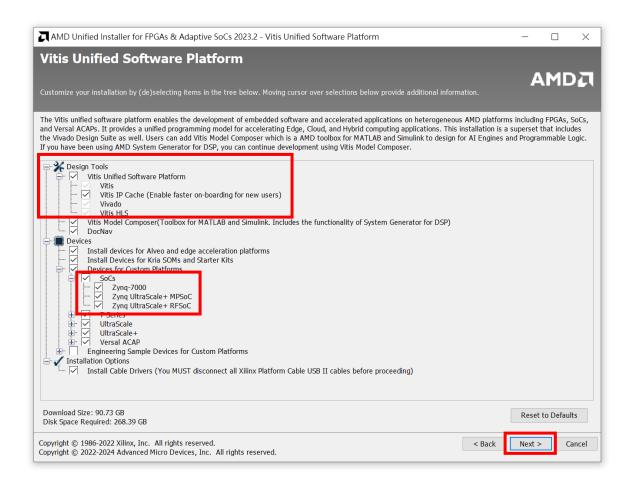


Select "Vitis" and then click "Next"



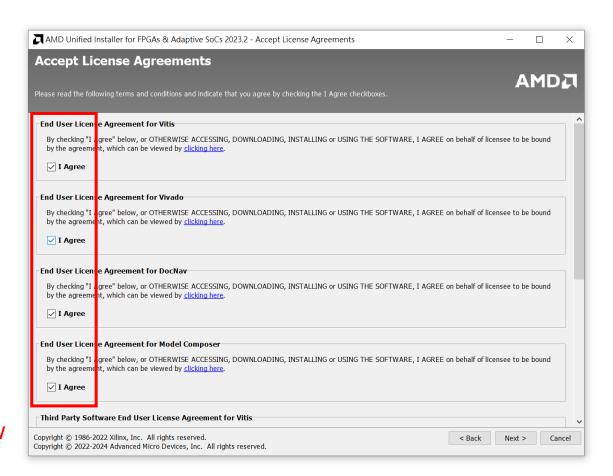


 Select all the "Vitis Unified Software Platform" and all the "SoCs" and then click "Next"





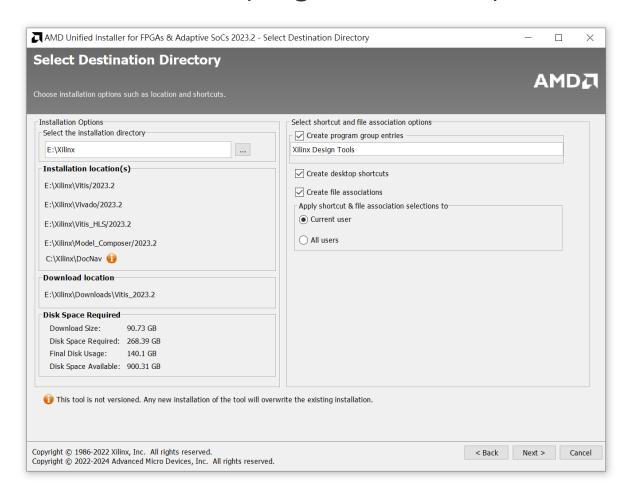
Select all "I Agree" and click "Next"



More in below



- Make sure you have enough free disk space to install
- Then wait until finish (Might > 2-3hrs)





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Thank You!

