

1. DRC Violation: 0

Total Area:  $258.115 \mu\text{m}^2$

Total Power:  $2.33769348 \text{ mW}$

Slack:  $0.031 \text{ ns}$

2.  $263.7466 \mu\text{m}^2$

3. Total area unchanged, while slack reduced to  $0.024 \text{ ns}$  with increasing row density. Total area unchanged probably because of the power routing grid, and slack decreased because of the reduced separation between standard cells.

