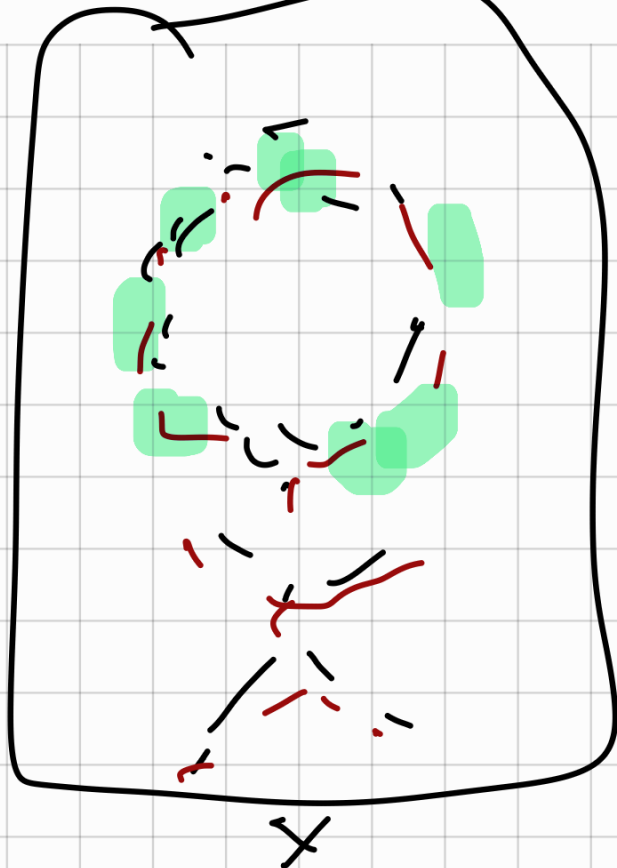
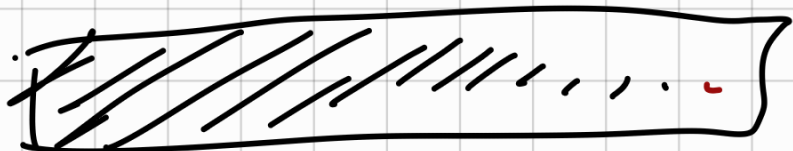


Left to right
downwards.



[0]



0

256
[6]

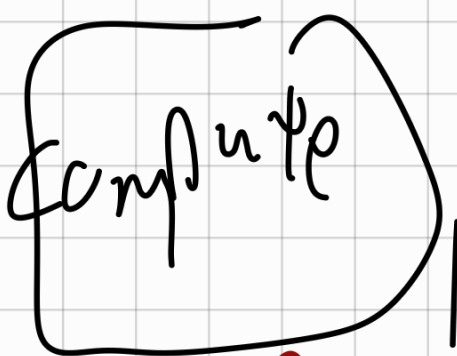
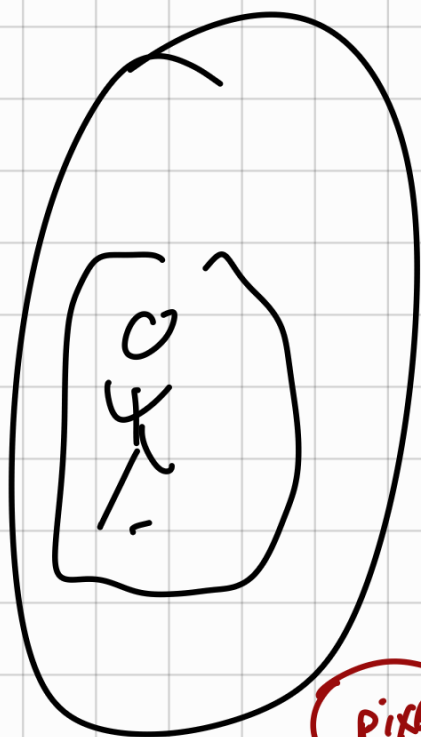
1000-0000

196

255
- 196

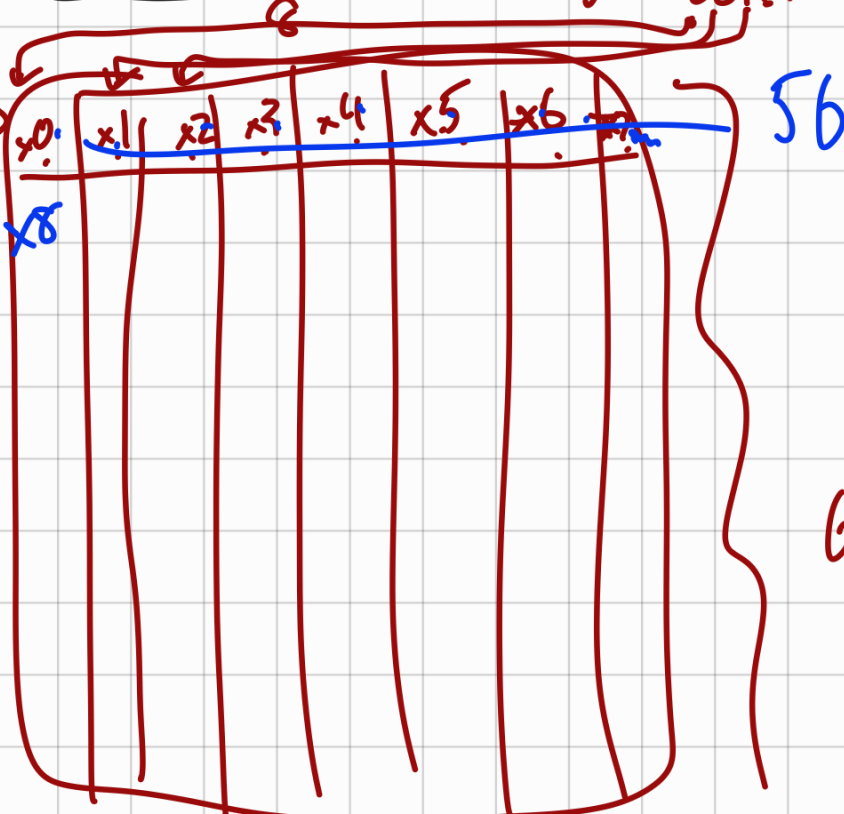
40

4bit
① 11 111
① 00 0100
2



56 = 00111000

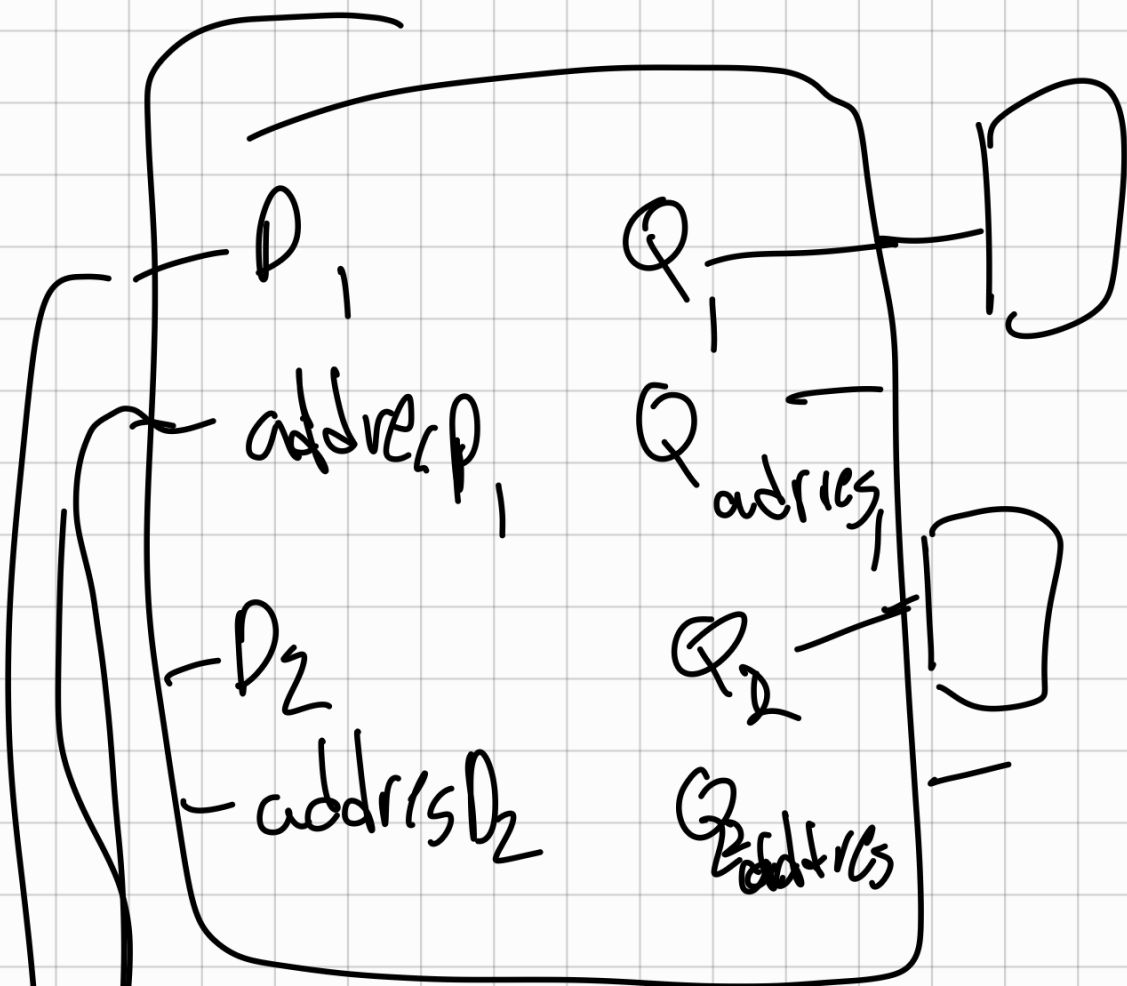
19K
pixel1
pixel2
pixel3



65K

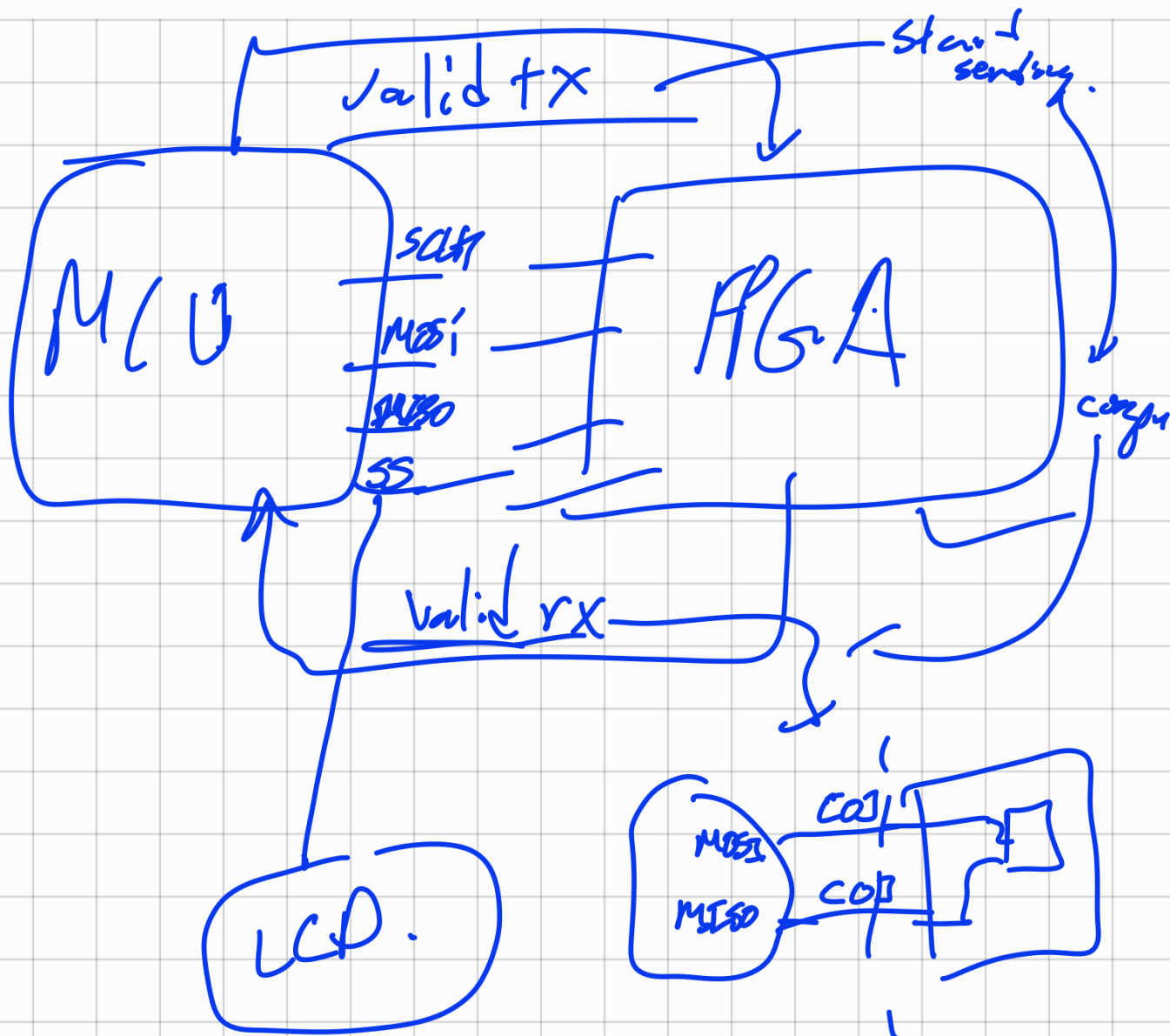
old
[7:0]

for (< code > [7:0]
 old ← sram[x][y] [7:0] IRD
 sram[x][y] ← closest (sram[x][y]) [7:0] IRD
 quant error ← [0] ②
 sram[x+1][y] ← sram[x+1][y] + quant ④ ③
 [7:0] [7:0]
 x 4
 - - -
 - - -



SPI/MOSI

center.



Key - Server Limit. & Printer.

Task - MCU flash 4MB.
SPI controller.