

1 Data Structures

1.1 Basic Types

The following are the basic types, out of which others are built, and which will be referred to. There is generally, but not always, a direct relationship to a C++ primitive.

Name	Closest C++ Equivalent	Description
Integer	int	Whole number
Float	float	Floating point number
Queue	std::list	FIFO queue
List(type)	std::list<type>	
String	std::string	String object that provides operations to manipulate itself
File	std::iostream	Abstract type to represent simple I/O operations
Map(KeyType → ValueType, DEFAULT: DefaultValue)	std::unordered_map<KeyType, ValueType>	A map to translate values of type KeyType to values of type ValueType. If the key isn't present, returns DefaultValue

The following are complex types, which are further defined below. This is merely a quick description of each.

Name	Description
Blif	Parent object, contains all information about a BLIF file and provides useful operations
BlifModel	Represents a circuit within a BLIF file, and provides methods to manipulate said circuit
BlifNode	A circuit element, or node in the DFG representing the circuit
Signal	A signal within a specific circuit, or BlifModel, representing a set of edges with common source

```
1 Signal
2 {
3     string name;
4     BlifNode* source;
5     list<BlifNode*> sinks;
6 }
```

2 Algorithm

Types marked with an * are defined in the previous section (Data Structures). We're given a blif file as

Algorithm 2 Main Algorithm

Variable	Type	Description
<i>input</i>	file	Input blif file
<i>targetRecoveryTime</i>	float	Per partition recovery time (in seconds)
<i>files</i>	list of files	circuit partitions, one per file
<i>file</i>	file	
<i>header</i>	string	string containing the first three lines of the input file
<i>output</i>	file	output file

varMain

main

```

1: procedure MAIN(input, targetRecoveryTime)
2:   files  $\leftarrow$  Partition(input)
3:   for all file  $\in$  files do
4:     file  $\leftarrow$  Triplicate(file)
5:   end for
6:   header  $\leftarrow$  input.lines[0  $\rightarrow$  3]
7:   file  $\leftarrow$  Join(files, header)
8:   output  $\leftarrow$  Flatten(output)
9: end procedure

```

input. In line 11 we partition the input circuit into a number of sub circuits, each in a separate file. Then in lines 12-13 we iterate over all the partitions, and transform them into a triplicated partition with three copies and a voter circuit. Then in line 14 we extract the original header, which provides the name, inputs and outputs of the original circuit. We then, in line 15, join all the partitions together with the original name, inputs and outputs (in the same order), as the original circuit.

Variable	Type	Description
<i>file</i>	file	input file
<i>targetRecoveryTime</i>	float	maximum per partition recovery time (in seconds)
<i>blif</i>	Blif*	In-memory representation of input blif file
<i>circuit</i>	BlifModel*	Main circuit from input file, represented as DFG
<i>partition</i>	BlifModel*	Circuit, which we are adding nodes to, to make our partition
<i>queue</i>	Queue	FIFO queue of nodes to visit
<i>visited</i>	Map(BlifNode* \rightarrow bool)	Map of whether a BlifNode is visited
<i>signal</i>	Signal*	
<i>circuit.outputs</i>	List of Signal*	List of output Signal* of a circuit
<i>signal.source</i>	BlifNode*	Node which drives this Signal*
<i>queue.size</i>	integer	Number of nodes in queue
<i>node</i>	BlifNode*	
<i>file</i>	file	
<i>files</i>	List of file	
<i>numPartitions</i>	int	Counter of number of partitions
<i>signalName</i>	string	Name of a Signal*
<i>node.inputs</i>	List of string	List of names of signals which are inputs to this node
<i>model.signals</i>	Map(string \rightarrow Signal*)	Map from signal name to Signal* representing it in that BlifModel*

Table 1: Variables for Partition

varPart

Algorithm 3 Partition

```

1: procedure PARTITION(file)
2:   blif  $\leftarrow$  new Blif(file) ▷ Read in file
3:   circuit  $\leftarrow$  blif.main ▷ The actual circuit within the blif file
4:   partition  $\leftarrow$  new BlifModel ▷ Empty Circuit
5:   queue  $\leftarrow$  new Queue ▷ Empty Queue
6:   visited  $\leftarrow$  new Map(BlifNode  $\rightarrow$  bool, DEFAULT: false)
7:   for all signal  $\in$  circuit.outputs do
8:     queue.Enqueue(signal.source)
9:   end for
10:  while queue.size > 0 do
11:    node  $\leftarrow$  queue.Dequeue()
12:    if visited[node] = true then
13:      continue ▷ Handle each node once and only once
14:    end if
15:    visited[node]  $\leftarrow$  true
16:    partition.AddNode(node)
17:    if partition.RecoveryTime() > targetRecoveryTime then
18:      partition.RemoveNode(node)
19:      CutLoops(partition)
20:      file  $\leftarrow$  partition.WriteToFile()
21:      files  $\leftarrow$  files + file
22:      numPartitions  $\leftarrow$  numPartitions + 1
23:      partition  $\leftarrow$  new BlifModel ▷ Empty Circuit
24:    end if
25:    for all signalName  $\in$  node.inputs do
26:      signal  $\leftarrow$  model.signals[signalName]
27:      queue.Enqueue(signal)
28:    end for
29:  end while
30:  if partition.size > 0 then
31:    CutLoops(partition)
32:    file  $\leftarrow$  partition.WriteToFile()
33:    files  $\leftarrow$  files + file
34:  end if
35:  return files
36: end procedure

```

Lines 2-6 are setting up our variables with initial values. We read a blif file in to memory, where it is represented as a DFG with a number of properties as described in [Reference](#). In line 3, circuit refers to the main circuit of a blif file. As we only support non-heirarchical blif files, this will always be the only circuit. In lines 7-8 we push our outputs onto the queue, to start traversing. Line 11 pops the node from the front of the queue. Next, in lines 12-15 we check if this node is already marked as visited. If so, we skip it as we only add each node to exactly one partition. Otherwise, we mark it as visited and proceed to partition it. In lines 16-17 we add the node to the current partition, and test if we're still within our recovery time. If not, then in lines 18-20 we remove the current node from the partition, cut cycles within the partition, and write the partition out to a file. One file per partition. Then in 21-22 we update our collection of output files and increment our counter for the number of partitions, and finally, in line 23-24 we create a new empty circuit for our next partition, and add the node to it. Then, we add the inputs to this node to our queue, and continue traversing and partitioning until we've reached every node. Lastly, in lines 31-35 we check if our current partition has anything in it. If so, cut loops and write it out. !

Algorithm 5 RecoveryTime

Variable	Type	Description
<i>latency</i>	float	Circuit latency (i.e. time for input to completely propagate to output) in seconds
<i>clockFrequency</i>	Integer	Operating frequency of the circuit, in seconds
<i>criticalPath</i>	Integer	Maximum number of steps between an input and an output
<i>numFF</i>	Integer	Number of Latches in circuit
<i>numLUT</i>	Integer	Number of look up tables in circuit
<i>resynchronisationTime</i>	Float	Time, in seconds, that it takes to resynchronise circuit
<i>detectionTime</i>	Float	Time, in seconds, that it takes to detect an error
<i>ReconfigureTime</i>	Float	Time, in seconds, that it takes to reconfigure circuit
<i>communicationTime</i>	Float	Time, in seconds, that it takes to transmit reconfiguration request to controller

varPart
main

```
1: procedure RECOVERYTIME(partition)
2:   latency  $\leftarrow$  frequency  $\times$  criticalpath
3:   detectionTime  $\leftarrow$  latency
4:   resynchronisationTime  $\leftarrow$  latency
5:   reconfigurationTime  $\leftarrow$   $\max(\text{numFF}, \text{numLUT})/10/15 \dots \text{morestuff}$ 
6:   communicationTime  $\leftarrow$  numPartitions  $\times$  latency  $\times$  morestuff
7:   recoveryTime  $\leftarrow$  detectionTime + resynchronisationTime + reconfigurationTime +
   communicationTime
8:   return recoveryTime
9: end procedure
```

Algorithm 7 CutLoops

Variable	Type	Description
<i>partition</i>	BlifModel*	BlifModel* containing DFG representing partition to cut cycles in
<i>state</i>	Map(BlifNode* \rightarrow int)	Map of whether a node is UNKNOWN, EXPLORING, or FINISHED
<i>signal</i>	Signal*	
<i>partition.outputs</i>	List of Signal*	List of Signal* representing primary outputs of circuit

varPart

main

```
1: procedure CUTLOOPS(partition)
2:   state  $\leftarrow$  Map(BlifNode*  $\rightarrow$  int, DEFAULT : 0)
3:   for all signal  $\in$  partition.outputs do
4:     CutLoopsRecursive(state, NULL, signal)
5:   end for
6: end procedure
```

Start recursing from outputs back to detect loops. Line 3 starts the recursive traversal for each output, with no parent.

Algorithm 9 CutLoopsRecursive

Variable	Type	Description
<i>partition</i>	BlifModel*	BlifModel* containing DFG representing partition to cut cycles in
<i>state</i>	Map(BlifNode* \rightarrow int)	Map of whether a node is UNKNOWN, EXPLORING, or FINISHED
<i>parent</i>	BlifNode*	Parent node
<i>signal</i>	Signal*	
<i>node</i>	BlifNode*	
<i>signal.name</i>	string	Name of a signal, as it appears in the blif file
<i>node</i>	BlifNode*	
<i>node.inputs</i>	List(string)	A list of names of input signals
<i>signalName</i>	string	Name of a signal, as it appears in the blif file
<i>partition.signals</i>	Map(string \rightarrow Signal)*	Map from signal name to signal with that name in that partition
<i>signal</i>	Signal*	

varPart

main

```
1: procedure CUTLOOPSRECURSIVE(partition, state, parent, signal)
2:   node  $\leftarrow$  signal.source
3:   if state[node] = EXPLORING then ▷ Found a cycle
4:     ReplaceSignalName(parent.inputs, signal.name, "qqrin" + signal.name)
5:   else if state[node] = FINISHED then ▷ Already explored this path
6:     return
7:   else
8:     state[node] = EXPLORING
9:     for all signalName  $\in$  node.inputs do
10:       signal  $\leftarrow$  partition.signals[signalName]
11:       CutLoopsRecursive(partition, state, node, signal)
12:     end for
13:   end if
14: end procedure
```
