


Date	Description
2018/06/21	1. Add Rx1,Rx2,CX1,CX2,CX3,CX4 for ICE interface issue 2. Add TP1
2018/08/02	1. R85,R86 DNI 2. R83 Load 3. VBUS change to EX_5V, move out SW4 4. VBUS connect to EX_5V via diode 5. change USB ESD protect diode(D12) 6. change USB data path from GPIO8/9 to GPIO 0/1 7. Add D1, D2

OPL1000 A1 DevKit



旺凌科技

Title	
History	
Size	Document Number
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ICE interface

M3_CLK
GPIO21 $RX1\ 330R\pm1\%$
M3_DAT

M0_CLK
GPIO18 $RX2\ 330R\pm1\%$
M0_DAT

GPIO9
GPIO10
GPIO11
GPIO12
GPIO13
GPIO14
GPIO15
GPIO16
GPIO17
GPIO19
GPIO20
GPIO22

J2
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17

J3
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17

GPIO8
GPIO7
GPIO23
GPIO6
GPIO5
GPIO4
GPIO3
GPIO2
GPIO0
RST_N
CHIP_EN

Ex_5V
VBAT

header 17x1
header 17x1

CX1 3pF/50V ±5%DNI
CX2 13pF/50V ±5%DNI
CX3 13pF/50V ±5%DNI
CX4 13pF/50V ±5%DNI

C83 10uF/16V ±20%

[illegible]

The schematic diagram illustrates the electrical connections for the ATtiny800 module. It includes the following components and connections:

- Power Supply:**
 - +3V:** Connected to the VCC pin (pin 7) and the +3V pin on the module header.
 - GND:** Connected to the GND pin (pin 8) and the GND pin on the module header.
- Reset:**
 - RST_N:** Connected to the RST_N pin on the module header.
 - CHIP_EN:** Connected to the CHIP_EN pin on the module header.
- I2C Interface:**
 - SCL:** Connected to the SCL pin (pin 6) on the module header.
 - SDA:** Connected to the SDA pin (pin 5) on the module header.
- Module Pins:**
 - GPIO12, GPIO15, GPIO16:** Connected to the module header pins.
 - GPIO13, GPIO14:** Connected to the module header pins.
 - GPIO17:** Connected to the module header pin.
- Internal Components:**
 - Resistors:** R114 (10K \pm 1%), R126 (10K \pm 1%), R81 (NC), R82 (0R), R83 (10K), R85 (0R/DNI), R86 (0R/NP), R160 (10K \pm 1%).
 - Capacitors:** C36 (0.1uF/16V \pm 5%), C65 (0.1uF/16V \pm 5%), C18 (0.1uF/16V \pm 5%), C31 (4.7uF/+10%/-10V).
 - Crystal:** 32.768KHz \pm 20ppm, Y3.
 - Switches:** SW2 (B3U-1000P), SW3 (B3U-1000P/NP).

[illegible]