

# Jennifer Brana

PHD STUDENT, CARNEGIE MELLON UNIVERSITY

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## Research Interests

I am interested in the intersection of hardware and software systems, particularly in the area of parallel computing and heterogeneous systems. My aim is to increase the scalability and sustainability of future computing systems.

*Research areas:* computer architecture; computer systems; memory systems; sustainability.

## Education

### Carnegie Mellon University

PH.D IN COMPUTER SCIENCE

Advisor: NATHAN BECKMANN

Pittsburgh, PA

Beginning June 2023

### University of Portland

B.S. IN COMPUTER SCIENCE (3.9 GPA), *Cum Laude*

MINOR IN COMPUTER ENGINEERING.

Portland, OR

Aug. 2019 - May 2023

## Publications

### Kobold: Simplified Cache Coherence for Cache-Attached Accelerators

Jennifer Brana, Brian C. Schwedock, Yatin A. Manerkar, Nathan Beckmann

IEEE CAL 2023

to appear

### Kobold: Simplified Cache Coherence for Cache-Attached Accelerators

Jennifer Brana, Brian C. Schwedock, Yatin A. Manerkar, Nathan Beckmann

WDDSA @ MICRO 2022

## Talks and Presentations

Kobold: Simplified Cache Coherence for Cache-Attached Accelerators

WDDSA @ MICRO, 2 Oct. 2022

Kobold: Simplified Cache Coherence for Cache-Attached Accelerators

SRC @ MICRO, 3 Oct. 2022

Comparison of Computer Architecture Specialization Methods for Performance and Power Efficiency

University of Portland Founders' Day, 12 April 2022

## Professional Experience

### Carnegie Mellon University

GRADUATE RESEARCH ASSISTANT

- Researching in computer architecture.

Pittsburgh, PA

June 2023 - Present

### Computer Organization Research Group (CORGi)

UNDERGRADUATE RESEARCH ASSISTANT

- Research methods to design cache coherence protocols for cache-attached accelerators.

Carnegie Mellon University

May 2022 - Present

### Team Lift

SENIOR CAPSTONE

- Deployed a connected network of sensors and computation nodes in an infrastructure-limited environment in Malawi, Africa.

Portland, OR; Karonga, Malawi

Aug. 2022 - May 2023

### University of Portland

UNDERGRADUATE RESEARCHER

- Investigated CPU specialization methods to increase the performance and efficiency of Viterbi Decoding.
- Simulated processor architecture using gem5 and modeled processor power consumption using McPat.

Portland, OR

Jan. 2022 - May 2022

### Intelligent, Complex, Adaptive, and Networks Lab

UNDERGRADUATE RESEARCH ASSISTANT

- Researched EEG-based view of comprehension of truth statements to understand how humans process undefined statements.

University of Portland

May 2021 - August 2021

## Honors & Awards

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2023	<b>NSF Graduate Research Fellowship</b> , GRFP	NSF
2023	<b>CS Outstanding Student Award</b> , For combination of coursework, research, and service.	U of Portland
2020	<b>Tau Beta Pi Induction</b> , National engineering honor society.	U of Portland
2019-2023	<b>President's Scholarship</b>	U of Portland
2019-2023	<b>FIRST Robotics Scholarship</b>	U of Portland

## Service & Leadership

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<b>Tau Beta Pi</b>	University of Portland
OREGON GAMMA CHAPTER PRESIDENT	2021 - 2022
• Responsible for planning meetings and activities to engage club members ranging from career development to design competitions.	
<b>Society of Women Engineers</b>	University of Portland
MENTOR	2020 - 2023
• Mentored freshman girls in the engineering program.	
<b>Tutoring Working Group</b>	University of Portland
STUDENT REPRESENTATIVE	2021
• Worked with faculty members to redesign the tutoring program for the Shiley School of Engineering following the Covid-19 pandemic to increase freshman and sophomore retention rates.	

## Teaching

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<b>University of Portland</b>	
Theory of Computation (CS 357)	Grader, Fall 2022
Digital Systems Design (EE 332)	Tutor, Spring 2022
Signals & Systems (EE 262)	Tutor, Spring 2022
Logic Design (EE 231)	Grader and Tutor, Fall 2021
Electrical Circuits (EE 261)	Tutor, Fall 2021-Spring 2022
Electrical Circuits Lab (EE 271)	Lab Assistant, Spring 2021

## Skills

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<b>Programming Languages</b>	C, C++, Python, Java, Assembly, MATLAB, Haskell, Verilog HDL, LaTeX
<b>Parallel Programming</b>	Experience in parallel/GPU computing using CUDA C/C++, OneTBB
<b>Computer Architecture Tools</b>	Experience using gem5, SLICC, McPat, Murphi model checker, CACTI, ProtoGen/HieraGen
<b>Other</b>	Proficiency with Unix, SSH, Git/Github, Xcode, VSCode. Experience in Agile