

RFSoc 2x2 Project; Next steps

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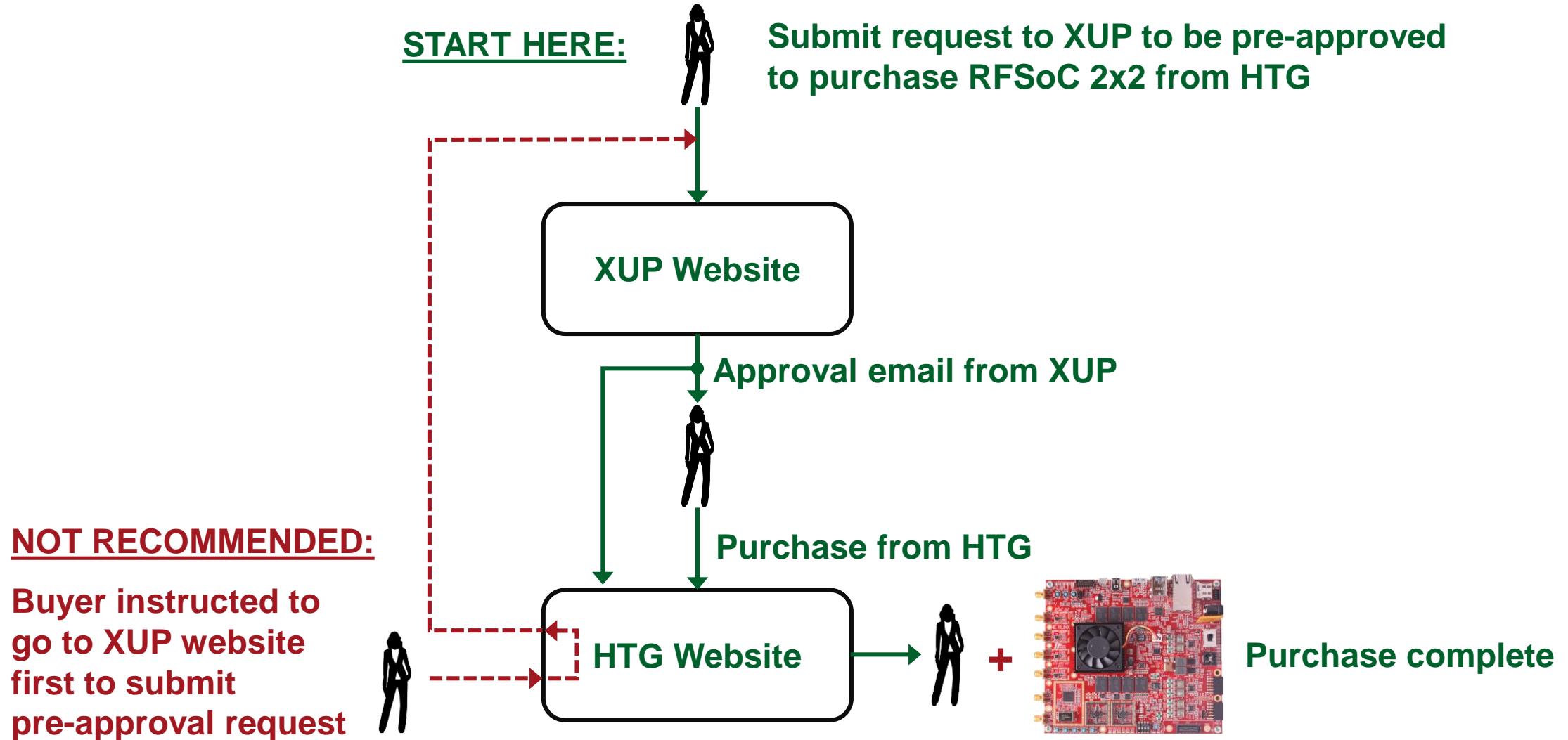
FPGA21 RFSoc PYNQ Tutorial





**We invite you to join
the RFSoc 2x2 community**

RFSoc 2x2 Purchase Process



XUP Website: <https://www.xilinx.com/support/university.html>

The image displays two screenshots of the Xilinx website, specifically the Xilinx University Program page and the RFSoc 2x2 Kit product page.

Left Screenshot: Xilinx University Program

The page features a dark header with the Xilinx logo and navigation links (Solutions, Products, Support). The main content area is titled "Xilinx University Program" and includes a large hero section for the "New Low-Cost RFSoc Teaching and Research Platform". This section shows a red circuit board with a fan and a screenshot of the PYNQ software interface. Below the hero section, there is a section for "Xilinx Adaptive Compute Clusters" with a list of bullet points:

- Support novel research in adaptive compute acceleration for high performance computing (HPC)
- Clusters are strategically located to provide access to the worldwide researchers
- Equipped with latest Alveo hardware and Vitis software technologies

The right sidebar contains sections for "What's New", "Events", "Videos", and "Resources".

Right Screenshot: RFSoc 2x2 Kit

This page is titled "RFSoc 2x2 Kit" and features a large image of the red circuit board. Below the image is a "Click to expand" button. The page includes a navigation bar with tabs for Overview, Hardware, Tools & IP, Docs & Designs, and Purchase. The "Overview" tab is selected, showing a detailed description of the kit and its features:

Xilinx's Radio Frequency System-on-Chip (RFSoc) devices have created a new class of integrated circuit architecture for the communications and instrumentation markets. RFSocs combine high-accuracy ADCs and DACs operating at Giga samples per second with programmable heterogeneous compute engines.

XUP is offering the **RFSoc 2x2 kit** exclusively for academic customers. The kit features:

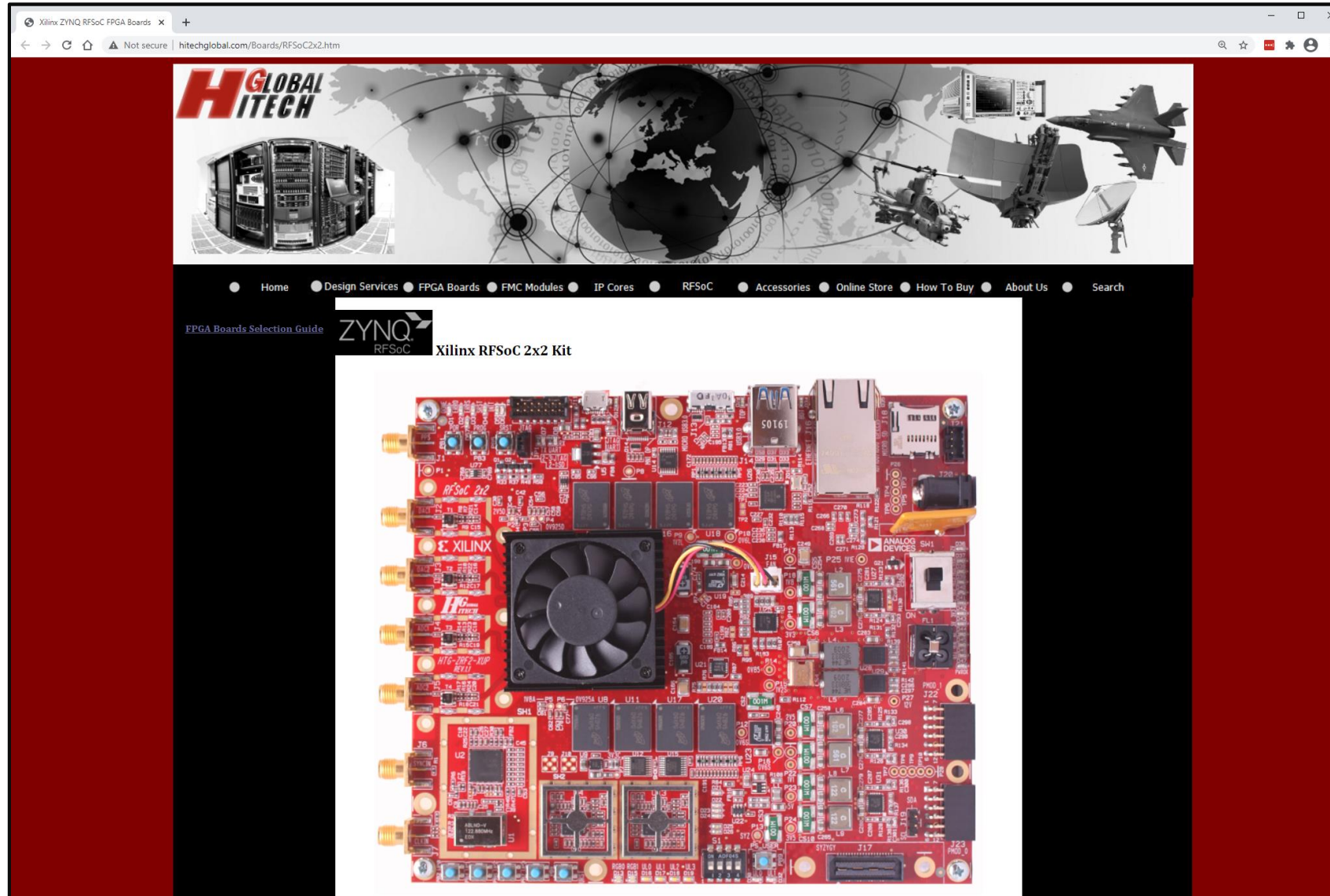
- Affordable price of \$1,899 available only to academic customers
- RFSoc 2x2 board with 2 RF DAC and 2 RF ADC channels
- PYNQ framework with Jupyter Lab for exceptional ease-of-use
- Open-source resources including teaching materials, notebooks, and design examples
- Complete end-to-end reference designs including spectrum analyzers and software defined radios
- Dedicated project webpage at rfsoc-pynq.io
- GitHub-hosted repositories of all project materials
- Online community support forum

Below the features list, there is a section titled "Register with XUP as academic to purchase ?" and another section titled "What is included?" with a list of items:

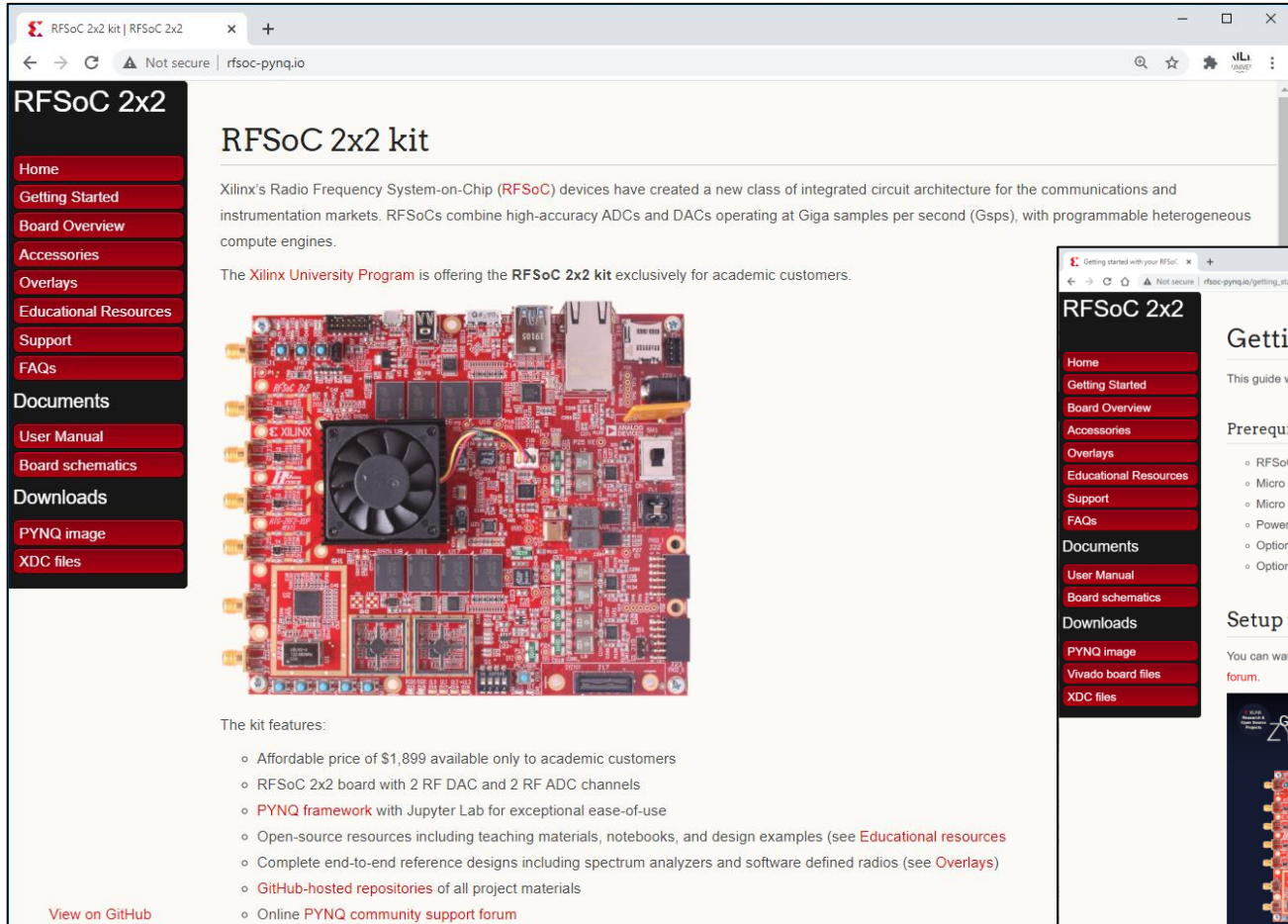
- RFSoc 2x2 Board
- 12V 72W power supply unit
- USB 3.0 A to Micro B Cable
- 2 RF cables with SMA connectors
- 16 GB MicroSD card with pre-loaded Linux and PYNQ image

A "Feedback" button is visible in the bottom right corner of the right screenshot.

High Tech Global: <http://www.hitechglobal.com/>



Project Website: rfsoc-pynq.io

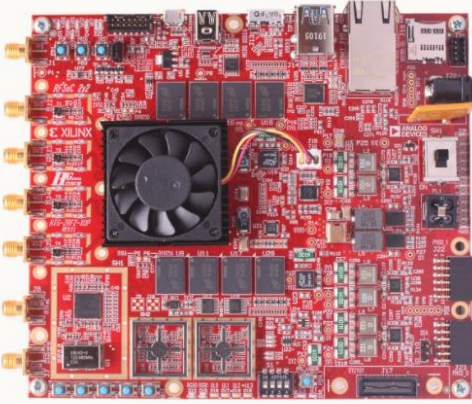


RFSoc 2x2

RFSoc 2x2 kit

Xilinx's Radio Frequency System-on-Chip (RFSoc) devices have created a new class of integrated circuit architecture for the communications and instrumentation markets. RFSocS combine high-accuracy ADCs and DACs operating at Giga samples per second (Gsp/s), with programmable heterogeneous compute engines.

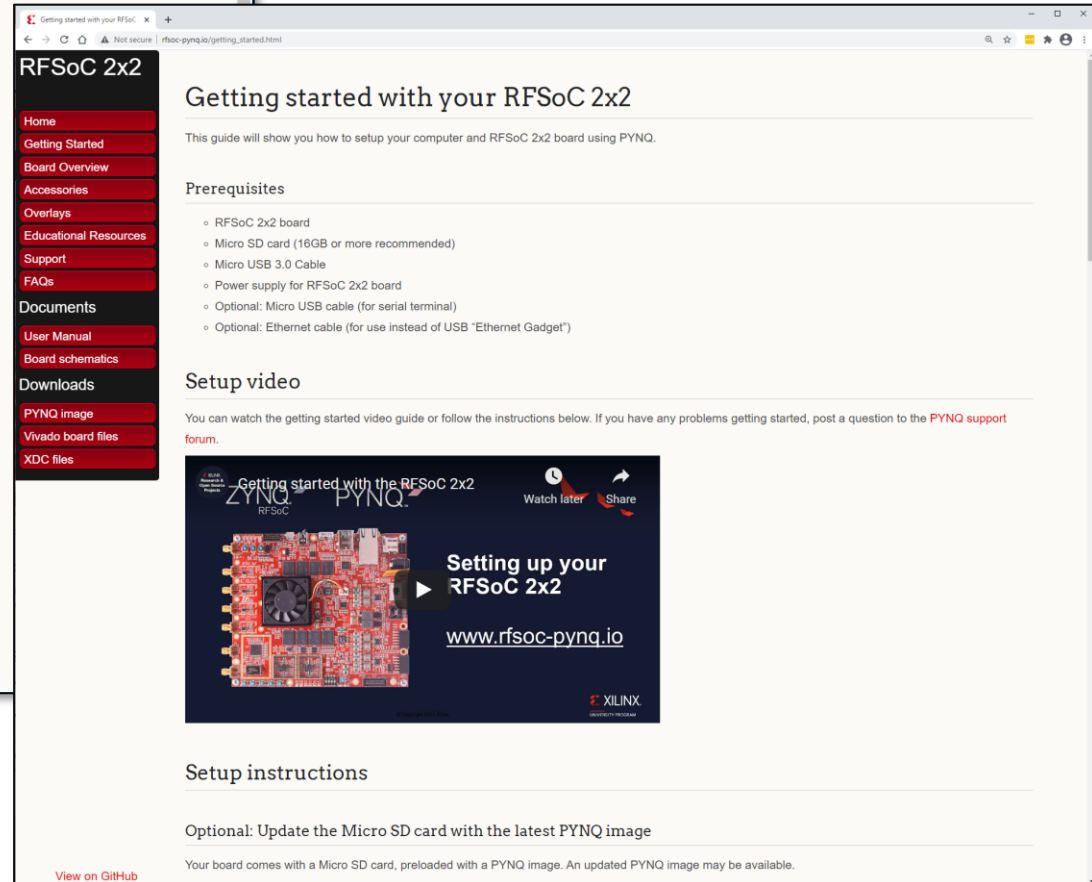
The [Xilinx University Program](#) is offering the **RFSoc 2x2 kit** exclusively for academic customers.



The kit features:

- Affordable price of \$1,899 available only to academic customers
- RFSoc 2x2 board with 2 RF DAC and 2 RF ADC channels
- **PYNQ framework** with Jupyter Lab for exceptional ease-of-use
- Open-source resources including teaching materials, notebooks, and design examples (see [Educational resources](#))
- Complete end-to-end reference designs including spectrum analyzers and software defined radios (see [Overlays](#))
- [GitHub-hosted repositories](#) of all project materials
- Online [PYNQ community support forum](#)

[View on GitHub](#)



Getting started with your RFSoc 2x2


This guide will show you how to setup your computer and RFSoc 2x2 board using PYNQ.

Prerequisites

- RFSoc 2x2 board
- Micro SD card (16GB or more recommended)
- Micro USB 3.0 Cable
- Power supply for RFSoc 2x2 board
- Optional: Micro USB cable (for serial terminal)
- Optional: Ethernet cable (for use instead of USB "Ethernet Gadget")

Setup video

You can watch the getting started video guide or follow the instructions below. If you have any problems getting started, post a question to the [PYNQ support forum](#).



Setup instructions

Optional: Update the Micro SD card with the latest PYNQ image

Your board comes with a Micro SD card, preloaded with a PYNQ image. An updated PYNQ image may be available.

[View on GitHub](#)

RFSoc 2x2 Overlays

RFSoc 2x2 overlays | RFSoc 2x2 x +

← → ↺ ⌂ ⚠ Not secure | rfsoc-pynq.io/overlays.html

RFSoc 2x2

Home

Getting Started

Board Overview

Accessories

Overlays

Educational Resources

Support

FAQs

Documents

User Manual

Board schematics

Downloads

PYNQ image

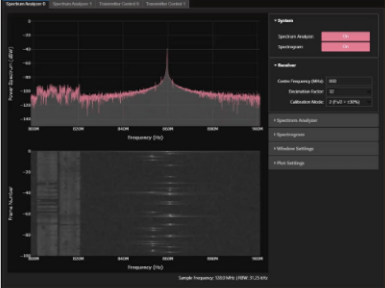
XDC files

RFSoc 2x2 overlays


Base Overlay

The base overlay is included in the PYNQ image for the RFSoc 2x2 board and will be available for you to use from the first time you start your board. The purpose of the base overlay design is to allow you to start exploring your board with PYNQ out-of-the-box.
See the [RFSoc 2x2 base overlay](#) page for more details.

Spectrum Analyzer

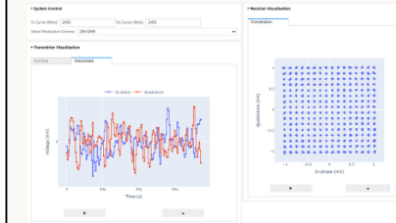
An RFSoc spectrum analysis tool is available on your RFSoc 2x2 from the first time you start your board. The spectrum analyzer was developed by the [University of Strathclyde](#) Software Defined Radio (SDR) research laboratory. You can use the spectrum analyzer tool to explore your surrounding RF spectrum.

You can follow this [link to the RFSoc Spectrum Analyzer GitHub page](#), where you can update your spectrum analyzer tool, contribute to the project, or post questions and feedback using the issue tracker.

RFSoc Frequency Planner

An RFSoc frequency planning tool created using Python is available to download and install on your RFSoc 2x2 development board. This frequency planning tool is based on the original RFSoc frequency planner that can be downloaded [here](#).

The Python based frequency planner uses widgets that allow the user to dynamically change system properties. You can follow this [link to the RFSoc Frequency Planner GitHub page](#), where you can download the frequency planner tool, contribute to the project, or post questions and feedback using the issue tracker.

RFSoc OFDM Demonstrator

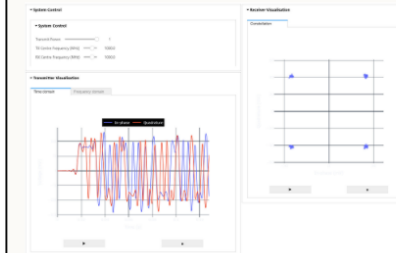
This overlay demonstrates the implementation of an Orthogonal Frequency Division Multiplexing (OFDM) transceiver on the RFSoc 2x2 board. PYNQ is used to control the underlying modulation scheme of the OFDM sub-carriers and for visualisation of data at various stages in the transmit/receive chain, such as the received constellations.



The OFDM demonstrator can transmit and receive up to 1024-QAM. You can follow this [link to the RFSoc OFDM demonstrator GitHub page](#), where you can download and contribute to the project, or post questions and feedback using the issue tracker.

RFSoc QPSK Demonstrator

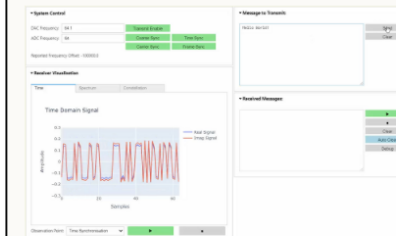
The QPSK demonstrator was the first University of Strathclyde RFSoc introspection system. The design is a full QPSK transceiver, which transmits and receives randomly-generated pulse-shaped symbols with full carrier and timing synchronisation. PYNQ is used to visualise the data at both the RF DAC and RF ADC, as well as visualising various DSP stages throughout the transmit and receive signal path.



The QPSK and PYNQ design has since been published in [IEEE Access](#). You can download and contribute to the project, or post questions and feedback from the GitHub repository, [RFSoc QPSK Demonstrator](#).

RFSoc BPSK Demonstrator

This overlay presents a BPSK transceiver radio design for RFSoc platforms. The radio is capable of transmitting and receiving BPSK modulated waveforms in loopback, or between RFSoc development boards running the same design. A simple "hello world" example is presented demonstrating that transmitted BPSK waveforms can be received, synchronised, and the payload extracted for analysis.



You can download and contribute to this project, or post questions and feedback from the GitHub repository, [RFSoc BPSK Radio Demonstrator](#).

Project Resources



RFSoc 2x2

- Home
- Getting Started
- Board Overview
- Accessories
- Overlays
- Educational Resources**
- Support
- FAQs

Documents

- User Manual
- Board schematics

Downloads

- PYNQ image
- XDC files

Educational Resources

The following educational material to support the Zynq RFSoc, and the RFSoc2x2 has been developed by the University of Strathclyde in partnership with Xilinx.

RFSoc Introduction Notebooks

There are a collection of RFSoc introductory notebooks specifically for your RFSoc2x2 development board. The RFSoc notebooks consist of the following topics:

- An Introduction to RFSoc
- The RF Data Converters
- Software Defined Radio and the RFSoc

You can obtain a copy of the RFSoc introductory notebooks from the following GitHub repository, [RFSoc Notebooks](#).

Digital Signal Processing Notebooks

Additional material has been developed by the University of Strathclyde to support teaching of fundamental concepts and techniques for digital signal processing design for wireless communications. The material consists of Jupyter notebooks that can be run on Jupyter on your computer, or on any PYNQ enabled board. These notebooks present introductory material for the following.

Core Topics:

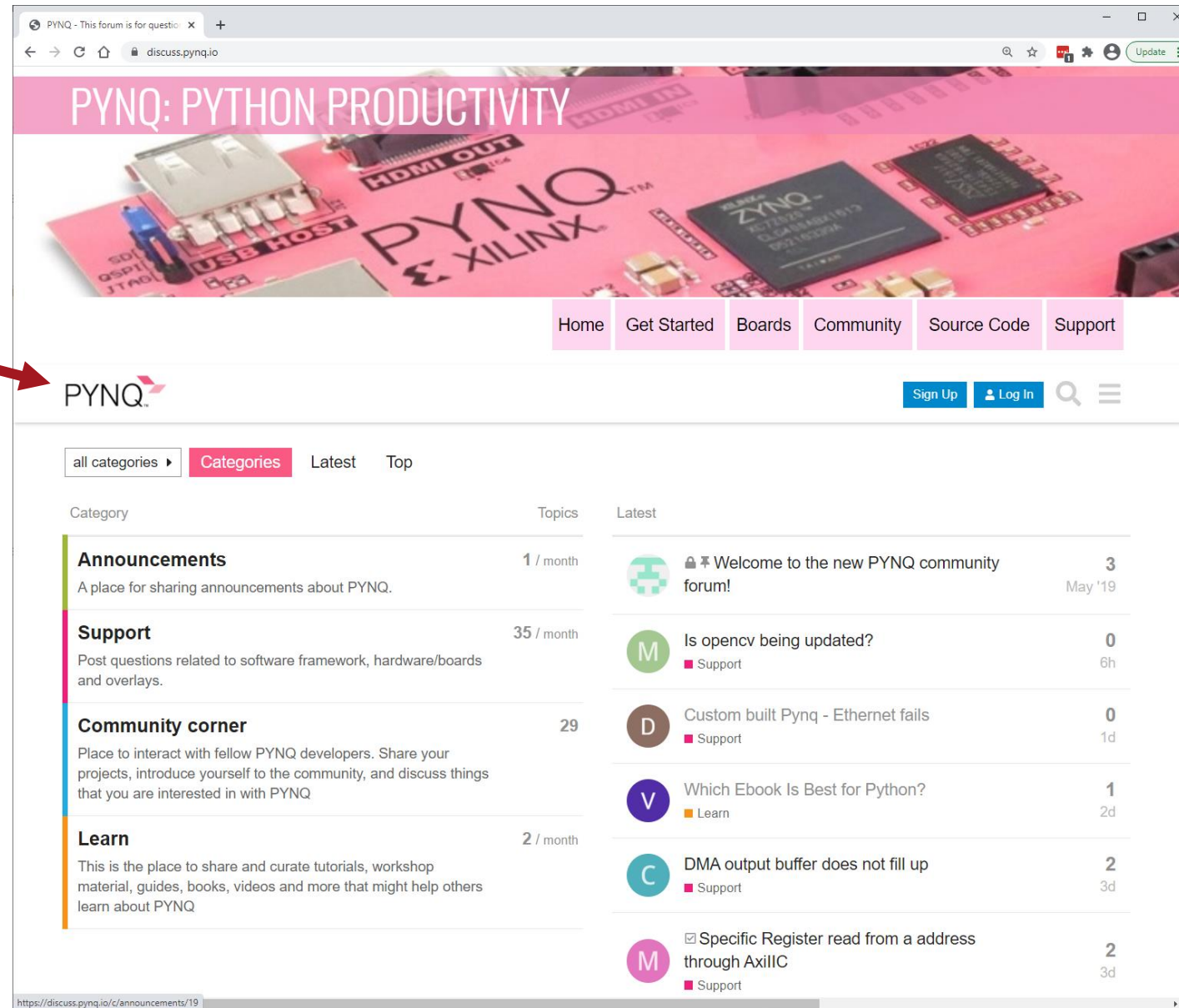
- Sampling and Quantisation
- The Frequency Spectrum
- Modulation and Demodulation
- Baseband Modulation
- Digital Filters







Specialised Topics:

- Machine Learning for Communication Systems
- OFDM Fundamentals

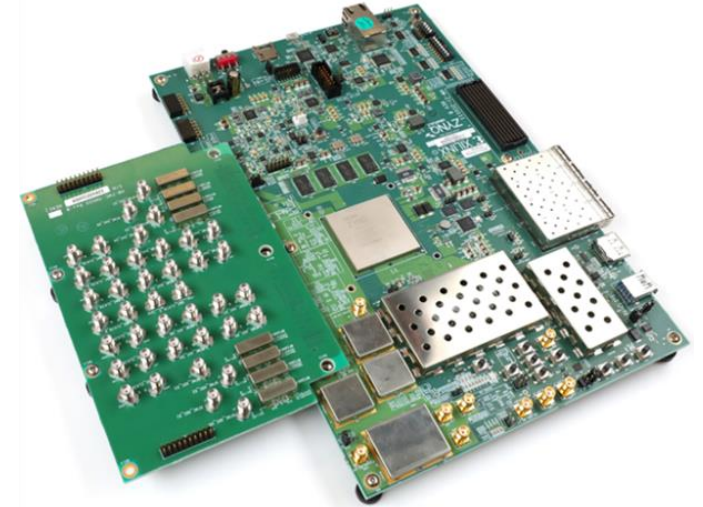
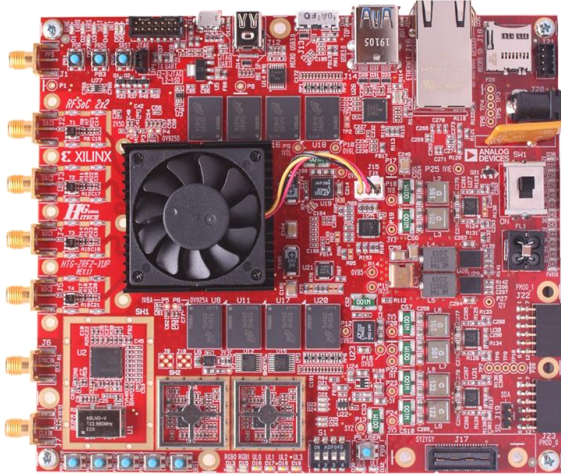
You can obtain a copy of these notebooks from the following GitHub repository, [DSP Notebooks](#).

PYNQ Community Support Forum: discuss.pynq.io

A screenshot of the PYNQ Community Support Forum interface. The browser address bar shows "discuss.pynq.io". The header features a pink banner with the text "PYNQ: PYTHON PRODUCTIVITY" and a background image of a PYNQ board. Below the banner is a navigation bar with links: Home, Get Started, Boards, Community, Source Code, and Support. The main content area is divided into two columns. The left column lists categories: Announcements (1 / month), Support (35 / month), Community corner (29), and Learn (2 / month). The right column shows a list of topics under the "Latest" heading, including "Welcome to the new PYNQ community forum!", "Is opencv being updated?", "Custom built Pynq - Ethernet fails", "Which Ebook Is Best for Python?", "DMA output buffer does not fill up", and "Specific Register read from a address through AxilIC".

Category	Topics	Latest
Announcements A place for sharing announcements about PYNQ.	1 / month	 Welcome to the new PYNQ community forum! 3 May '19
Support Post questions related to software framework, hardware/boards and overlays.	35 / month	 Is opencv being updated? 0 6h ■ Support
Community corner Place to interact with fellow PYNQ developers. Share your projects, introduce yourself to the community, and discuss things that you are interested in with PYNQ	29	 Custom built Pynq - Ethernet fails 0 1d ■ Support
Learn This is the place to share and curate tutorials, workshop material, guides, books, videos and more that might help others learn about PYNQ	2 / month	 Which Ebook Is Best for Python? 1 2d ■ Learn
		 DMA output buffer does not fill up 2 3d ■ Support
		 Specific Register read from a address through AxilIC 2 3d ■ Support

RFSoc 2x2 Complements ZCU111 Evaluation Kit

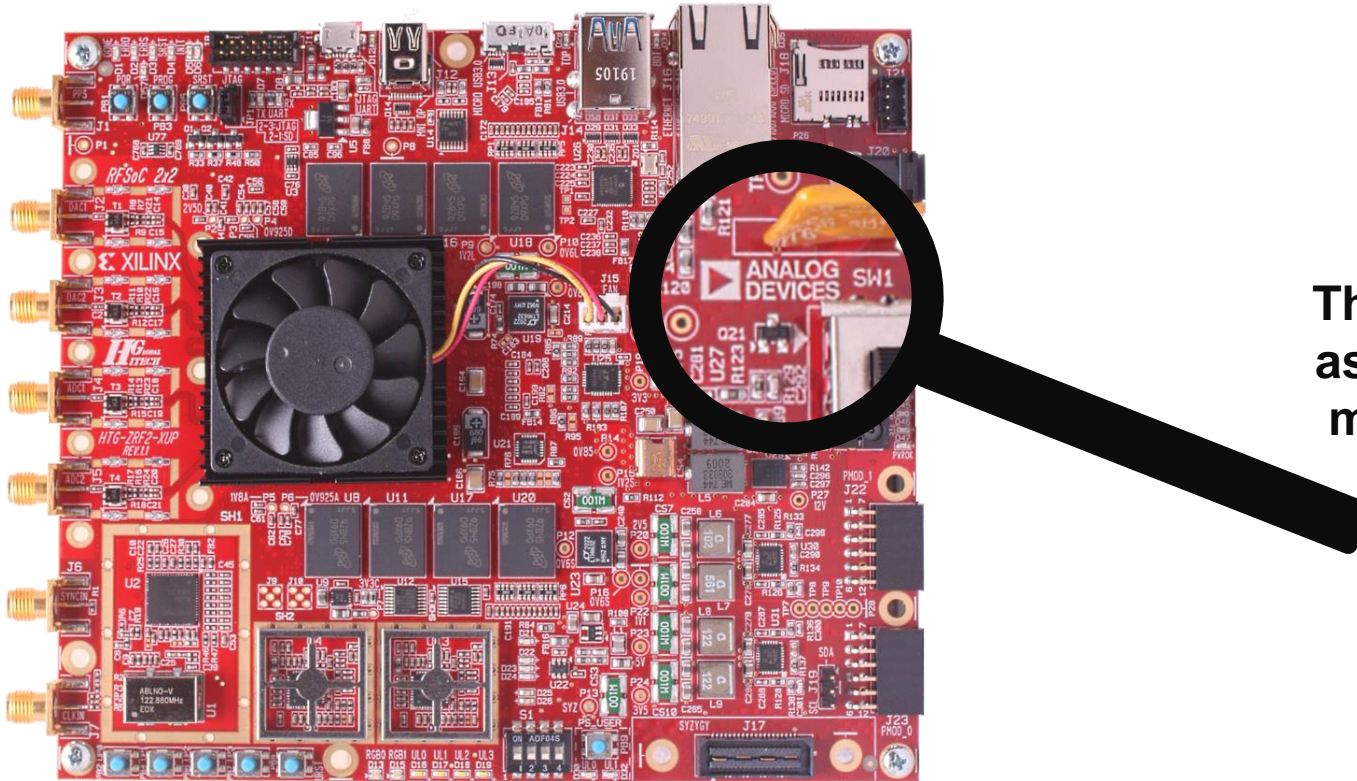


The RFSoc 2x2 kit is designed to complement the ZCU111 kit

- All the resources created for the RFSoc 2x2 are available for the ZCU111
- For example, there is a 4-channel spectrum analyzer for the ZCU111
- Both academia and industry can use the ZCU111 and the open-source resources

Acknowledgements

Analog Devices



**Thanks to ADI for their
assistance with power
management devices**



Thank You



Xilinx Mission

**Building the Adaptable,
Intelligent World**

