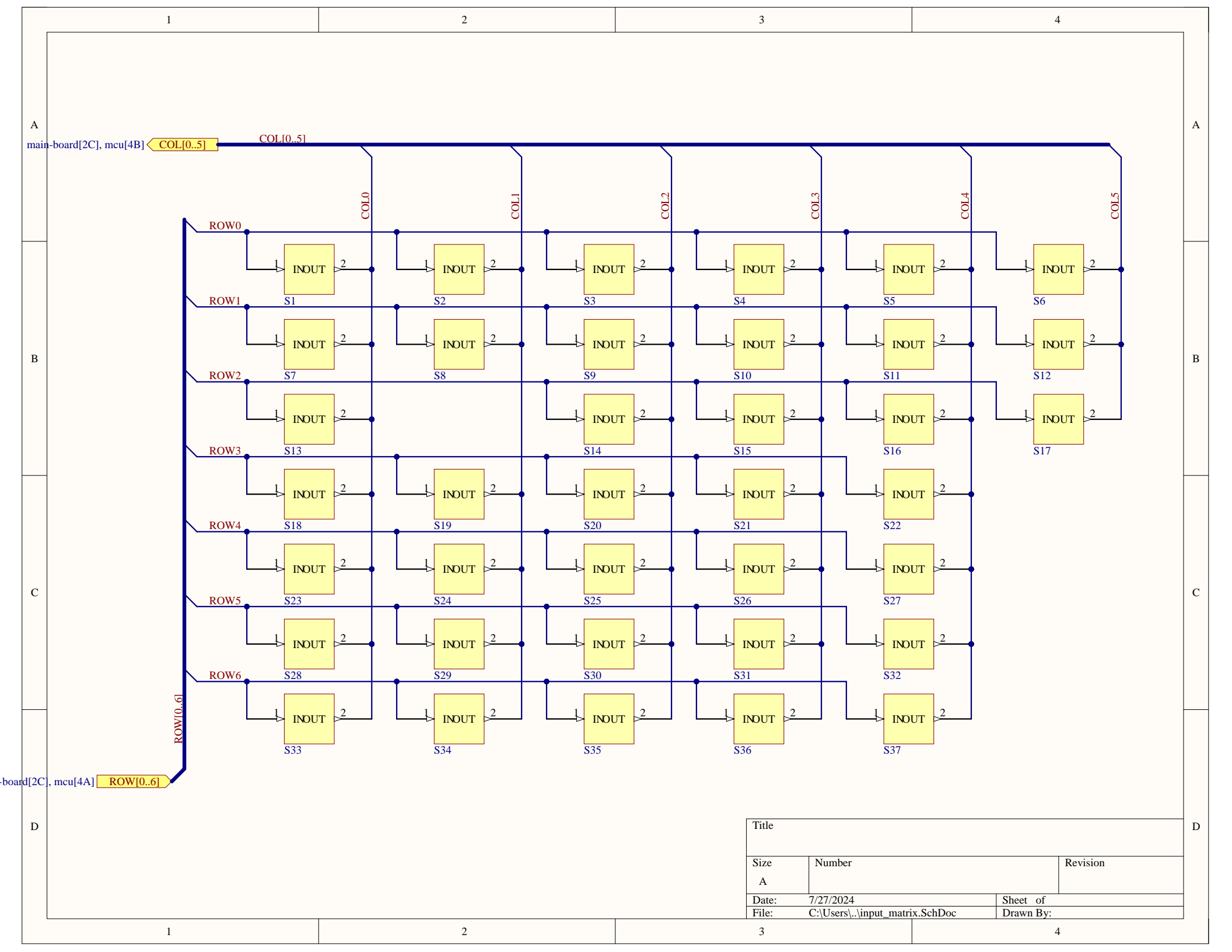
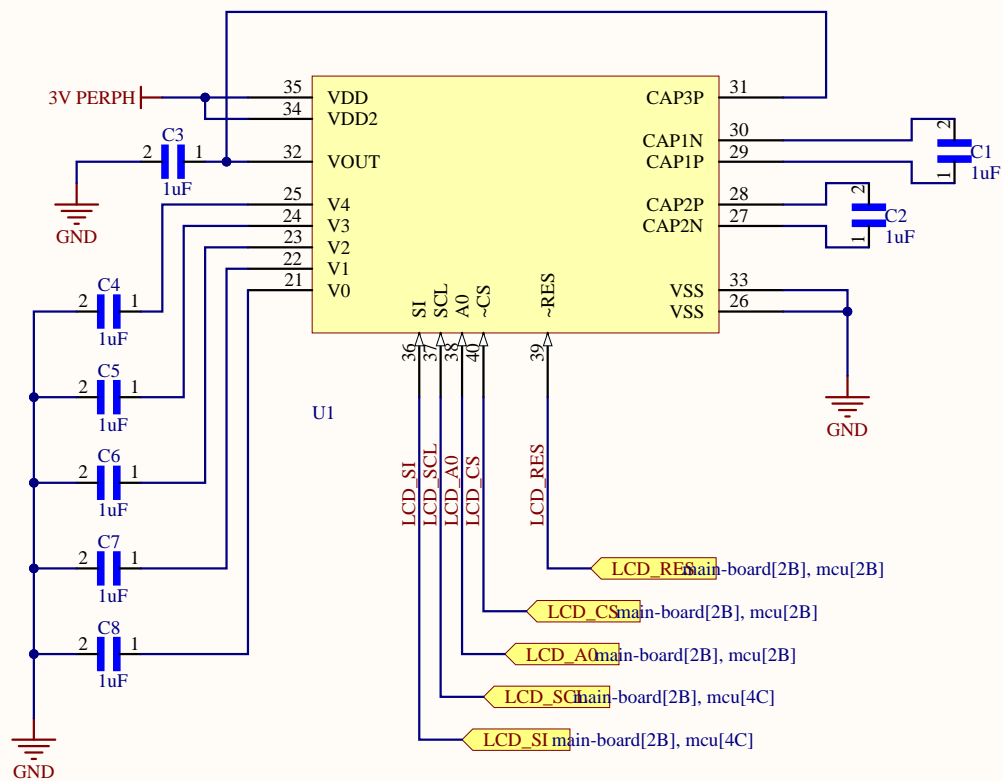


Title		
Size	Number	Revision
A		
Date:	7/27/2024	Sheet of
File:	C:\Users\...\main-board.SchDoc	Drawn By:





Title		
Size	Number	Revision
A		
Date:	7/27/2024	Sheet of
File:	C:\Users\...\screen.SchDoc	Drawn By:

A

A

B

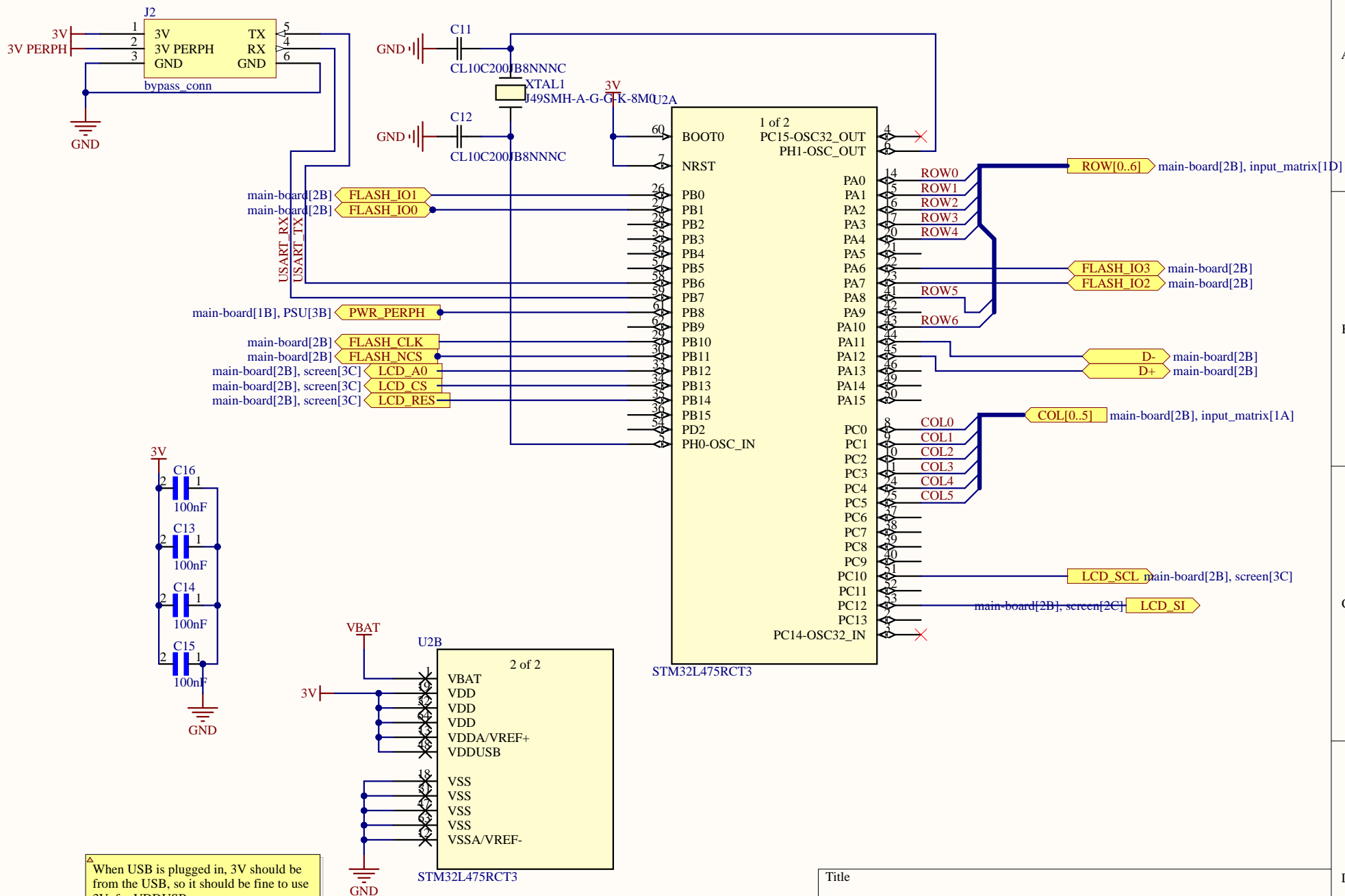
B

C

C

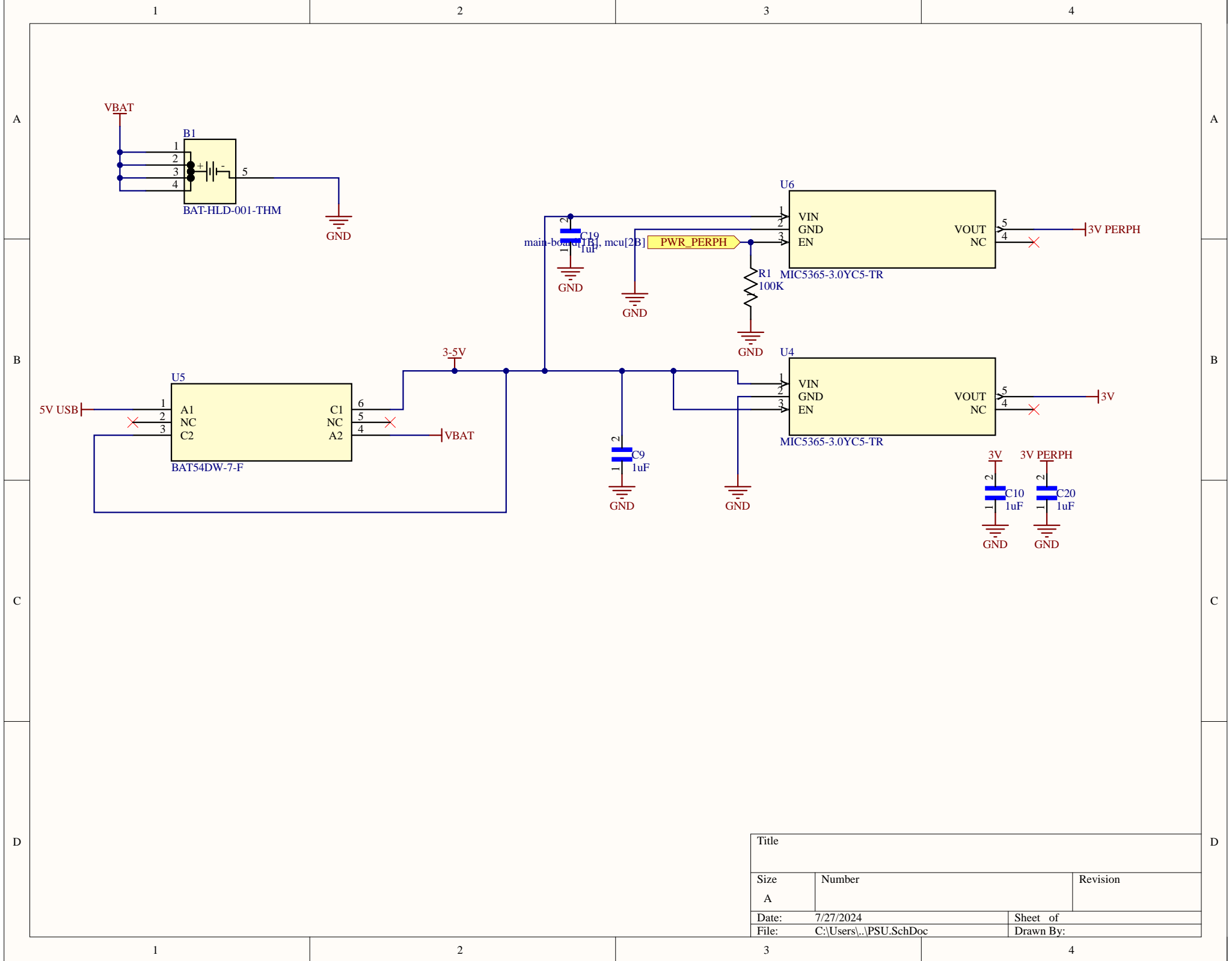
D

D

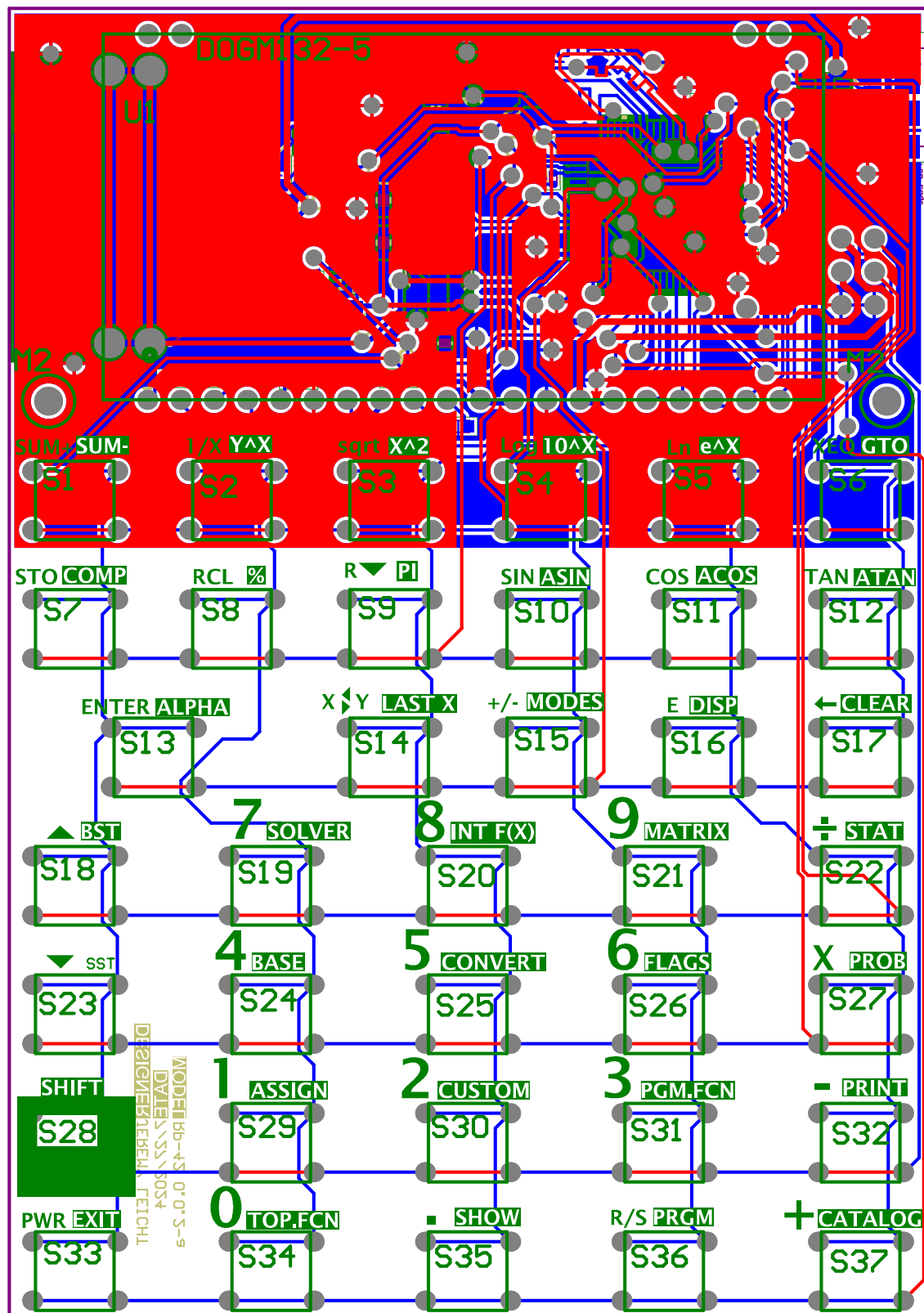


When USB is plugged in, 3V should be from the USB, so it should be fine to use 3V for VDDUSB

Title		
Size	Number	Revision
A		
Date:	7/27/2024	Sheet of
File:	C:\Users\...\mcu.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	7/27/2024	Sheet of
File:	C:\Users\...\PSU.SchDoc	Drawn By:



Line #	Name	Description	Designator	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
	BAT-HLD-001-THM	No Description Available	B1	1							
	1uF	CL10B105KP8NNNC	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C19, C20	12							
	CL10C200JB8NNNC	No Description Available	C11, C12	2							
	100nF	0.1µF ±10% 10V Ceramic Capacitor X7R 0402_1005 Metric_	C13, C14, C15, C16	4							
	USBL06-4SC6	No Description Available	CR1	1							
	10118192-0002LF	Micro USB 2.0, Input Output Connectors, B TYPE RECEPTACLE without flange	J1	1							
	bypass_conn		J2	1							
	m2		m1, m2	2							
	100K	RO0603FR-07100KL	R1	1							
	temp_switch		S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S12, S13, S14, S15, S16, S17, S18, S19, S20, S21, S22, S23, S24, S25, S26, S27, S28, S29, S30, S31, S32, S33, S34, S35, S36, S37	37							
	INTERFACE		U1	1							
	STM32L475RCT3	No Description Available	U2	1							
	W25Q64JVS1Q	No Description Available	U3	1							
	MIC5365-3.0YC5-TR	3V Linear Voltage Regulator	U4, U6	2							
	BAT54DW-7-F	No Description Available	U5	1							
	J49SMIHA-G-G-K-8M0	No Description Available	XTAL1	1							