Jérémy Barthélemy
Matt Drummond
Final Project
ECE 645

Montgomery Multiplication
Assumptions

Assumptions were made for both circuits, primarily to simplify the design so we could focus on other aspects.

- Operand size will be 1024 bits
- Word size will be 16 bits for Architecture 1 and 32 bits for Architecture 2
    - This is mainly for purposes of comparison to the original Tenca and Koç architecture.
- The number of PEs will be predefined.
    - Simplifies circuit logic.
    - Allows for closer comparison to Tenca and Koç architecture.
- X input size will be based upon the number of PEs.

Note that the assumptions about lengths are mainly for testing purposes. The code was designed to be generic so the lengths can be specified as needed.