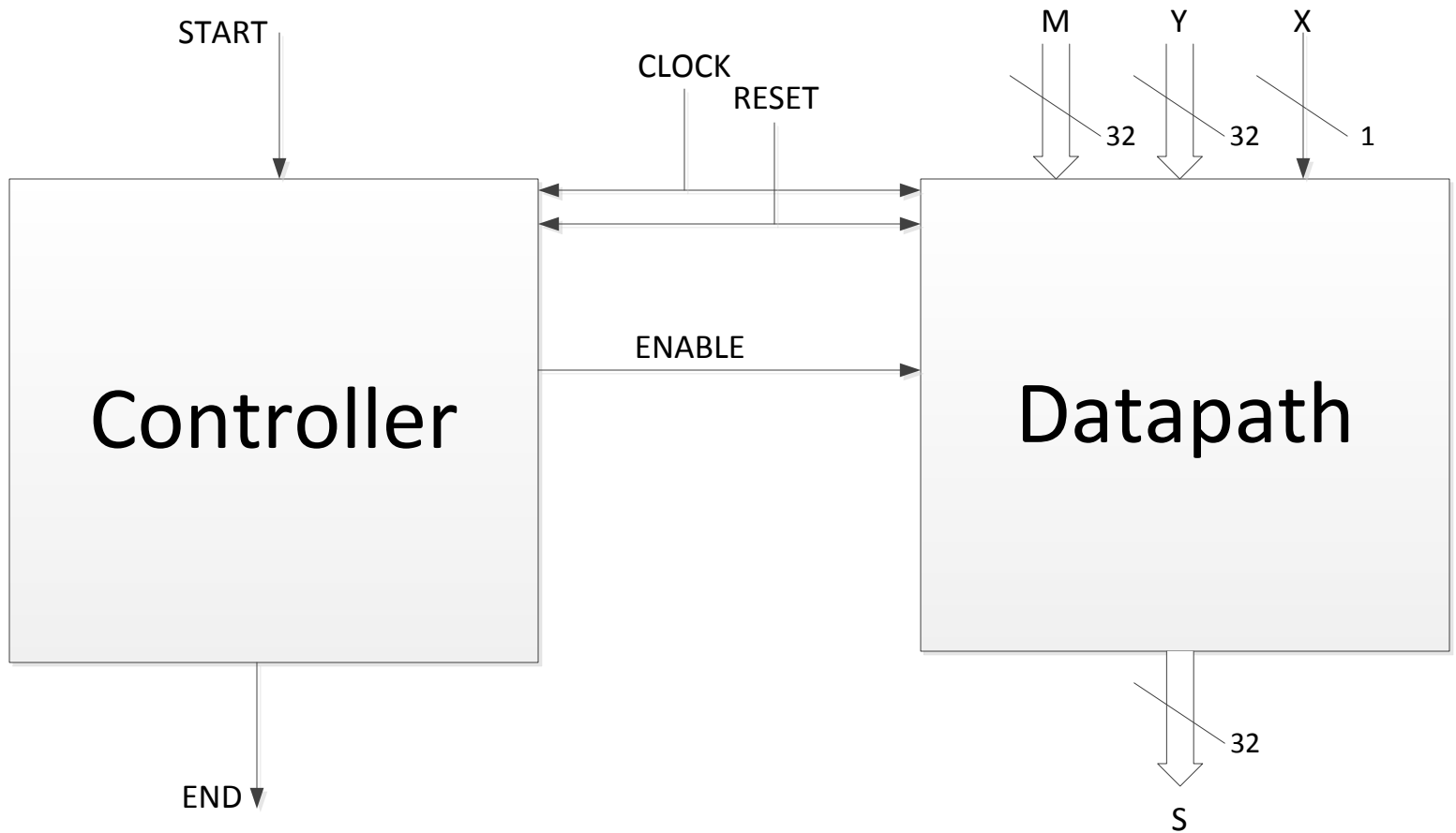


# Controller-Datapath Interface

## *Architecture 2*



START and END signals are theoretical.  
Very little control is needed for this  
circuit. In this diagram and in the  
implementation all control is handled  
through test bench.