
MODULE *GreyCodeCounter*

EXTENDS *Sequences, Integers, TLC*

Grey Code is a way of encoding binary information such that no two bits flip in one step.
This is used in Flash Memory to minimize bit flips during self-discharge

```

--algorithm GreyCode{
variables
  flashCell ∈ {⟨0, 0⟩, ⟨0, 1⟩, ⟨1, 1⟩, ⟨1, 0⟩};
  actions = [before ↦ ⟨⟩, after ↦ ⟨⟩];
{
while ( TRUE ) {
  actions.before := flashCell;
  if ( flashCell = ⟨0, 0⟩ ) {
    flashCell := ⟨0, 1⟩;
    actions.after := ⟨0, 1⟩;
  } else if ( flashCell = ⟨0, 1⟩ ) {
    flashCell := ⟨1, 1⟩;
    actions.after := ⟨1, 1⟩;
  } else if ( flashCell = ⟨1, 0⟩ ) {
    flashCell := ⟨0, 0⟩;
    actions.after := ⟨0, 0⟩;
  } else {
    flashCell := ⟨1, 0⟩;
    actions.after := ⟨1, 0⟩;
  }
}
}
}
end algorithm;

BEGIN TRANSLATION (chksum(pcal) = "f604e792" ∧ chksum(tla) = "ce051695")
VARIABLES flashCell, actions, pc

vars ≜ ⟨flashCell, actions, pc⟩

Init ≜ Global variables
  ∧ flashCell ∈ {⟨0, 0⟩, ⟨0, 1⟩, ⟨1, 1⟩, ⟨1, 0⟩}
  ∧ actions = [before ↦ ⟨⟩, after ↦ ⟨⟩]
  ∧ pc = "Lbl_1"

```

$$\begin{aligned}
Lbl_1 &\triangleq \wedge pc = \text{"Lbl_1"} \\
&\wedge actions' = [actions \text{ EXCEPT } !.before = flashCell] \\
&\wedge \text{IF } flashCell = \langle 0, 0 \rangle \\
&\quad \text{THEN } \wedge flashCell' = \langle 0, 1 \rangle \\
&\quad \wedge pc' = \text{"Lbl_2"} \\
&\quad \text{ELSE } \wedge \text{IF } flashCell = \langle 0, 1 \rangle \\
&\quad \quad \text{THEN } \wedge flashCell' = \langle 1, 1 \rangle \\
&\quad \quad \wedge pc' = \text{"Lbl_3"} \\
&\quad \quad \text{ELSE } \wedge \text{IF } flashCell = \langle 1, 0 \rangle \\
&\quad \quad \quad \text{THEN } \wedge flashCell' = \langle 0, 0 \rangle \\
&\quad \quad \quad \wedge pc' = \text{"Lbl_4"} \\
&\quad \quad \quad \text{ELSE } \wedge flashCell' = \langle 1, 0 \rangle \\
&\quad \quad \quad \wedge pc' = \text{"Lbl_5"}
\end{aligned}$$

$$\begin{aligned}
Lbl_2 &\triangleq \wedge pc = \text{"Lbl_2"} \\
&\wedge actions' = [actions \text{ EXCEPT } !.after = \langle 0, 1 \rangle] \\
&\wedge pc' = \text{"Lbl_1"} \\
&\wedge \text{UNCHANGED } flashCell
\end{aligned}$$

$$\begin{aligned}
Lbl_3 &\triangleq \wedge pc = \text{"Lbl_3"} \\
&\wedge actions' = [actions \text{ EXCEPT } !.after = \langle 1, 1 \rangle] \\
&\wedge pc' = \text{"Lbl_1"} \\
&\wedge \text{UNCHANGED } flashCell
\end{aligned}$$

$$\begin{aligned}
Lbl_4 &\triangleq \wedge pc = \text{"Lbl_4"} \\
&\wedge actions' = [actions \text{ EXCEPT } !.after = \langle 0, 0 \rangle] \\
&\wedge pc' = \text{"Lbl_1"} \\
&\wedge \text{UNCHANGED } flashCell
\end{aligned}$$

$$\begin{aligned}
Lbl_5 &\triangleq \wedge pc = \text{"Lbl_5"} \\
&\wedge actions' = [actions \text{ EXCEPT } !.after = \langle 1, 0 \rangle] \\
&\wedge pc' = \text{"Lbl_1"} \\
&\wedge \text{UNCHANGED } flashCell
\end{aligned}$$

$$Next \triangleq Lbl_1 \vee Lbl_2 \vee Lbl_3 \vee Lbl_4 \vee Lbl_5$$

$$Spec \triangleq Init \wedge \Box[Next]_{vars}$$

$$\Box[NextVariables] \text{ "OR" } _unchanged_vars$$

END TRANSLATION

$$TypeOk \triangleq flashCell[1] \in \{0, 1\} \wedge flashCell[2] \in \{0, 1\} \quad \text{They are 1 indexed!}$$

Only 1 bit can change between two states. $\langle 0, 0 \rangle \rightarrow \langle 1, 1 \rangle$ is an illegal transition. Hence, "distance" between any two states is $|\{0, 1\}|$

$$OneBitAtATime \triangleq \text{IF } actions.before \neq \langle \rangle \wedge actions.after \neq \langle \rangle$$

```

THEN
  (actions.after[1] + actions.after[2]) - (actions.before[1] + actions.before[2]) ∈ { -1, 0, 1}
ELSE TRUE

```

$OurFriendship \triangleq HackingDay \Rightarrow \Box(Friends)$

```

OneBitAtATime  $\triangleq$  (flashCell'[1] + flashCell'[2] - (flashCell[1] + flashCell[2])) ∈ { -1, 0, 1}

```

```

AlwaysOneBitAtATime  $\triangleq$   $\Box([OneBitAtATime]_{\neg flashCell})$ 

```

```

\ * Modification History
\ * Last modified Fri Mar 05 10:26:11 MST 2021 by jeremy
\ * Created Tue Mar 02 16:05:58 MST 2021 by jeremy

```