





[PIN]	PB0 [12]	PB1 [11]	PB2 [10]	PB3 [5]	PB4 [8]	PB5 [7]	PB6 [6]	PB7 [9]	Restore [3]	shift lock [21]
PA0 [13]	DEL	Return	\leftrightarrow	F7	F1	F3	F5	1		
PA1 [19]	3	W	A	4	Z	S	E	Left Shift		
PA2 [18]	5	R	D	6	C	F	T	X		
PA3 [17]	7	Y	G	8	В	Н	U	V		
PA4 [16]	9	I	J	0	M	K	O	N		
PA5 [15]	+	P	L	-		:	(a)	,		
PA6 [14]	£	*	;	CLR	Right Shift	=	1	/		
PA7 [20]	1	←	CTRL	2	Space	Ç.	Q	STOP		
GND [1,23,24]									RESTORE	Shift lock

pin [2],[25] NC pin [4] + 5 Volt Shift lock led [22] — GND [1,23,24]

C128	O	Ć	2	³ C	⁴ C	5	6 C	7	8 C	9 C) C) C) C	13
~	$\int $)	O ₁₅	O ₁₆	O ₁₇	O ₁₈	O ₁₉	O ₂₀	O ₂₁	O ₂₂	O ₂₃	O 24	O ₂₅	

[PIN]	PB0 [12]	PB1 [11]	PB2 [10]	PB3 [5]	PB4 [8]	PB5 [7]	PB6 [6]	PB7 [9]	Restore [3]
PA0 [13]	DEL	Return	\leftrightarrow	F7	F1	F3	F5	1	
PA1 [19]	3	W	A	4	Z	S	E	Left Shift	
PA2 [18]	5	R	D	6	C	F	T	X	
PA3 [17]	7	Y	G	8	В	Н	U	V	
PA4 [16]	9	I	J	0	M	K	O	N	
PA5 [15]	+	P	L	-		:	(a)	,	
PA6 [14]	£	*	;	CLR	Right Shift	=	1	/	
PA7 [20]	1	←	CTRL	2	Space	(c	Q	STOP	
K0 [21]	help	8	5	tab	2	4	7	1	
K1 [22]	esc	+	-	line feed	enter	6	9	3	
K2 [23]	alt	0		A	▼	◀		no scroll	
GND [1]									RESTORE

switch between [9] and [19] shift lock switch between [1] and [24] 40/80 col. switch between [1] and [25] caps lock

pin [2] KEY pin [4] NC

pin [21],[22] and [23] (K0,K1 and K2) are handled by VIC IIe

		$\overline{}$	$\overline{}$	$\overline{}$	$\overline{}$	$\overline{}$	$\overline{}$	\sim	
	(3) (4) () (<u> </u>	<u> </u>) (1)	1) (2)	(13) (14) (1) (6 (7)
<u> </u>									
[PIN]	PB0 [12]	PB1 [11]	PB2 [10]	PB3 [5]	PB4 [8]	PB5 [7]	PB6 [6]	PB7 [9]	Restore [3]
PA0 [13]	DEL	Return	\leftrightarrow	F7	F1	F3	F5	‡	
PA1 [19]	3	W	A	4	Z	S	E	Left Shift	
PA2 [18]	5	R	D	6	C	F	T	X	
PA3 [17]	7	Y	G	8	В	Н	U	V	
PA4 [16]	9	I	J	0	M	K	O	N	
PA5 [15]	+	P	L	-		:	(a)	,	
PA6 [14]	£	*	;	CLR	Right Shift	=	1	/	
PA7 [20]	1	←	CTRL	2	Space	Ç.	Q	STOP	
GND [1]									RESTORE

pin [2] KEY (NC) pin [4] + 5 Volt

C16

[PIN]	K0 [18]	K1 [15]	K2 [14]	K7 [3]	K4 [7]	K5 [9]	K6 [10]	K3 [17]
P7 [6]	1	CLR		Run Stop			Q	2
P0 [8]	3	W	A	Shift	Z	S	E	4
P2 [12]	5	R	D	X	C	F	T	6
P1 [11]	7	Y	G	V	В	Н	U	8
P3 [13]	9	I	J	N	M	K	О	0
P6 [1]	▼	P	L			:	-	A
P4 [16]	- ■	*	;	/	ESC	=	+	>
P5 [19]	De1	Return	£	(a).	F1	F2	F3	Help

pin [2] KEY (NC) pin [5],[20] NC pin [4] + 5 Volt

	P	1	2	3	4	5	6	7		8		9		10		11		12		13	14	4	15		16		17		18
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<u>2</u>									
\bigcirc	[PIN]	K0 [17]	K1 [14]	K2 [13]	K7 [2]	K4 [6]	K5 [8]	K6 [9]	K3 [16]
	P7 [5]	1	CLR	CTRL	Run Stop	Space	(¢	Q	2
9	P0 [7]	3	W	A	Shift	Z	S	E	4
	P2 [11]	5	R	D	X	C	F	T	6
	P1 [10]	7	Y	G	V	В	Н	U	8
	P3 [12]	9	I	J	N	M	K	O	0
	P6 [1]	▼	P	L			:	-	\blacktriangle
	P4 [15]	◀	*	;	/	ESC	=	+	
	P5 [18]	Del	Return	£	@	F1	F2	F3	Help
	pin [4] + 5 Vo	lt			[4]—		-	[3]	