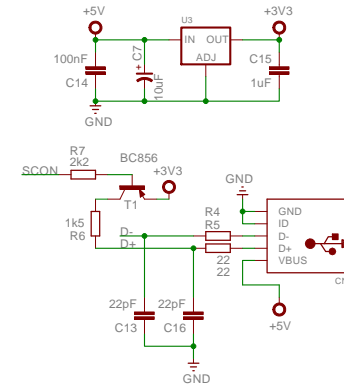
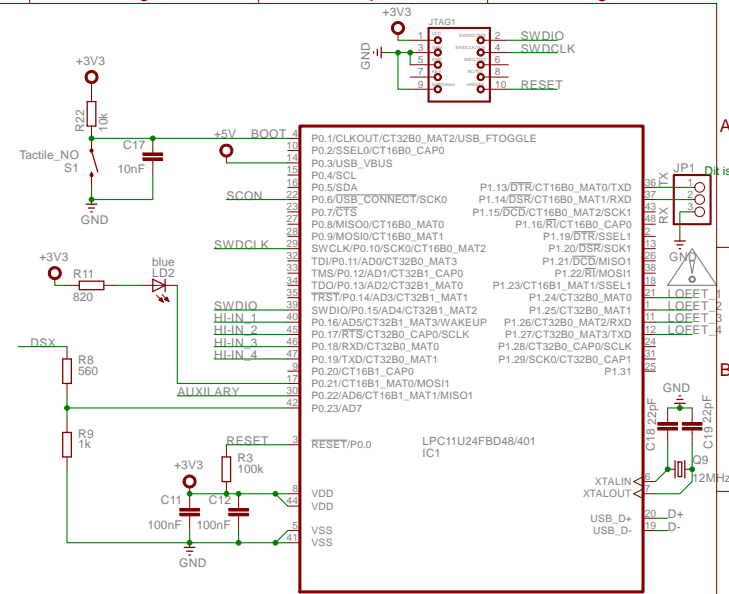
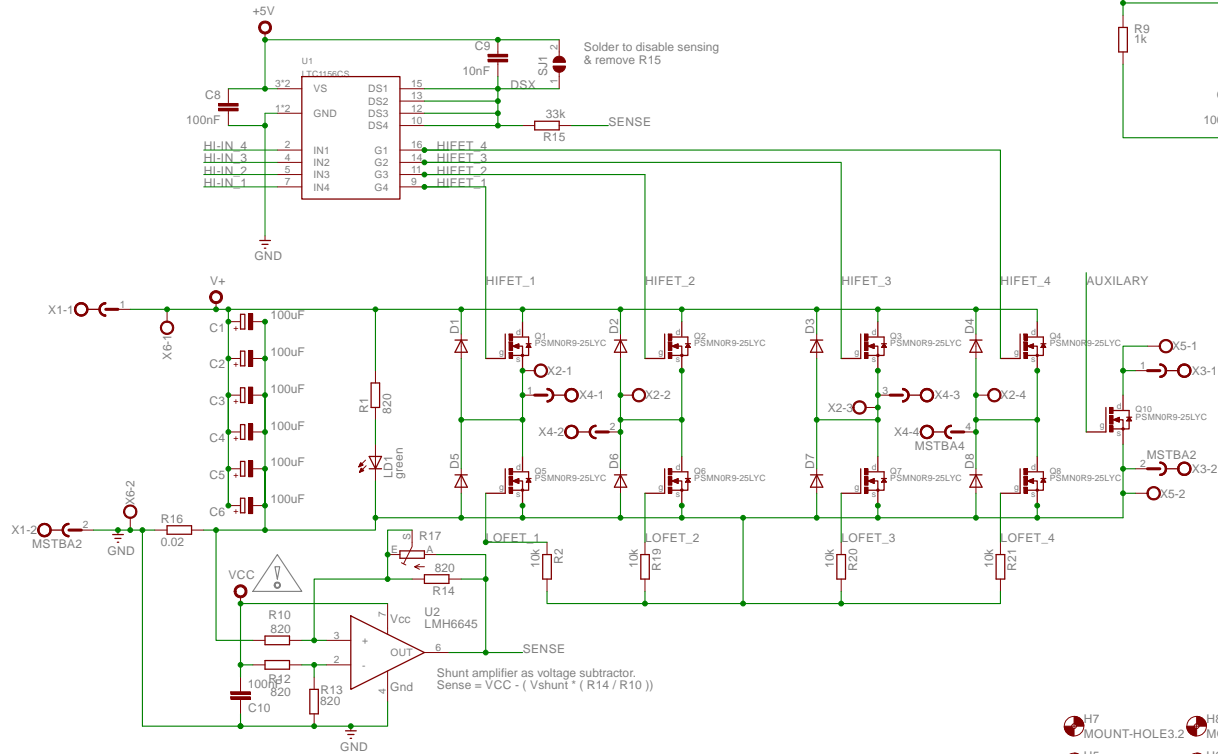




Bug: LOFET signals to CT32B0 timer, only 3 concurrent PWM outputs possible

Bug: Opamp Vcc should be 5v, manual re-trace required

Improvement: Pull down termination on HI_FET gate signals



H7 MOUNT-HOLE3.2
H8 MOUNT-HOLE3.2
H5 MOUNT-HOLE3.2
H6 MOUNT-HOLE3.2

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