

LAB #02 System Calls and Integer Arithmetic

System Calls

Programs do input and output using system calls. On a real system, the operating system provides system call services to application programs. The MIPS architecture provides a special **syscall** instruction that generates a system call exception, which is handled by the operating system.

System calls are operating-system specific. Each operating system provides its own set of system calls. Because MARS is a simulator, there is no operating system involved. The MARS simulator handles the **syscall** exception and provides system services to programs. The following table shows a small set of services provided by MARS for doing basic I/O.

Before using the **syscall** instruction, you should load the service number into register \$v0, and load the arguments, if any, into registers \$a0, \$a1, etc. After issuing the **syscall** instruction, you should retrieve return values, if any, from register \$v0.

Service	Code in \$v0	Arguments	Result
Print Integer	1	\$a0 = integer to print	
Print String	4	\$a0 = address of null-terminated string	
Read Integer	5		\$v0 = integer read
Read String	8	\$a0 = address of input buffer \$a1 = maximum characters to read	
Exit program	10		Terminates program
Print char	11	\$a0 = character to print	
Read char	12		\$v0 = character read

Fig. 1 syscall summary.

NOTE: All instructions, pseudo-instructions, directives, etc. can be found in MARS help.

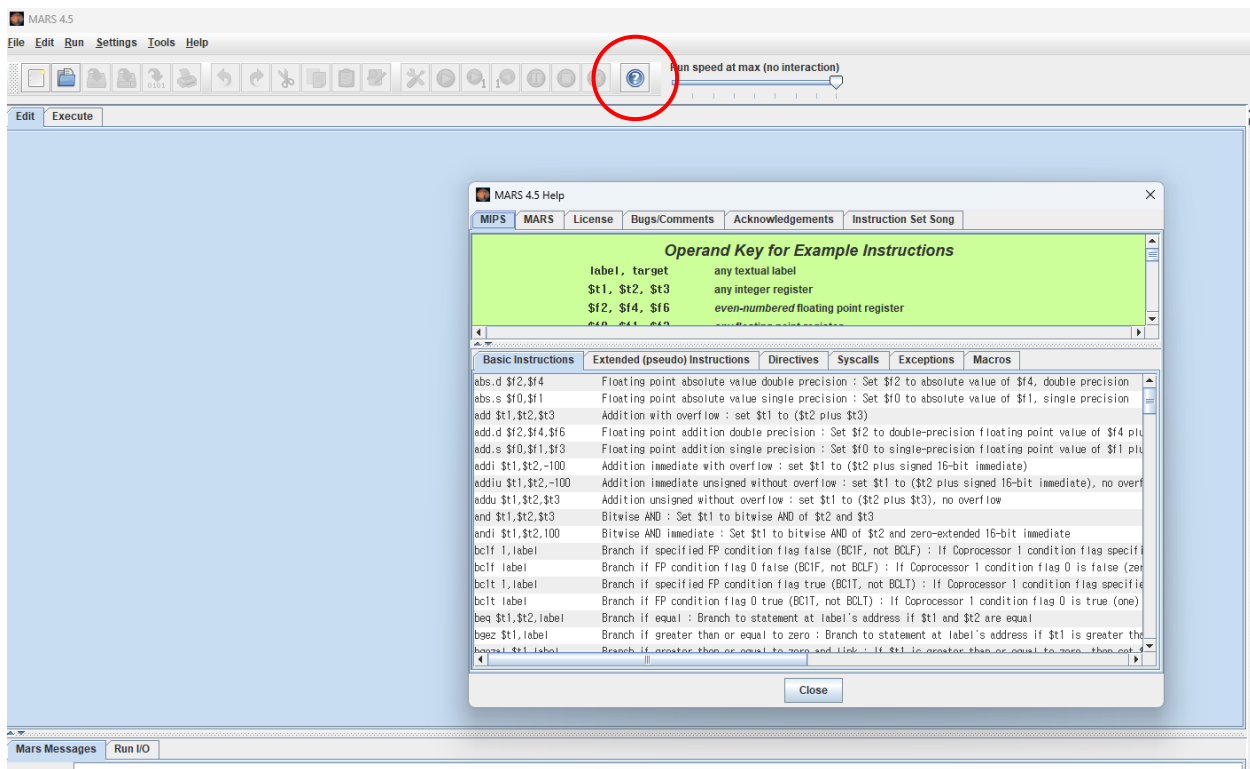


Fig. 2 MARS help

Add/Subtract Instructions

The difference between **add/sub** and **addu/subu** instructions is that in case of overflow occurrence, the **add/sub** instructions will cause an arithmetic exception and the result will not be written to the destination register. However, for the instructions **addu/subu**, overflow occurrence is ignored.

Operation	Meaning
add \$s1, \$s2, \$s3	$\$s1 = \$s2 + \$s3$
addu \$s1, \$s2, \$s3	$\$s1 = \$s2 + \$s3$
sub \$s1, \$s2, \$s3	$\$s1 = \$s2 - \$s3$
subu \$s1, \$s2, \$s3	$\$s1 = \$s2 - \$s3$
addi \$s1, \$s2, 10	$\$s1 = \$s2 + 10$
addiu \$s1, \$s2, 10	$\$s1 = \$s2 + 10$

Fig. 3 Arithmetic instructions

Task to do

1. Modify the following program in Fig.4.

Condition:

- Ask the user "enter an integer value: "
- Print the result of twice of that number (e.g., input x becomes 2x)
- Use the add instruction.

```
.data
str1:    .asciiz  "Enter an integer value: "
str2:    .asciiz  "You entered "

.globl  main
.text
main:
    li    $v0, 4      # service code for print string
    la    $a0, str1   # load address of str1 into $a0
    syscall           # print str1 string
    li    $v0, 5      # service code for read integer
    syscall           # read integer input into $v0
    move   $s0, $v0    # save input value in $s0
    li    $v0, 4      # service code for print string
    la    $a0, str2   # load address of str2 into $a0
    syscall           # print str2 string
    li    $v0, 1      # service code to print integer
    move   $a0, $s0    # copy input value
    syscall           # print integer
    li    $v0, 10     # service code to exit program
    syscall           # exit program
```

Fig. 4 An example of a program using syscall.

2. Modify your program of question 1 as follows:

- At the end, ask the user to repeat the program as "\nRepeat [y/n]? ".
- Use service code 12 to read a character and repeat the main function if the answer is 'y'.

3. Write a MIPS program that executes the statement:

$s = (a + b) - (c + 101)$, where a, b, and c are user provided integer inputs, and s is computed and printed as an output. Answer the following:

- Suppose the user enters a = 5, b = 10, and c = -30, what is the expected value of s?
- Which instruction in your program computed the value of s and which register is used?

c) What is the address of this instruction in memory?

d) Put a breakpoint at this instruction and write the value of the register used for computing s in decimal and hexadecimal.

4. Write a MIPS program as follows:

Condition:

- I/O input: two integer values
- I/O output: **equal** if those two integers are equal. **not equal** otherwise
- Use the branch instruction to check for equality.