Module 1 Quiz

Quiz, 10 questions

1 point

1.

Consider the following parallel pseudo-code that uses the async-finish notation for task creation and termination introduced in Lecture 1.1:

```
1  finish {
2    async S1;
3    finish {
4      async S2;
5      S3;
6    }
7    S4;
8  }
9  S5;
```

Which of the following statements are true? *Check all that apply*

- [ ] A. S2 can potentially execute in parallel with S3
- [ ] B. S2 can potentially execute in parallel with S4
- [ ] C. S1 can potentially execute in parallel with S4
- [ ] D. S1 can potentially execute in parallel with S5

---

1 point

2.

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Consider the following parallel pseudo-code that uses the async-finish notation for task creation and termination introduced in Lecture 1.1::

```
1  S1;
2  finish {async S2;}
3  S3;
```

The line "finish {async S2;}" could be equivalently replaced by which of the following? You may assume that S1, S2, and S3 do not spawn any nested async tasks.

Check all that apply

- [ ] A. async S2;
- [ ] B. async finish S2;
- [ ] C. S2;
- [ ] D. finish S2;

---

1 point

3.

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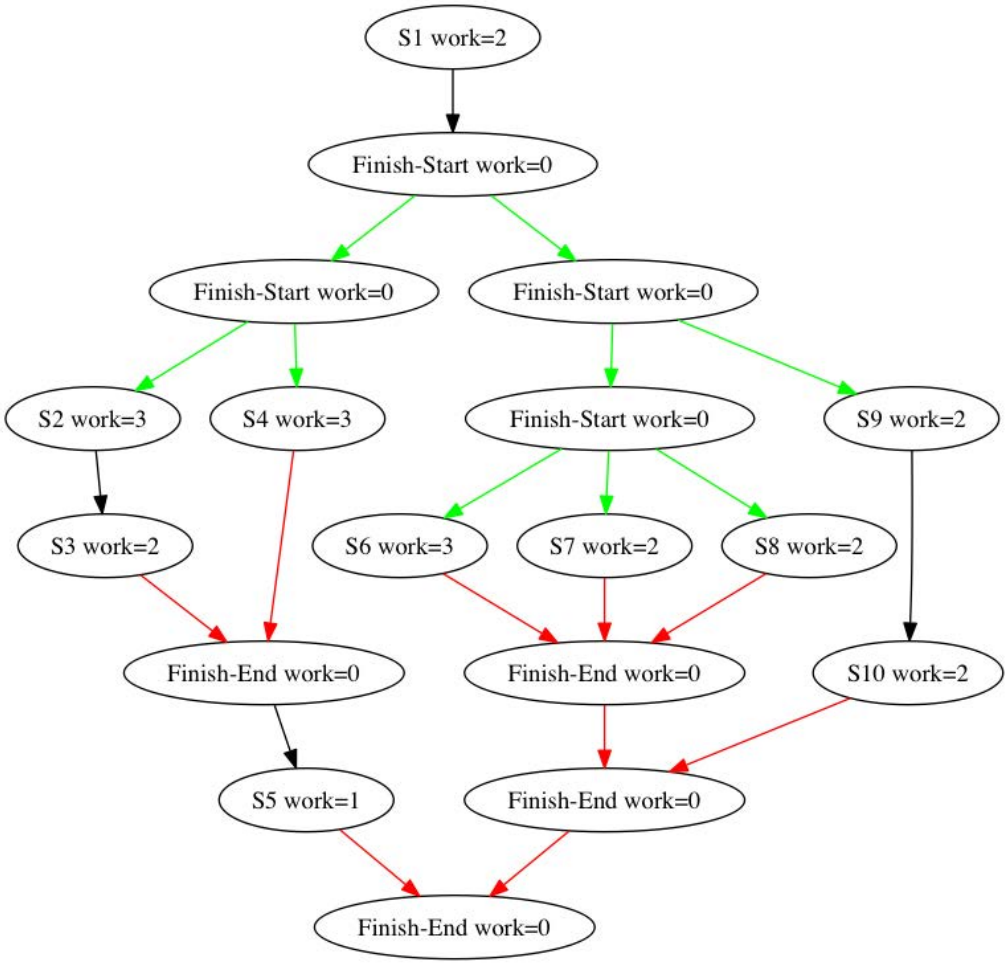
Consider the computation graph in Figure 1 below (where black, green and red arrows represent continue, fork, and join edges respectively, as explained in Lecture 1.3).

Figure 1.



What is the total WORK for the computation graph in Figure 1? Please enter an integer.

Enter answer here

1 point

4.

What is the SPAN or CPL (critical path length) for the computation graph in Figure 1? Please enter an integer.

Enter answer here

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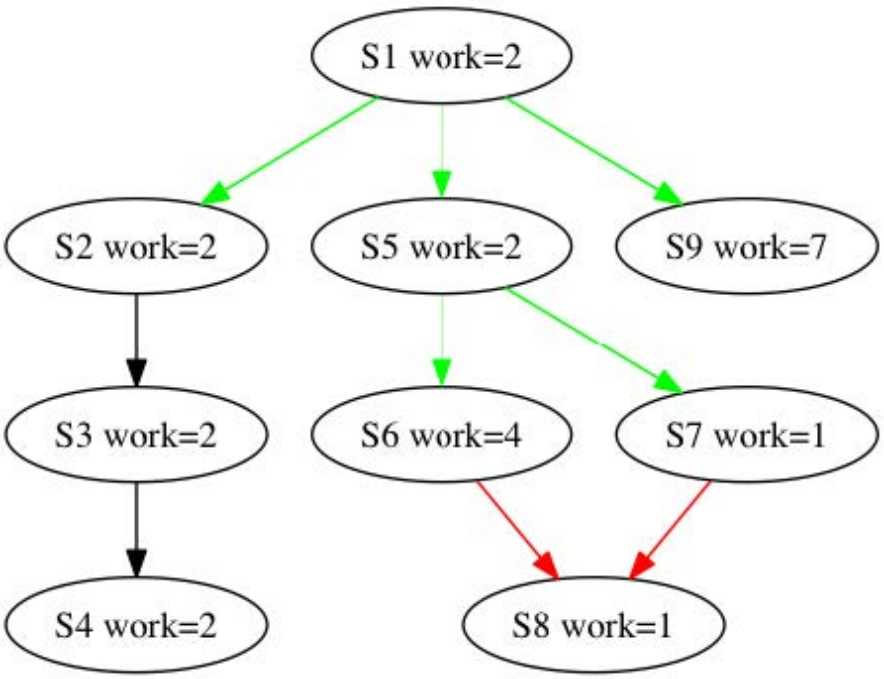
5.

Consider the computation graph in Figure 2 below (where black, green and red arrows represent continue, fork, and join edges respectively, as explained in Lecture 1.3).

Figure 2.



For the computation graph in Figure 2, identify which of the following paths are critical paths?

Check all that apply

- [ ] A. S1 → S2 → S3 → S4
- [ ] B. S1 → S5 → S8
- [ ] C. S1 → S5 → S6 → S8

D. S1 → S5 → S7 → S8

E. S1 → S9

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6.

Recall that the concepts of *WORK* and *SPAN* were introduced in Lecture 1.3. Consider the pseudo-code in Figure 3 below for adding two lower triangular matrices (square matrices in which all the elements above and including the (0,0) to (n,n) diagonal are zero), in which each execution of the statement A[i][j] = B[i][j] + C[i][j]; represents one unit of work in the computation graph, WORK(S5) = 1:

Figure 3.

```
1  finish {
2    for (int i = 0; i < n; i++) {
3      async {
4        for (int j = 0; j < i; j++) {
5          A[i][j] = B[i][j] + C[i][j];
6        } // for-j
7      } // async
8    } // for-i
9  }
```

The total WORK performed by the program in Figure 3 (after it completes execution), in terms of n equals _____.

A. 1

B. $n - 1$

C. $\frac{n(n-1)}{2}$

D. None of the above

1 point

7.

The Critical Path Length (CPL) of the program in Figure 3 in terms of n equals _____.

A. 1

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B. $n - 1$

C. $\frac{n(n-1)}{2}$

D. None of the above

---

1 point

8.

Recall that Ideal Parallelism was also defined in Lecture 1.3. The Ideal Parallelism of the program in Figure 3, as a function of n equals _____.

Figure 3

```
1  finish {
2    for (int i = 0; i < n; i++) {
3      async {
4        for (int j = 0; j < i; j++) {
5          A[i][j] = B[i][j] + C[i][j];
6        } // for-j
7      } // async
8    } // for-i
9  } // finish
```

A. 1

B. $\frac{n}{2}$

C. $n - 1$

D. None of the above

---

1 point

9.

Recall that multiprocessor schedules were introduced in Lecture 1.4. Though, by default, we focus on schedules with no unforced idleness in this course, this question will allow for all possible legal schedules, including those that may have unforced idleness, i.e., schedules in which a process may be idle even if there is work that is available to be executed.

Consider the computation graph shown below in Figure 4. Select the

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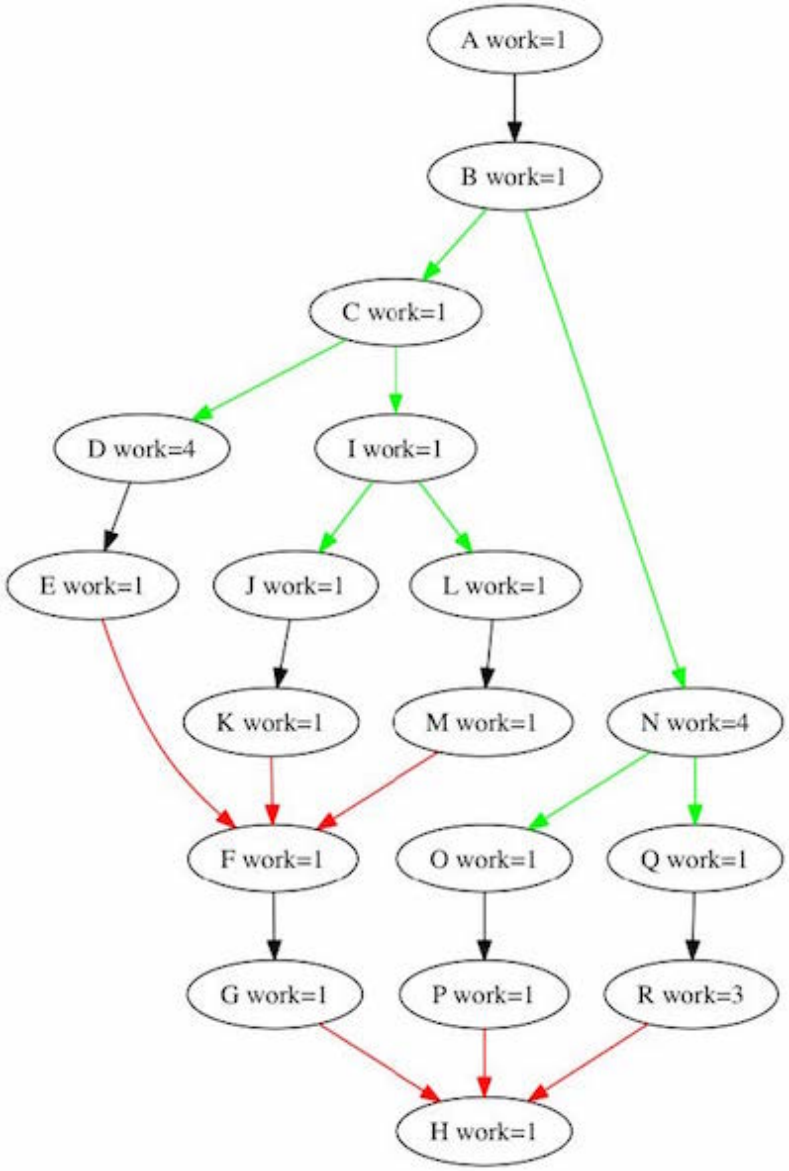
Consider the computation graph shown below in Figure 4. Select the statement below that is true about legal schedules for this graph on 3 processors.

Figure 4.



- A. There exists a legal schedule in which node C can start execution before node B starts.

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B. There exists a legal schedule in which node P can complete execution before node C starts.

C. There exists a legal schedule in which nodes J, L, O, Q can all be scheduled at the same time.

1 point

10.

For an arbitrary computation graph and its schedule on P processors, which of the following relationships must always hold between Speedup(P) and Ideal Parallelism?

A. Speedup(P) < Ideal Parallelism

B. Speedup(P) ≤ Ideal Parallelism

C. Speedup(P) = Ideal Parallelism

D. Speedup(P) ≥ Ideal Parallelism

E. Speedup(P) > Ideal Parallelism

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