

# SERCOM 引脚配置基本思路（I2C请确认可使用的PIN口）

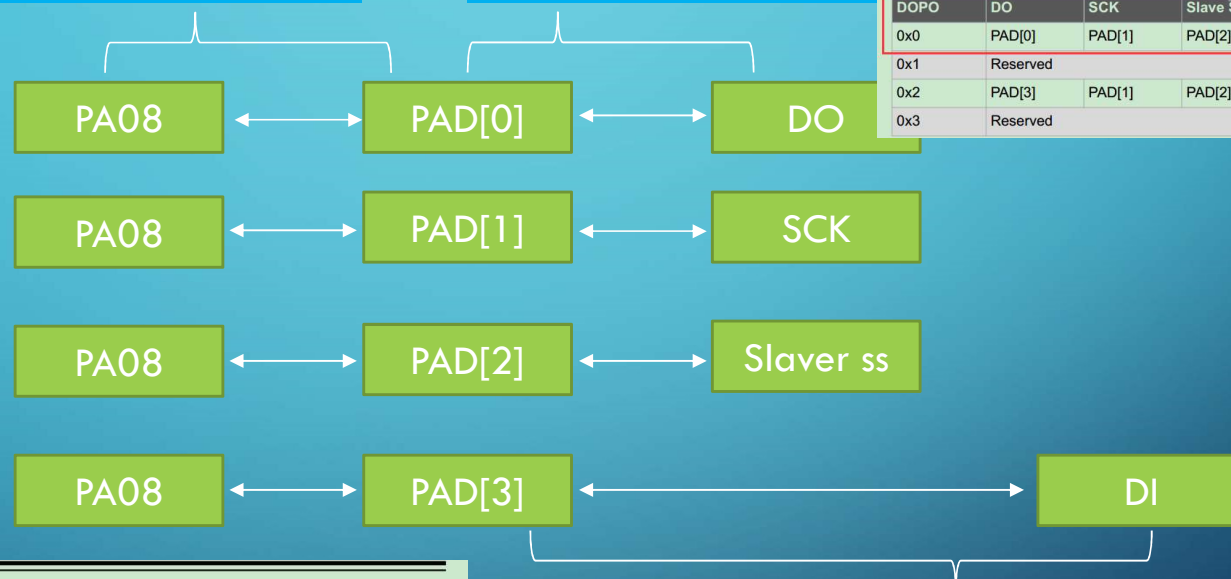
第一步：

IOSET Configuration

1. 确定SERCOMx的引脚簇(哪些引脚可用)。
2. 同时确定引脚与PAD[x]的对应关系

第二步：

Control A寄存器DOPO[1:0]位用于决定DO,SCK,Slaver ss引脚与PAD[x]对应关系



DOPO	DO	SCK	Slave SS	Master SS
0x0	PAD[0]	PAD[1]	PAD[2]	System configuration
0x1	Reserved			
0x2	PAD[3]	PAD[1]	PAD[2]	System configuration
0x3	Reserved			

Table 6-9. SERCOM0 IO SET Configuration

SERCOM Signal	IOSET 1 PINs	IOSET 2 PINs	IOSET 3 PINs	IOSET 4 PINs
PAD0	PA08	PB24	PA04	PC17
PAD1	PA09	PB25	PA05	PC16
PAD2	PA10	PC24	PA06	PC18
PAD3	PA11	PC25	PA07	PC19

第二步：

Control A寄存器DOPI[1:0]位用于决定DI引脚与PAD[x]对应关系

DIPO[1:0]	Name	Description
0x0	PAD[0]	SERCOM PAD[0] is used as data input
0x1	PAD[1]	SERCOM PAD[1] is used as data input
0x2	PAD[2]	SERCOM PAD[2] is used as data input
0x3	PAD[3]	SERCOM PAD[3] is used as data input

# I2C可使用PIN口确认

- 要具体确认有哪些pin口可以作为I2C用

**6.2.6 SERCOM I<sup>2</sup>C Configurations**

The SAM D5x/E5x has up to eight instances of the serial communication interface (SERCOM) peripheral. All instances support USART, including RS485 and ISO7816, SPI and I<sup>2</sup>C protocols. The following table lists the I<sup>2</sup>C pins location.

**Table 6-7 SERCOM I<sup>2</sup>C Pinout**

Package Pin Count	Supply	I/O pins with I <sup>2</sup> C Support
128	VDDIOB	PA08, PA09
	VDDIO	PA12, PA13, PA16, PA17, PA22, PA23, PD08, PD09
100	VDDIOB	PA08, PA09
	VDDIO	PA12, PA13, PA16, PA17, PA22, PA23